DATA SHEET

NEC

MOS FIELD EFFECT TRANSISTOR

2SK4070

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK4070 is N-channel MOS FET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

FEATURES

Low on-state resistance

 $R_{DS(on)} = 11 \Omega MAX. (V_{GS} = 10 V, I_D = 0.5 A)$

Low gate charge

 Q_G = 5 nC TYP. (VDD = 450 V, VGS = 10 V, ID = 1.0 A)

 \bullet Gate voltage rating : $\pm 30 \text{ V}$

Avalanche capability ratings

<R> ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE		
2SK4070-S15-AY Note		Tube 70 p/tube	TO-251 (MP-3-a) typ. 0.39 g		
2SK4070(1)-S27-AY Note	Pure Sn (Tin)	Tube 75 p/tube	TO-251 (MP-3-b) typ. 0.34 g		
2SK4070-ZK-E1-AY Note		Tape 2500 p/reel	TO-252 (MP-3ZK) typ. 0.27 g		
2SK4070-ZK-E2-AY Note					

Note Pb-free (This product does not contain Pb in external electrode.)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Vdss	600	V
Vgss	±30	V
ID(DC)	±1.0	А
D(pulse)	±4.0	А
PT1	22	W
Pt2	1.0	W
Tch	150	°C
Tstg	-55 to +150	°C
AS	0.8	А
Eas	38.4	mJ
	VGSS ID(DC) ID(pulse) PT1 PT2 Tch Tstg IAS	VGSS ±30 ID(DC) ±1.0 ID(pulse) ±4.0 PT1 22 PT2 1.0 Tch 150 Tstg -55 to +150 IAS 0.8





(TO-252)



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Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on glass epoxy board of 40 mm × 40 mm × 1.6 mm

3. Starting T_{ch} = 25°C, V_{DD} = 150 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 600 V, V _{GS} = 0 V			100	μA
Gate Leakage Current	Igss	V _{GS} = ±30 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.5	2.9	3.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 0.5 A	0.2	0.4		S
Drain to Source On-state Resistance ^{Note}	RDS(on)	V _{GS} = 10 V, I _D = 0.5 A		9.2	11	Ω
Input Capacitance	Ciss	V _{DS} = 10 V,		110		pF
Output Capacitance	Coss	V _{GS} = 0 V,		50		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		11		pF
Turn-on Delay Time	td(on)	V _{DD} = 150 V, I _D = 0.5 A,		7.5		ns
Rise Time	tr	V _{GS} = 10 V,		6		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		11		ns
Fall Time	tr			18		ns
Total Gate Charge	QG	V _{DD} = 450 V,		5		nC
Gate to Source Charge	QGS	V _{GS} = 10 V,		1		nC
Gate to Drain Charge	Qgd	I _D = 1.0 A		2.8		nC
Body Diode Forward Voltage Note	VF(S-D)	I⊧ = 1.0 A, V _{GS} = 0 V		0.86	1.5	V
Reverse Recovery Time	trr	I⊧ = 1.0 A, V₀s = 0 V,		135		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		285		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME

9

D.U.T.

RG

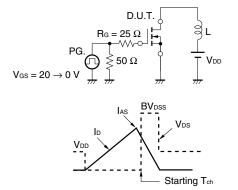
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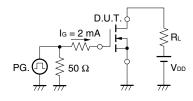
 $\tau = 1 \,\mu s$ Duty Cycle $\leq 1\%$

Vgs

0

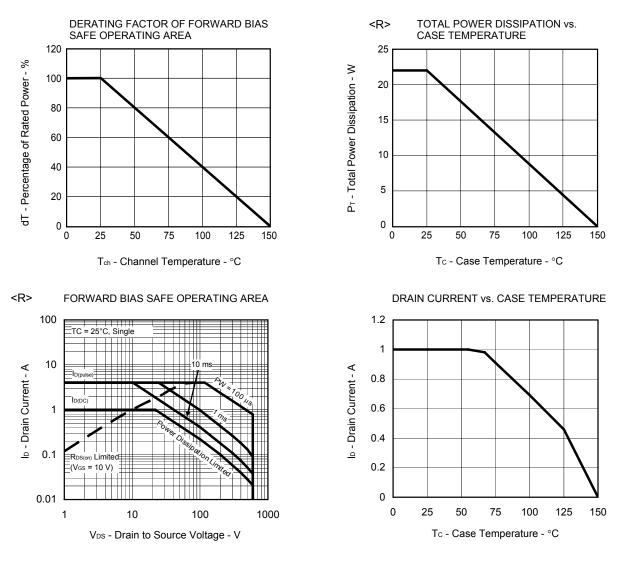


TEST CIRCUIT 3 GATE CHARGE

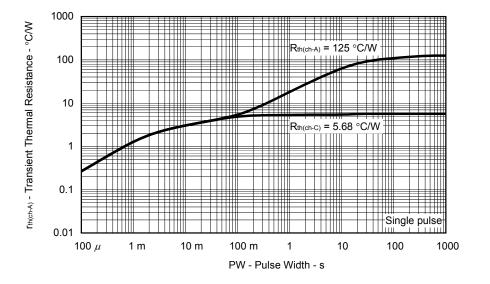


≷R∟ Vgs 90% VGS Wave Form 0 10% Vgs V_{DD} Vds 90% 190% VDS 10% 10% VDS Wave Form 0 td(off tr tſ td to

TYPICAL CHARACTERISTICS (TA = 25°C)

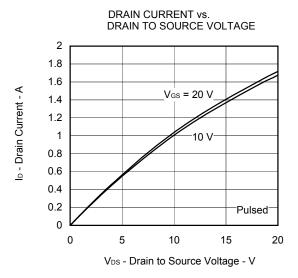


<R> TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

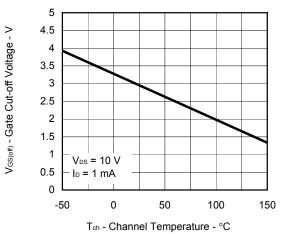


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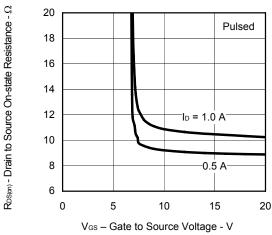




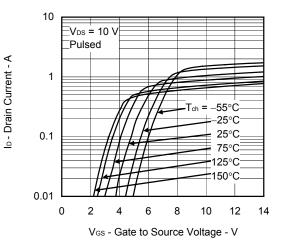




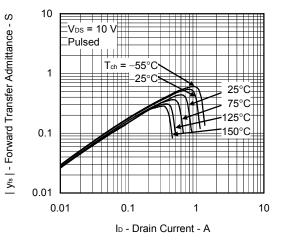




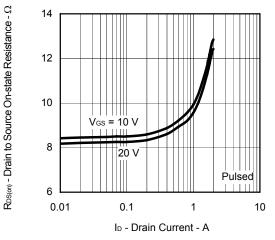


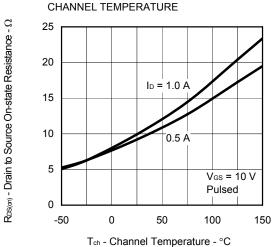


FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



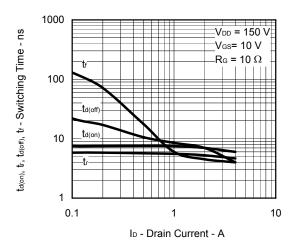
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

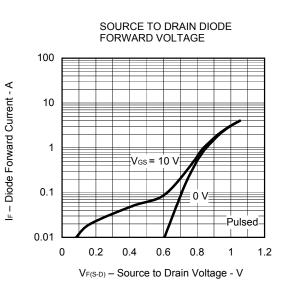


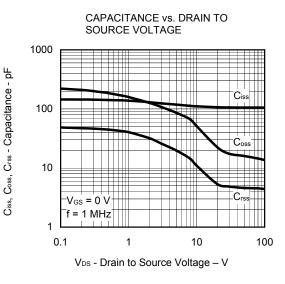




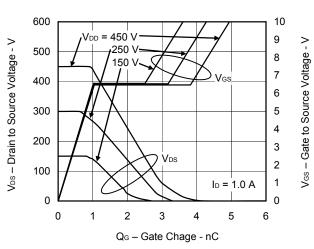




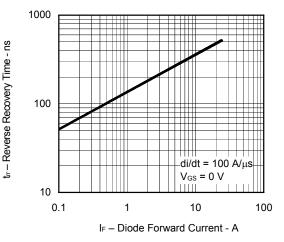




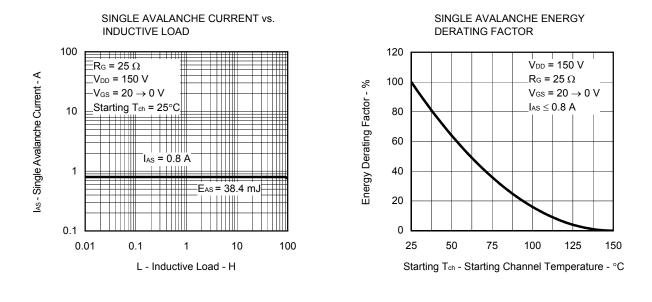
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



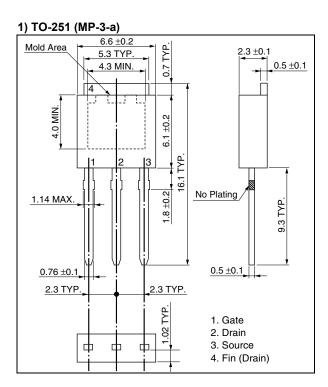


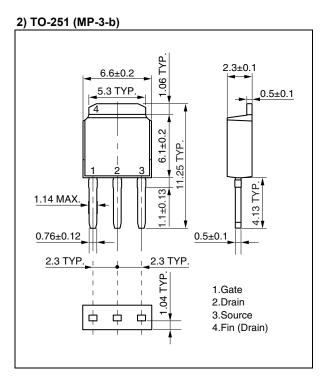


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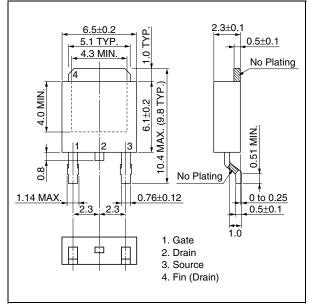


<R> PACKAGE DRAWINGS (Unit: mm)

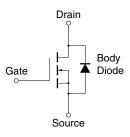




3) TO-252 (MP-3ZK)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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