SANYO Semiconductors

DATA SHEET

N-Channel Silicon MOSFET

2SK4085LS — General-Purpose Switching Device Applications

Features

- Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- High reliability (Adoption of HVP process).
- Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		500	V
Gate-to-Source Voltage	V_{GSS}		±30	V
Drain Current (DC)	I_{Dc}*1	Limited only by maximum temperature	16	A
	I_{Dpack}*2	SANYO's ideal heat dissipation condition	11	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	60	A
Allowable Power Dissipation	P_D		2.0	W
		Tc=25°C (SANYO's ideal heat dissipation condition)	40	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		–55 to +150	°C
Avalanche Energy (Single Pulse) *3	E_{AS}		159	mJ
Avalanche Current *4	I_{AV}		16	A

*1 Shows chip capability

*2 Package limited

*3 V_{DD}=99V, L=1mH, I_{AV}=16A

*4 L≤1mH, single pulse

Marking : K4085

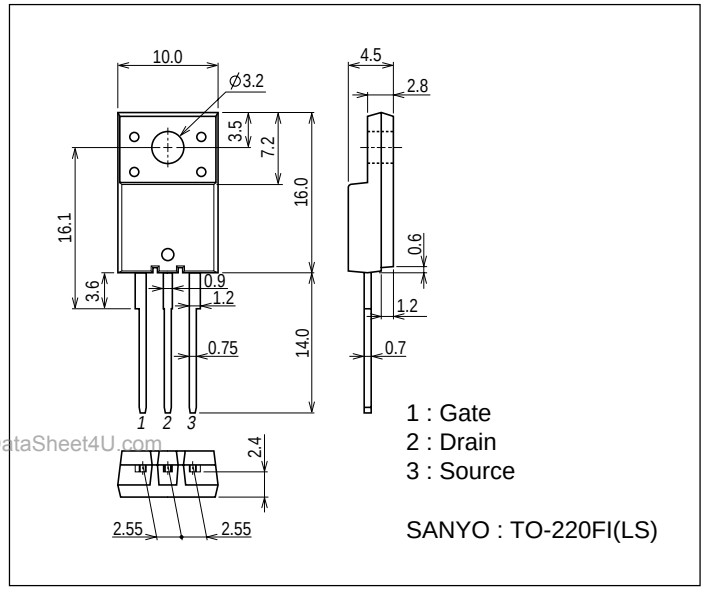
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=10mA, V_{GS}=0V	500			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=400V, V_{GS}=0V			100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V			±100	nA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	3		5	V
Forward Transfer Admittance	|yfs|	V_{DS}=10V, I_D=8A	4.5	9		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)	I_D=8A, V_{GS}=10V		0.33	0.43	Ω
Input Capacitance	Ciss	V_{DS}=30V, f=1MHz		1200		pF
Output Capacitance	Coss	V_{DS}=30V, f=1MHz		250		pF
Reverse Transfer Capacitance	Crss	V_{DS}=30V, f=1MHz		55		pF
Turn-ON Delay Time	t_d(on)	See specified Test Circuit.		26.5		ns
Rise Time	t_r	See specified Test Circuit.		78		ns
Turn-OFF Delay Time	t_d(off)	See specified Test Circuit.		146		ns
Fall Time	t_f	See specified Test Circuit.		57		ns
Total Gate Charge	Qg	V_{DS}=200V, V_{GS}=10V, I_D=16A		46.6		nC
Gate-to-Source Charge	Qgs	V_{DS}=200V, V_{GS}=10V, I_D=16A		8.2		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=200V, V_{GS}=10V, I_D=16A		27.4		nC
Diode Forward Voltage	V_{SD}	I_S=16A, V_{GS}=0V		0.95	1.3	V

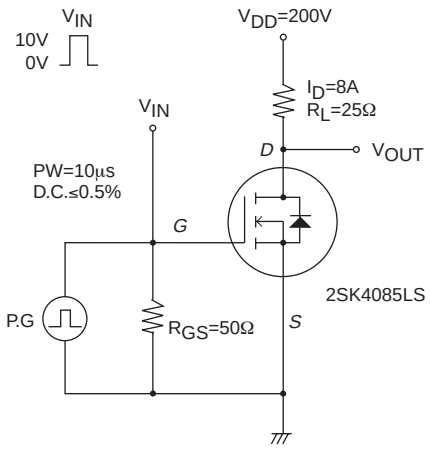
Package Dimensions

unit : mm (typ)

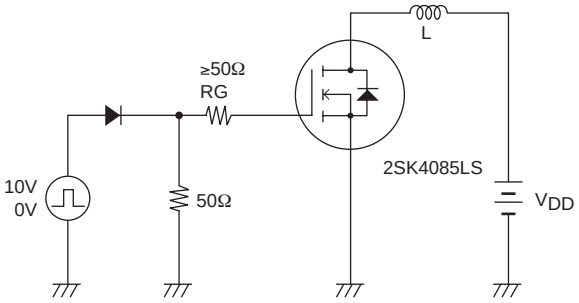
7509-002

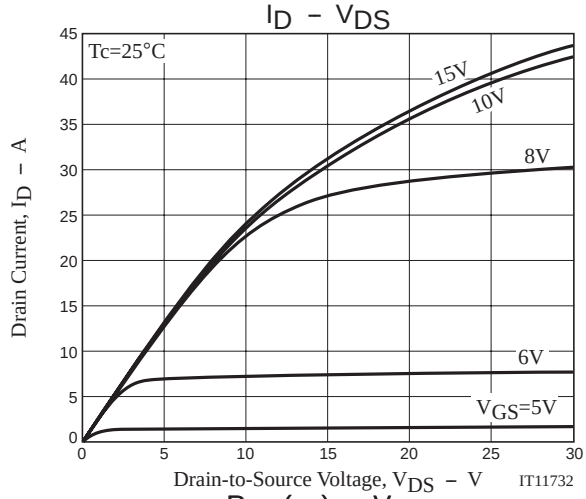


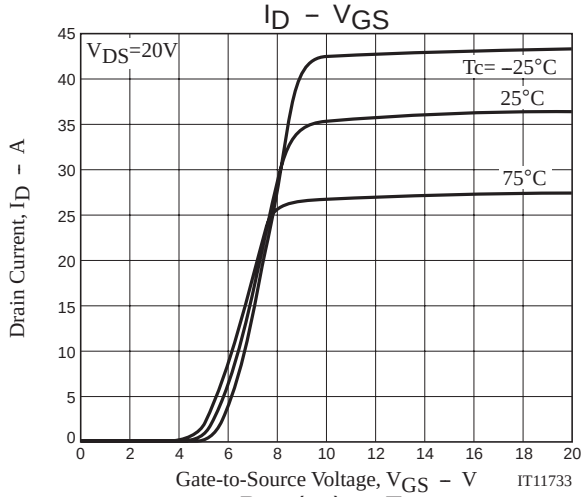
Switching Time Test Circuit

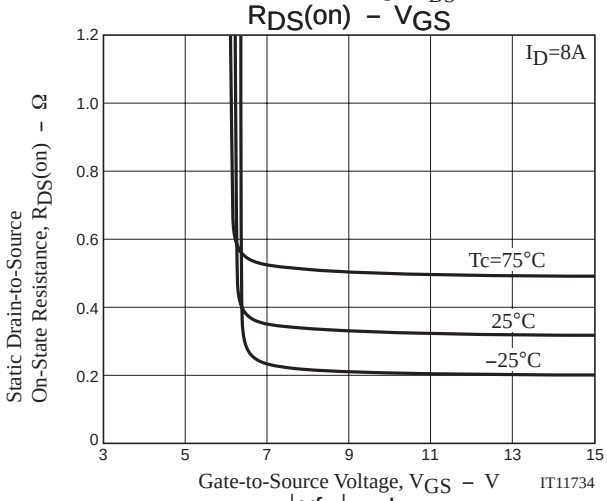


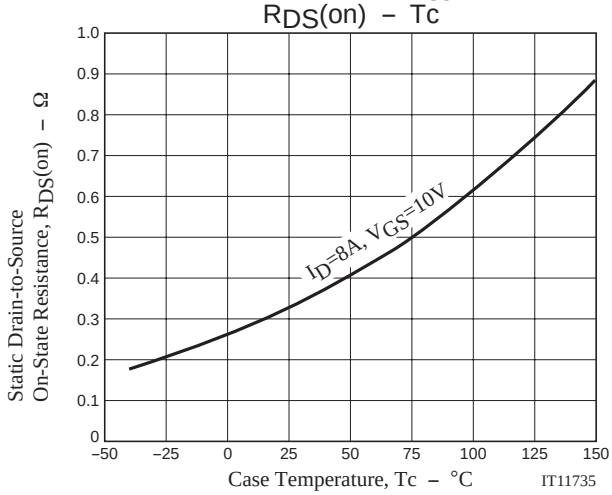
Avalanche Resistance Test Circuit

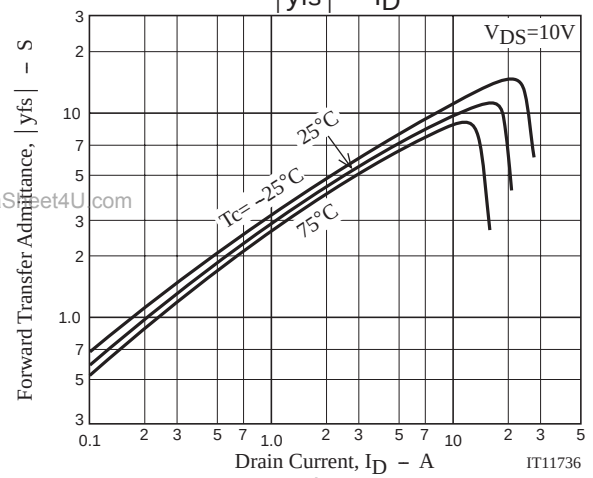


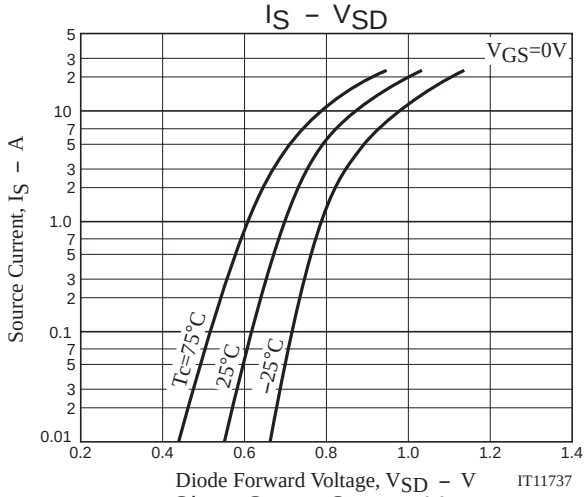


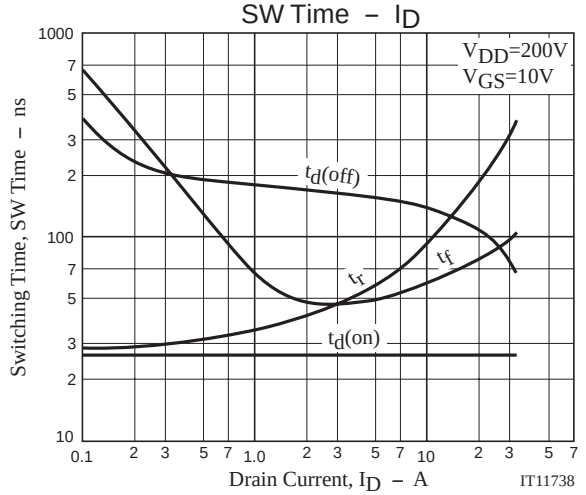


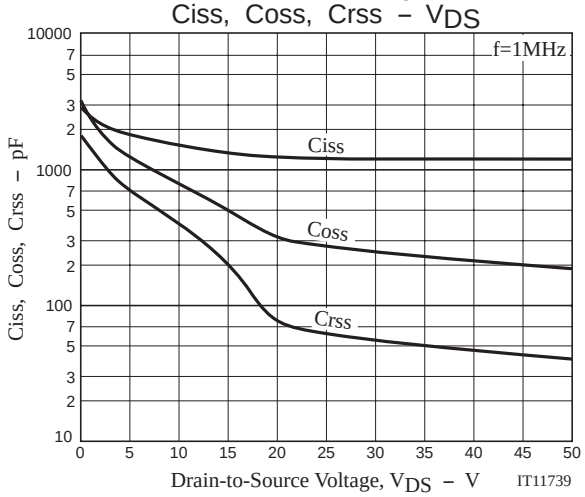




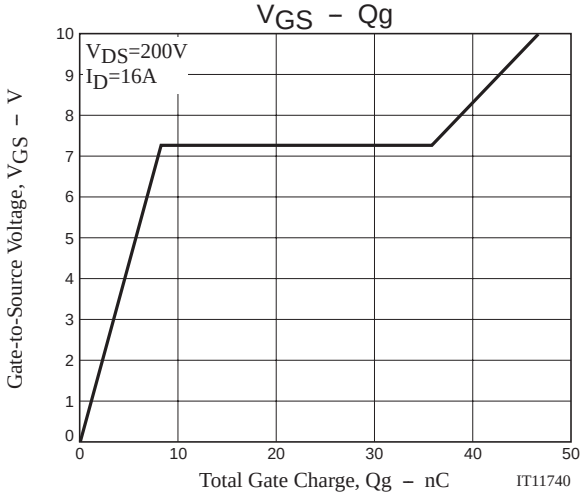




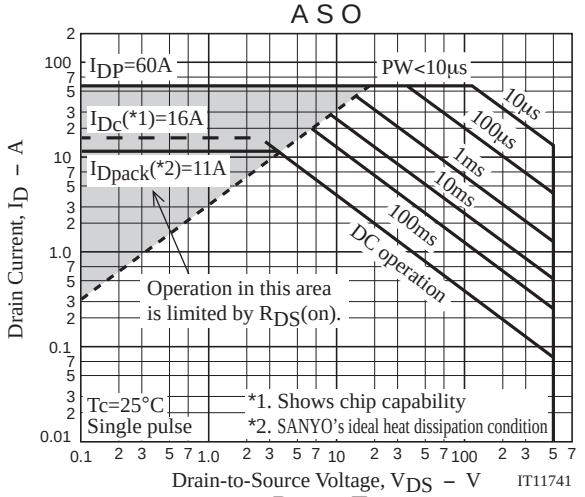




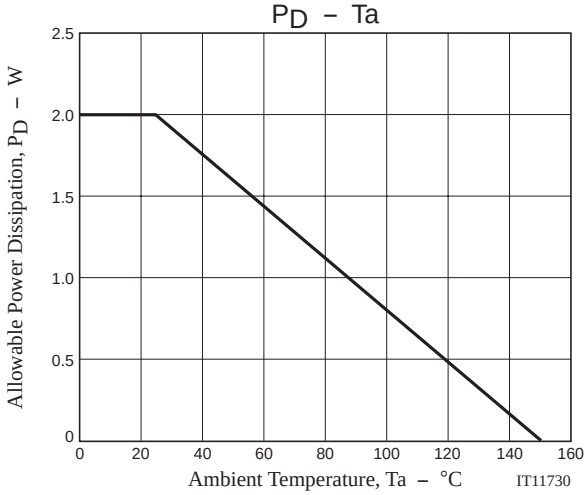
V_{GS} – Qg



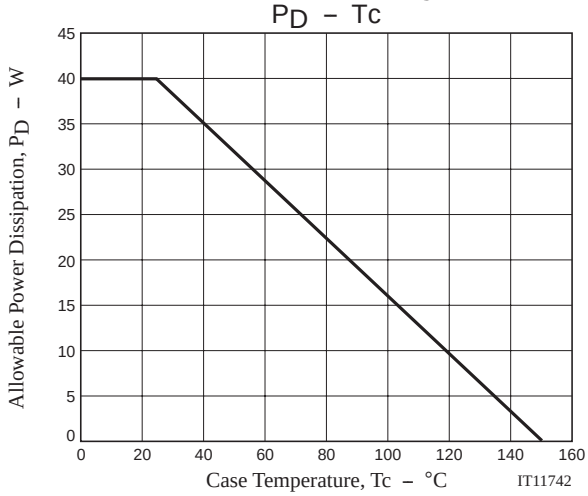
A S O



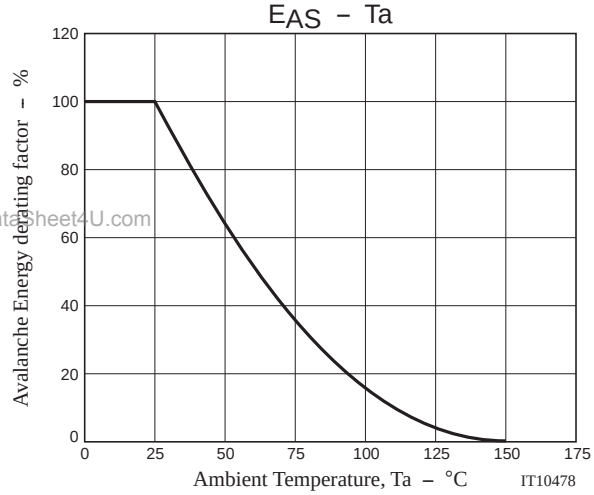
P_D – Ta



P_D – Tc



E_{AS} – Ta



Note on usage : Since the 2SK4085LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of April, 2007. Specifications and information herein are subject to change without notice.