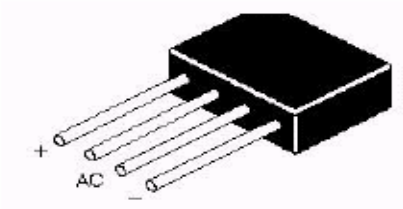
## SINGLE PHASE SILICON BRIDGE RECTIFIER

## KBP2005 - KBP210




**KBP**
**PLASTIC PACKAGE**

**Marking: 2KBPXXM**
**CDIL YYWW**
**YYWW-Date Code**

**Ratings at $T_a$=25°C Ambient Temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%**

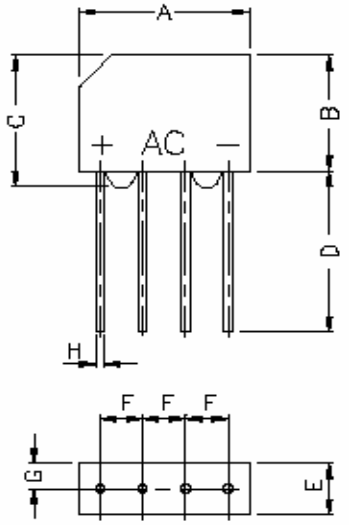
| DESCRIPTION | SYMBOL | KBP 2005 | KBP 201 | KBP 202 | KBP 204 | KBP 206 | KBP 208 | KBP 210 | UNIT |
|---|---|---|---|---|---|---|---|---|---|
| Maximum Recurrent Peak Reverse Voltage | $V_{RRM}$ | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum RMS Voltage | $V_{RMS}$ | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V |
| Maximum DC Blocking Voltage | $V_{DC}$ | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum Average Forward Rectified Current 0.375" (9.5mm) Lead Length at $T_a$=50ºC | $I_{F(AV)}$ | 2.0 | | | | | | | A |
| Peak Forward Surge Current, 8.3ms single half sine wave superimposed on rated load (JEDEC method) | $I_{FSM}$ | 60 | | | | | | | A |
| Maximum Forward Voltage at 2A at $T_a$= 25ºC | $V_F$ | 1.1 | | | | | | | V |
| Maximum Reverse Current at $T_a$=25°C<br>at Rated DC Blocking Voltage $T_a$=100°C | $I_R$ | 10<br>500 | | | | | | | µA<br>µA |
| Typical Junction Capacitance | *$C_J$ | 25 | | | | | | | pF |
| Typical Thermal Resistance | **$R_{th\ (j-a)}$ | 30 | | | | | | | °C/W |
| Typical Thermal Resistance | **$R_{th\ (j-L)}$ | 11 | | | | | | | °C/W |
| Operating and Storage Temperature Range | $T_J$, $T_{stg}$ | - 55 to +125 | | | | | | | °C |

***Measured at 1MHz and applied reverse voltage of 4.0 V**

****Thermal resistance junction to Ambient and from Junction to lead at 0.375" (9.5mm) lead length PCB mounted**

**KBP2005_210Rev_1 140307E**

# KBP Plastic Package




| DIM | MIN. | MAX. |
|---|---|---|
| A | 14.22 | 15.24 |
| B | 10.57 | 11.58 |
| C | 13.4 | 14.4 |
| D | 15.2 | – |
| E | 4.57 | 5.08 |
| F | 3.6 | 4.1 |
| G | – | 2.67 |
| H | 0.76 | 0.86 |

All dimensions are in mm

900 Pcs/Bulk Pack

## Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.

2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

## Disclaimer



CDIL is a registered Trademark of
**Continental Device India Limited**
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119
email@cdil.com www.cdilsemi.com

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