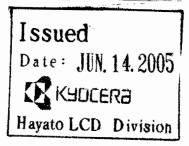
SPEC. NO.	TQ3C-8EACO-E1CUQ24-00
DATE	May 20, 2005

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TYPE: KCG047QVLAE-G00

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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD D1VISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by	:Engineering	Dept.	Confirmed by :QA Dept.	
Issue Data	Prepared	Checked	Approved	Checked	Approved
May 20, 2005	7. Onoteta	y Yanazaki	M.Fyjitani	Jujoshita	S. Hyports

Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

D	0 + 0		Design	ed by:	Engineering D	ept.	Confirmed by:	QA Dept.
Date		Prepa	red	Checked	Approved	Checked	Approved	
Rev. No	ο.	Date		Page		Descriptio	ons	

1. Application

This data sheet defines the specification for a $(320 \times R.G.B) \times 240$ dot, STN Transmissive color dot matrix type Liquid Crystal Display with LED backlight. FROHS Compliance

2. Construction and Outline

(320 x R.G.B) x 240 dots, COG type LCD with LED backlight.

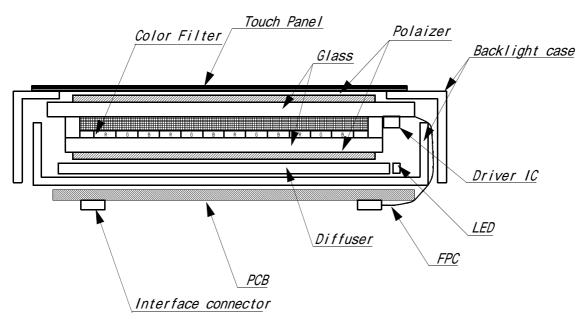
Backlight system : Side-edge type (LED).

Polarizer : Glare treatment.

Additional circuit : Bias voltage circuit, Randomizing circuit, DC-DC converter,

Constant current circuit for LED Backlight

Touch Panel : Analog type . Non-Glare treatment.



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	122.4(W) × 90.0(H) × 5.7(D)(PCB and bosses are not included) Refer outline drawing in detail.	mm
Effective viewing area	98.0 (W) × 74.0 (H)	mm
Dot number	(320×R.G.B) (W) × 240 (H)	Dots
Dot size	0.08 (W) × 0.28 (H)	mm
Dot pitch	0.10 (W) × 0.30 (H)	mm
Display color *1	White *2	-
Base color *1	Black *2	-
Mass	(120)	g

^{*1} Due to the characteristics of the LC material, the color vary with environmental temperature.

*2 Negative-type display

Display data "H" :R.G.B Dots ON : White Display data "L" :R.G.B Dots OFF : Black

3-2. Mechanical Specifications of touch panel

ITEM	SPECIFICATION	UNIT
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.5N ± 0.3N	-
Transmittance	Тур.80	%
Surface hardness	pencil hardness 2H or more according	-

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	6.0	V
Supply voltage for LCD driving	VCONT	0	VDD	V
Input signal voltage for LCD *1	Vin	0	VDD	V
FRM frequency	f FRM	-	150	Hz
LED forward current (Backlight)	IF	-	12	mA
Supply voltage for backlight	VinB	0	6.0	V
Backlight ON-OFF	BLEN	0	VinB	V
Brightness adjust voltage	VBRT	0	VinB	V
Touch Panel Supply voltage	Vtp	0	6.0	V
Touch Panel Input Current	Itp	0	0.5	mA

^{*1} Input signal :CP, LOAD, FRM, DISP, D0~D7

4-2. Environmental absolute maximum ratings

ITEM	SYMBOL	MIN	MAX	UNIT
Operating temperature *1	Тор	0	50	
Storage temperature *2	Тѕто	-20	60	
Operating humidity *3	Нор	10	*4	%RH
Storage humidity *3	Нѕто	10	*4	%RH
Vibration	-	*5	*5	-
Shock	-	*6	*6	-

^{*1} LCD's display quality shall not be guaranteed at the temperature range of : below 0 and upper 40 .

^{*2} Temp. = (-20) < (48h) , Temp = (60) < (168h)
Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock.
LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.
(Please refers to 16. Precautions for use)

^{*3} Non-condensation.

^{*4} Temp. 40 , 85% RH Max. Temp. > 40 , Absolute Humidity shall be less than 85%RH at 40 .

*5

Frequency	10 ~ 55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	(0.3~9 m/s ²)
Interval	10-55-10 Hz	1 minute

2 hours in each direction $\ensuremath{\text{X/Y/Z}}$ (6 hours as total) EIAJ ED-2531

 $^{*}6$ Acceleration: 490m/s^2 Pulse width: 11 ms

3 times in each direction : $\pm X/\pm Y/\pm Z$.

EIAJ ED-2531

5. Electrical Characteristics

5-1. VDD=5.00V

 $VDD = 5.00V \pm 0.25V$, $Temp. = 0 \sim 50$

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	-	4.75	5.00	5.25	V
LCD driving voltage *1	Vop=	0	1.20	-	-	V
	VCONT	25	1.30	1.80	2.30	V
		50	-	-	2.40	V
Input voltage (FRM,LOAD,CP,D0~D7,DISP)	Vin	"H" level	0.8VDD	-	VDD	V
(FRW, LOAD, GP, DO ~ D7, D13P)	VIII	"L" level	0	-	0.2VDD	V
Input current	lin	Vin=VDD or VSS	-100	-	100	μA
Rush current for logic	Irush	When rush current happens		3.0A(Peak)	× 1ms	
Clock frequency	f cp	-	2.02	2.16	10.00	MHz
Frame frequency *2	f FRM	-	70	75	150	Hz
Current consumption for logic	IDD	*3	-	30	45	mA
Power consumption	Pdisp		-	150	225	mW

- *1 Maximum contrast is obtained by adjusting the LCD driving voltage (Vop=Vcont) while at the viewing angle of = = 0°
- *2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performan -ce and quality prior to finalizing the frequency values:

 Generally, as frame and clock frequencies become higher current consumption will get bigger and display quality will be degraded.

$VDD = 3.30V \pm 0.30V$, Temp.	=	0~5	งบ
---------------------------------	---	-----	----

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	-	3.00	3.30	3.60	٧
LCD driving voltage *1	Vop=	0	1.20	-	-	٧
	VCONT	25	1.30	1.80	2.30	٧
		50	-	-	2.40	٧
Input voltage (FRM,LOAD,CP,D0~D7,DISP)	Vin	"H" level	0.8VDD	-	VDD	٧
(FRM, LOAD, GP, DO ~ D7, D15P)	Vin	"L" level	0	-	0.2VDD	V
Input current	lin	Vin=VDD or VSS	-100	-	100	μA
Rush current for logic	Irush	When rush current happens		3.0A(Peak)	× 1ms	
Clock frequency	f cp	-	2.02	2.16	10.00	MHz
Frame frequency *2	f FRM	-	70	75	150	Hz
Current consumption for logic	IDD	*3	-	45	68	mA
Power consumption	Pdisp		-	150	225	mW

- *1 Maximum contrast is obtained by adjusting the LCD driving voltage (Vop=Vcont) while at the viewing angle of = = 0°
- *2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performan-ce and quality prior to finalizing the frequency values:

 Generally, as frame and clock frequencies become higher current consumption will get bigger and display quality will be degraded.

5-2. Touch Panel

5-2-1. Terminal resistance

Between xL and xR : 200 \sim 1000 Between yU and yL : 200 \sim 1000

5-2-2. Linearity ± 1.5%

5-2-3. Insulation resistance 100M or more at DC25V

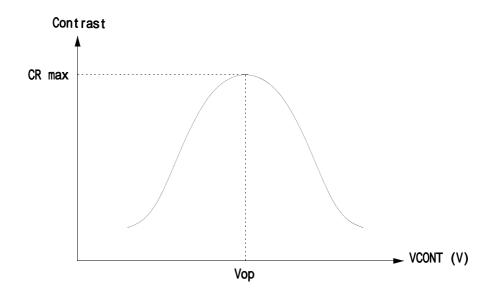
6 . Optical Characteristics

Measuring Spot 6.0mm Temp. = 25ITEM SYMBOL CONDITION MIN. TYP. MAX. UNIT Tr = =0 ° 190 290 Response Rise ms time = =0 ° Td 180 280 Down Upper 20 deg. Lower 30 Viewing angle range CR 2 Left 50 deg. Right 50 = =0 ° Contrast ratio CR 15 30 Brightness L (IF=10mA/1 LED line) (50)cd/m² (90)Chromaticity (0.44)(0.49)(0.54)Х coordinates Red = =0 ° (0.30)(0.35)(0.40)У (0.24)(0.29)(0.34)Х = =0 ° Green У (0.43)(0.48)(0.53)(0.10)(0.15)(0.20)Х Blue = =0 ° (0.07)(0.12)(0.17)у (0.22)(0.27)(0.32)Х White = =0 ° У (0.24)(0.29)(0.34)

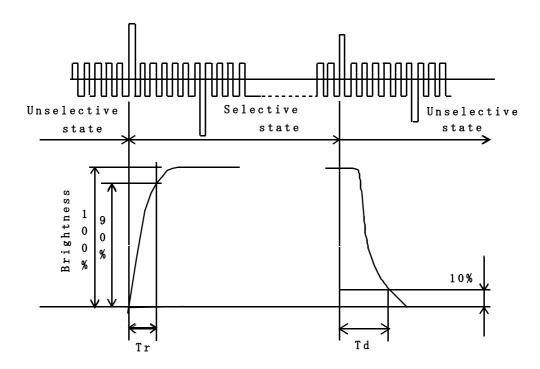
Optimum contrast is obtained by adjusting the LCD driving voltage (Vop=Vcont) while at the viewing angle of = = 0°

6-1. Contrast ratio is defined as follows:

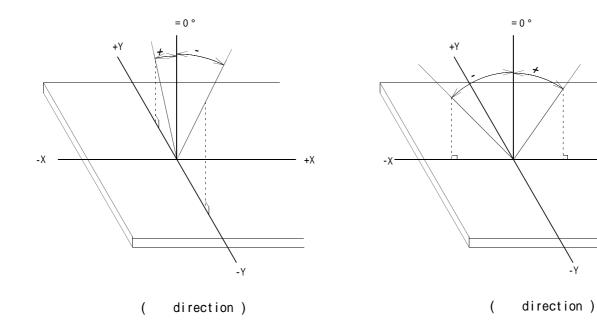
6-2. Definition of Vop



6-3. Definition of response time



6-4. Definition of viewing angle



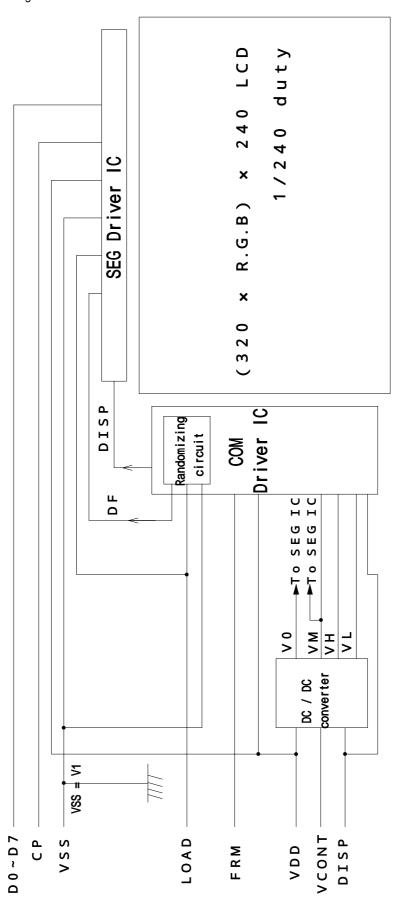
+X

6-5. Measuring points

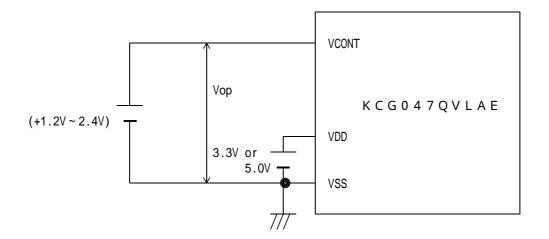
	80 x 3	160	× 3	240 ×	3	(dot)
	1] 	4		
60			<u> </u>			
120	 	3	<u> </u>	 		
120	2		1	5		
180	<u> </u>		<u>i</u> !			
(dot)			1	, 1 1		

- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after LED is turned on. (Ambient Temp.=25)

7. Circuit Block Diagram



7-1. Power supply



8. Interface signals

8-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	FRM	Synchronous signal for driving scanning line	Н
2	LOAD	Data signal latch clock	H L
3	CP	Data signal shift clock	H L
4	DISP	Display control signal	H(ON),L(OFF)
5	VDD	Power supply for logic	
6	VSS	GND	
7	VCONT	LCD adjust voltage	
8	D7		
9	D6		
10	D5		
11	D4	Display data	H(ON),L(OFF)
12	D3		
13	D2		
14	D1		
15	DO DO		
16	VDD	Supply voltage for logic	-
17	VDD		
18	VSS	GND	-
19	VSS		
20	VSS		

LCD side connector : 08-6210-020-340-800+ (ELCO) Recommended matching connector : 0.5mm pitch FFC or FPC

8-2. Backlight

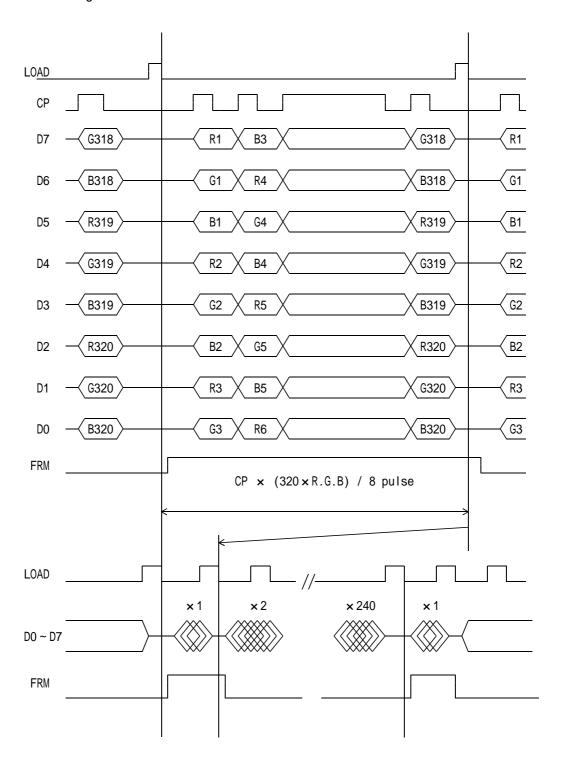
PIN No.	SYMBOL	DESCRIPTION
1	VinB	
2	VinB	Supply voltage
3	VinB	
4	VinB	
5	BLEN	Backlight ON-OFF (H : ON , L : OFF)
6	VBRT	Brightness adjust voltage
7	GND	
8	GND	GND
9	GND	
10	GND	

Backlight side connector : 08-6210-010-340-800+ (ELCO) Recommended matching connector : 0.5mm pitch FFC or FPC

8-3. Touch panel

PIN No.	SYMBOL	DESCRIPTION
1	уU	y-Upper terminal
2	xL	x-Left terminal
3	yL	y-Lower terminal
4	xR	x-Right terminal

9. Interface Timing Chart

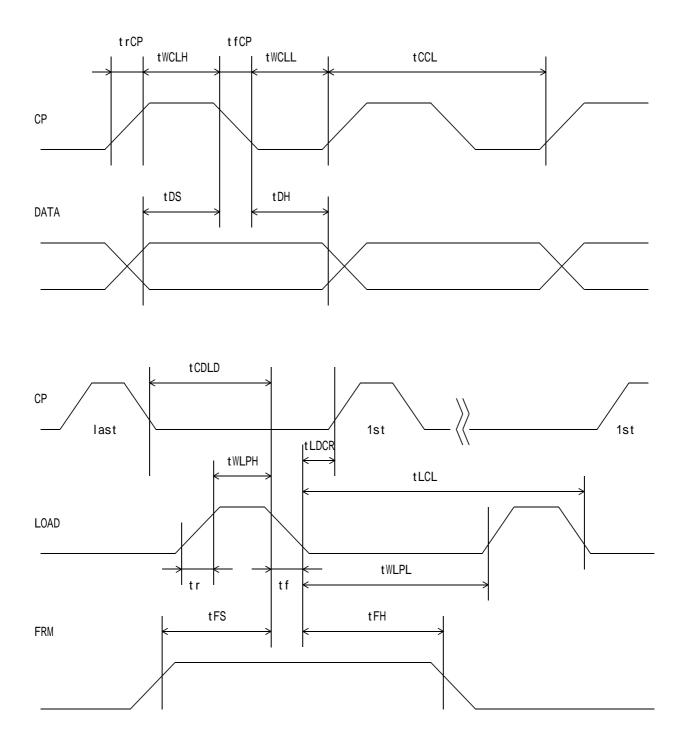


- * The cycle of load signal should be stable and continuously applied without interruption.
- * The above-mentioned timing chart shows a reference to set up a LCD module, not an electrical rating.

10 . Data and Screen

							Cŀ	HIP ARE	ĒΑ					
_	V.4		Y1			Y2			Y3		•••		Y320	
C H I P	X1	D7 R1	D6 G1	D5 B1	D4 R2	D3 G2	D2 B2	D1 R3	D0 G3	D7 B3		D2 R320	D1 G320	D0 B320
A R E A														
	X240													

11. Input Timing Characteristics



11-1. Switching characteristics (VDD = 5.00V)

Input Characteristics ; VDD = 5.00V \pm 0.25V, Temp. = 0 \sim 50

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1	tCCL	100	-	ns
CP "H" Pulse Width	tWCLH	30	-	ns
CP "L" Pulse Width	tWCLL	30	-	ns
CP Rise Up Time	t rCP	-	15	ns
CP Fall Down Time	t f CP	-	15	ns
Data Set Up Time	tDS	25	-	ns
Data Hold Time	t DH	25	-	ns
LOAD "H" Pulse Width	tWLPH	40	-	ns
LOAD "L" Pulse Width	tWLPL	400	-	ns
LOAD Cycle *2	tLCL	500	-	ns
CP Down LOAD Down Delay Time	tCDLD	60	-	ns
LOAD Down CP Rise Delay Time	tLDCR	60	-	ns
Input Signal Rise Up Time	tr	-	20	ns
Input Signal Fall Down Time	tf	-	20	ns
FRM Data Set Up Time	tFS	120	-	ns
FRM Data Hold Time	t FH	30	-	ns

^{*1} CP Cycle is adjust so that FRM signal is 75Hz.

^{*2} Load cycle is const.

11-2. Switching characteristics (VDD = 3.30V)

Input Characteristics ; VDD = $3.30V \pm 0.30V$, Temp. = $0 \sim 50$

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1	tCCL	100	-	ns
CP "H" Pulse Width	tWCLH	40	-	ns
CP "L" Pulse Width	tWCLL	40	-	ns
CP Rise Up Time	t rCP	-	20	ns
CP Fall Down Time	t f CP	-	20	ns
Data Set Up Time	tDS	35	-	ns
Data Hold Time	t DH	35	-	ns
LOAD "H" Pulse Width	tWLPH	50	-	ns
LOAD "L" Pulse Width	tWLPL	400	-	ns
LOAD Cycle *2	tLCL	500	-	ns
CP Down LOAD Down Delay Time	tCDLD	60	-	ns
LOAD Down CP Rise Delay Time	tLDCR	80	-	ns
Input Signal Rise Up Time	tr	-	20	ns
Input Signal Fall Down Time	tf	-	20	ns
FRM Data Set Up Time	tFS	120	-	ns
FRM Data Hold Time	tFH	30	-	ns

^{*1} CP Cycle is adjust so that FRM signal is 75Hz.

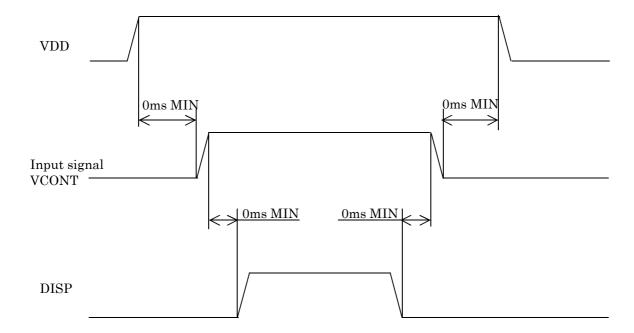
^{*2} Load cycle is const.

12. Supply Voltage Sequence Condition

Always follow the power supply ON / OFF sequence as specified below.

Unless you follow the power supply ON / OFF sequences, driving circuit in the LCD may irreparably break and / or DC voltage may be supplied to the LCD.

DC voltage induces irreversible electrochemical reactions on the screen and reduce LCD life.



- * Input signal : CP, LOAD, FRM, D0 ~ D7
 Each signal (CP,LOAD,FRM) is constant.
- * The above sequence should be designed as to keep each normal figure on condition that liquid crystal module is loaded on your system.
- * Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

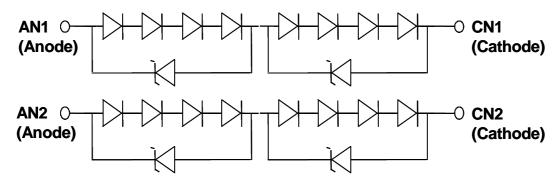
13. LED Backlight Characteristics

13-1. LED Backlight Characteristics

Temp. = 25

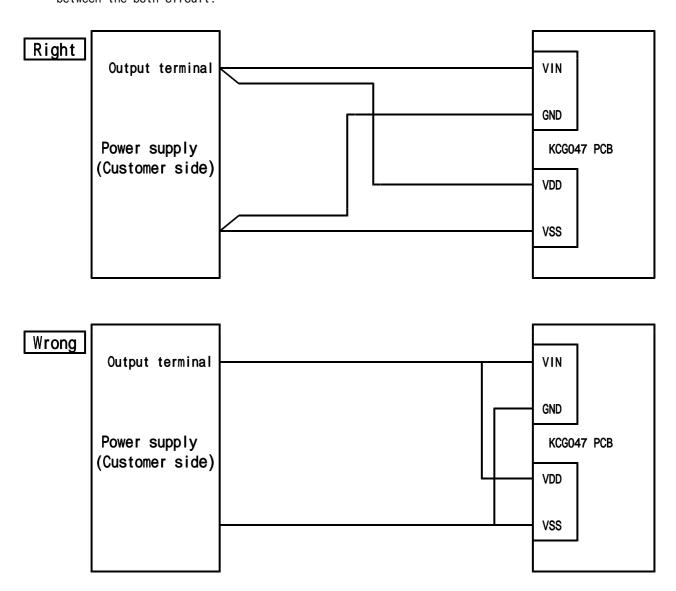
ITEM	Symbol	MIN	TYP.	MAX	UNIT	NOTE
Supply voltage	VinB	3.0	-	5.5	V	0 ~ 50
ON-OFF (H)	DI ENI	0.8VinB	-	VinB	V	-
ON-OFF (L)	- BLEN	0.0	-	0.2VinB	V	-
LED forward current	IF	9.0	10.0	11.0	m A	VBRT=0 ~ 0.8V
*1	IF	-	(2.0)	-	mA	VBRT=2.8V
Cupply ourrent	LinD	-	(290)	(350)	A	VinB=3.3V,IF=10mA *1
Supply current	linB	-	(180)	(220)	mA mA	VinB=5.0V, IF=10mA *1
Operating Life *2	Т	-	(50,000) *3	-	hour	IF=10mA *1

*1 Between AN1-CA1 and between AN2-CA2 each. (Please refer to below drawing.)

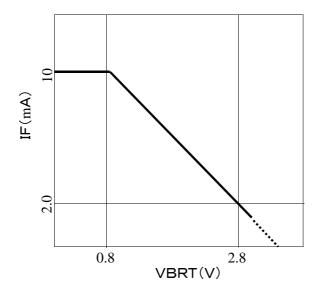


- *2 When surface brightness decreases 50% of initial brightness .
- *3 Life time is estimated data.
- *4 When you start-up, please charge in sequence of VinB->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT->VinB.
- *5 Please do not connect the other than our backlight to this output connector on the PCB.

*6 In case VDD and VIN are supplied by a single power source, VDD & VIN, and VSS & GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & VIN, and for VSS & GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.



13-2. VBRT-IF characteristics



VBRT-IF characteristics (reference) Ta=25

1 4 . Design Guidance for Analog Touch-Panel(T/P)

14-1. Electrical

In customer's design, please remember the following considerations.

- 1. Do not use the current regulated circuit.
- 2. Keep the current limit with top and bottom layer. (See Sec, 4-1)
- 3. Analog T/P can not sense two point touching separately.
- 4. A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
- 5. Analog T/P is also a "Capacitor" in an equivalent circuit.

 Design your sensing circuit and low-pass filter with considering this "Capacitor" value.
- 6. Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

14-2. Software

- 1. Do the "User Calibration".
- 2. "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3. When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

14-3. Mounting on display and housing bezel

- 1. Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2. Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3. If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur.

This will cause sometimes a short circuit.

15 . Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

KCG047QVLAE-G00 - -___ - MADE IN _____

Year Month Date Version Number Country or origin(Japan or China)

YEAR	2005	2006	2007	2008	2009	2010
CODE	5	6	7	8	9	0
MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6
MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	Х	Υ	Z

16.Warranty

16-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

16-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

17. Precautions for use

17-1. Installation of the LCD

- 1. Please ground of an LCD module, in order to stabilize brightness and display quality.
- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

 Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

17-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

17-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Adjust "LCD driving voltage" to obtain optimum viewing angle and contrast.
- 3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

<u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

17-4. Storage

- 1. The LCD shall be stored within the temperature and humidity limits specified.

 Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

17-5. Screen Surface

- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3. When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4. Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
- 7. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body, rinse it off right away with water and soap.

18 . Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	(70)	(240 h)	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	(-20)	(240 h)	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	(40) (90%RH)	(240 h)	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	(-20 0.5 h) (R.T. 0.5 h) (70 0.5 h)	(10 cycles)	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	(50) (Vop)	(240 h)	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Display Quality : No defect Current Consumption : No defect

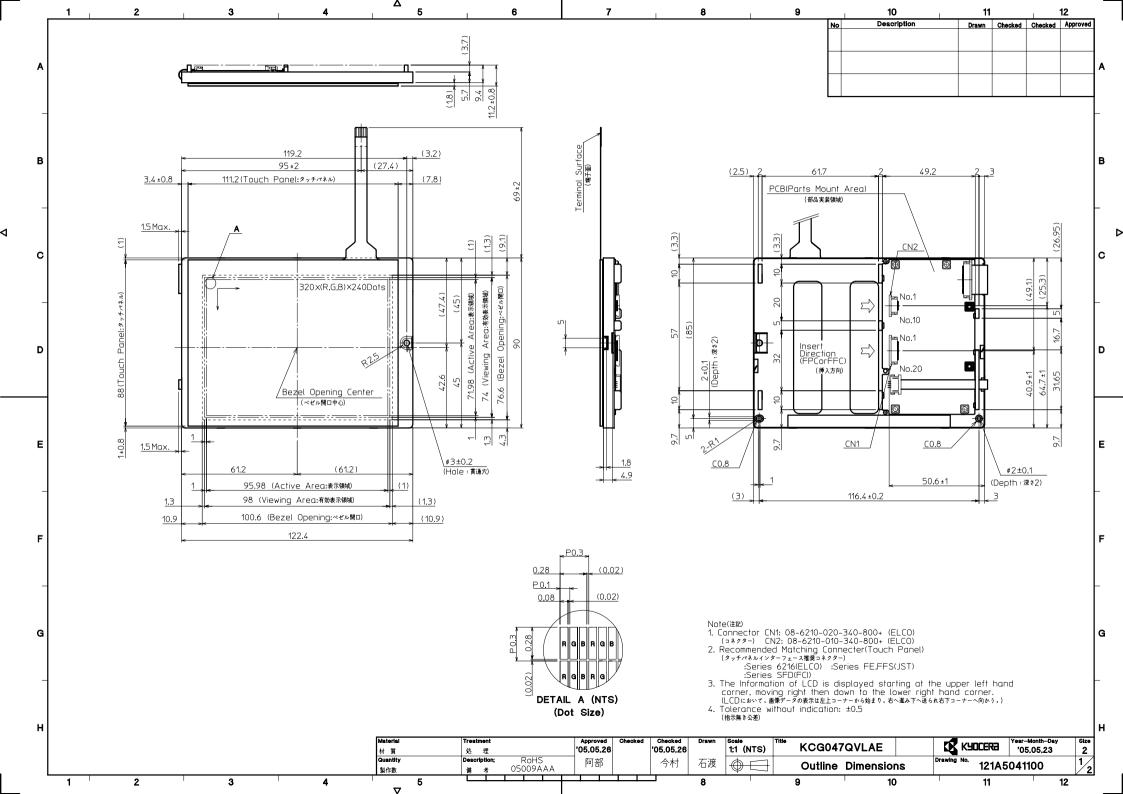
 $^{^{\}star}$ Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

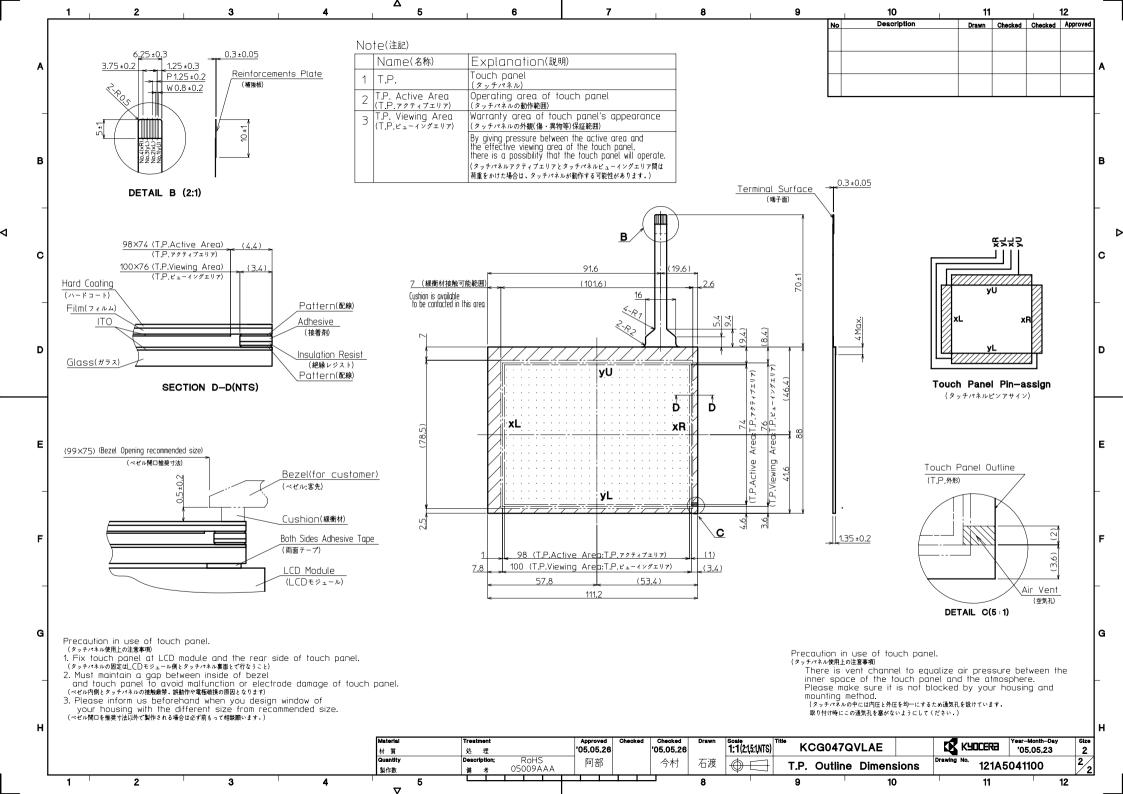
^{*} The LCD is tested in circumstances in which there is no condensation.

^{*} The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

^{*} The reliability test is not an out-going inspection.

^{*} The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





SPEC. NO.	TQ3C-8EAC0-E2CUQ25-00
DATE	May 20, 2005

F	O R	•		
T	O II	•		

KYOCERA INSPECTION STANDARD

TYPE : KCGO47QVLAE-GOO

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	Confirmed by :QA Dept.		
Issue Data	Prepared	Checked	Approved	Checked	Approved
May 20, 2005	7. Ondera	J. Yamazaki	M.FujiTani	J. Yosluta	S. Hypohi

Revision Record

D	ate		Designed by:		Engineering D	ept.	Confirmed by: QA Dept.		
Da	аге		Prepa	red	Checked	Approved	Checked	Approved	
Rev. No	ο.	Date		Page		Descriptio	ons		

Visuals specification

1)Note

Item		Note				
General	 When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION) This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area. 					
	standard happen, additi by mutual agreement bet	ch are not specified in this onal standard shall be determined ween customer and Kyocera.				
	Inspection distance : 3 Temperature : 2	500 Lux minimum . 300 mm (from the sample) $25~\pm~5~^\circ\mathrm{C}$ right above				
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.				
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.				
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.				

2)Standard

2)Standard	1					
Inspection item			Judgeme	nt standard		
Pinhole, Bright spot Black spot, Foreign particle (LCD portion)	a			d = (a + b) / 2		
	Category	Size	(mm)	Acceptabl	e number	
	A		≤ 0.2		ected	
	В	0.2 < d	≦ 0.3	5		
	С	0.3 < d	≦ 0.5	3		
	D	0.5 < d		0		
Scratch, Foreign particle (LCD portion)	W L					
	Wid	th (mm)	Len	gth (mm)	Acceptable No.	
	A	W ≤ 0.03			neglected	
	В			L ≤ 2.0	neglected	
	C 0.03<	$<$ W ≤ 0.1	2.0 <	L ≦ 4.0	3	
	D		4.0 <	C L	0	
	E 0.1 <	< W	-		According to Circular	
Contrast variation						
	Category	a Size (mm) ≤ 0.5	Acceptabl	+ b) / 2 e number ected	
	В	0.5 < d		3		
	С	0.3 < d $0.7 < d$		0		
				0		

Inspection item			Judgeme	nt standard			
Polarizer (Scratch, Bubble, Dent)	(1) Scratc	h	L	W			
	Widt	h (mm)	Len	gth (mm)	Acceptable No.		
	A	W ≦ 0.1		_	neglected		
	B 0 1 <	$W \leq 0.3$		L ≦ 5.0	neglected		
	С 0.1	" = 0.0	5.0 <	L	0		
	D 0.3 < W — 0						
	(2) Bubble	(dent)		a d = (a +	b) / 2		
	Category	Size	Acceptable	number			
	A $d \leq 0.2$ neglected				cted		
	В						
	С	0.3 < d	≦ 0.5	3			
	D 0.5 < d 0						

Inspection item	Judgement standard							
	(D = Average Diameter = (major axis + minor axis)/2							
	Width(mm) Length(mm) Acceptable No.							
		0.1≧W		4 ≧ L		within φ		
	Scratch	0.08 ≥ ₩		6 ≧ L		within φ		
Scratch,		$0.05 \ge W$	> 0.03	10 ≧ L		within φ		
Foreige particle		0.03 ≧ W 20 ≧ L		neglected				
(Touch Screen portion)	Foreige	$0.1 \ge W$	> 0.05	5 ≧ L	2pcs	2pcs within φ30mm		
	(line	0.05≧W		neglecte	ed	neglected neglected		
	like)							
	Foreige		0.3 ≧ D >	> 0.2	2pcs	within ϕ		
		(circle 0.2≧D			neglected			
	like)							
	Above are applied to the visible area. Unless there are foreign particles and damage affected seriously to the electrical performance in the visible area, we approve of this product.							
Glass crack								
(Touch Screen portion)		Corr	ner cra	ck	Cracks in other area			
					than in corner			
	Judgeme							
	nt stan							
	dard			<u>/</u> ///				
		X	Y	Z	X	Y	Z	
		≦ 3	≦ 3	<t< td=""><td>≦ 5</td><td>≤ 1.5</td><td><t< td=""></t<></td></t<>	≦ 5	≤ 1.5	<t< td=""></t<>	
		2pcs/panel			2pcs/side			
		Progr						
]				
	Judgeme							
	nt stan							
	dard							
		Opcs(NG even 1pcs)						
		Upcs	(NG eve	n ipcs)				
	Above are	e applied		e visible	area.			
	Unless there are foreign particles and damage affected							
						amage aff in the vi		