SPEC

Spec No.	TQ3C 8EAC0 E1DDV13 00
Date	February 26, 2007

TYPE : KCG057QV1DC-G770

< 5.7 inch QVGA transmissive color STN with touch panel >

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Hayato LCD Division

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

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Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.	
	Prepared	Checked	Approved	Checked	Approved
February 26, 2007	Y. Asano	Y. Yamazaki	M. Fujitani	S. Higashi	

Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module In such applications.

2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision record

Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev.No.	Date	Page	Descriptions

1. Application

This document defines the specification of KCG057QV1DC-G770. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type STN
Duty ratio	: 1/240 duty
Backlight system	: CFL (1 tube, U-figure type)
Inverter	: Option
Recommended inverter	: CXA-L0612A-VJL (TDK) or equivalent
Polarizer	: Glare treatment
Additional circuit	: Bias voltage circuit, Randomizing circuit, DC/DC converter circuit, Temperature compensation circuit
Touch panel	: Analog type, Non-Glare treatment

3. Mechanical specifications

3-1. Mechanical specifications of LCD

Item	Specification	Unit
Outline dimensions	144 (W)× (104.8) (H) × 14.5 (D)	mm
Active area	115.18 (W) × 86.38 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Effective viewing area	117.2 (W) × 88.4 (H)	mm
Dot format	320×(R,G,B) (W) × 240 (H)	dot
Dot size	0.10 (W) × 0.34 (H)	mm
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color *1	Normally Black	-
Mass	220	g

*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation force	0.5±0.3	N
Transmittance	Typ. 80	%
Surface hardness	Pencil hardness 2H or more according	-

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V_{DD}	0	6.0	V
Supply voltage for LCD driving	V_{CONT}	0	V_{DD}	V
Input signal voltage *1	V_{IN}	0	V_{DD}	V
FRM frequency	f_{FRM}	-	150	Hz
Supply voltage for touch panel	V_{TP}	0	6.0	V
Input current of touch panel	I_{TP}	0	0.5	mA

*1 Input signal : FRM, LOAD, CP, DISP, D0～D7

4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature *1	T_{OP}	0	60	°C
Storage temperature *2	T_{STO}	-20	60	°C
Operating humidity *3	H_{OP}	10	*4	%RH
Storage humidity *3	H_{STO}	10	*4	%RH
Vibration	-	*5	*5	-
Shock	-	*6	*6	-

*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

*2 Temp. = -20°C＜48h, Temp. = 60°C＜168h
Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.
(Please refer to “Precautions for Use” for details.)

*3 Non-condensing

*4 Temp.≦40°C, 85%RH Max.
Temp.＞40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10～55 Hz	Acceleration value (0.3～9 m/s²)
Vibration width	0.15mm	
Interval	10-55-10 Hz 1 minutes	

2 hours in each direction X, Y, Z (6 hours total)
EIAJ ED-2531

*6 Acceleration: 490 m/s², Pulse width: 11 ms
3 times in each direction: ±X, ±Y, ±Z
EIAJ ED-2531

5. Electrical characteristics

5-1. V_{DD} = 5.0V

V_{DD} = +5.0V±5%, Temp. = 0～60°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	V_{DD}	-	4.75	5.00	5.25	V
Supply voltage for LCD driving *1, *2	V_{CONT}= V_{OP}	0～60°C *3	1.30	1.80	2.30	V
Input signal voltage (FRM,LOAD,CP,DISP,D0～D7)	V_{IN}	"High" level	0.8V_{DD}	-	V_{DD}	V
		"Low" level	0	-	0.2V_{DD}	V
Input current	I_{IN}	Input signal	-100	-	100	μA
Rush current for logic	I_{RUSH}	When LCD turn on.	3.0A (Peak) × 1ms			
Clock frequency	f_{CP}	-	-	-	10.00	MHz
Frame frequency *4	f_{FRM}	-	70	75	80	Hz
Current consumption for logic	I_{DD}	*5	-	19	41	mA
Power consumption	P_{DISP}		-	95	143	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (V_{CONT} = V_{OP}) for driving the LCD.

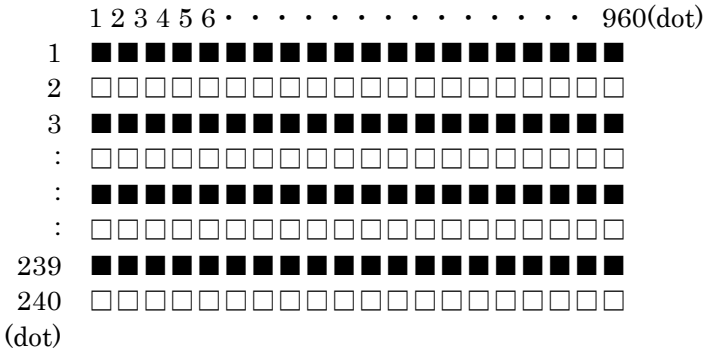
*2 Frame frequency : f_{FRM} = 75Hz

*3 The LCD module has a temperature compensation circuit.

*4 In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

*5 Display pattern:

V_{DD} = 5.0V, V_{CONT} = V_{OP}, f_{FRM} = 75Hz, f_{CP} = 2.16MHz, Temp. = 25°C



5-2. V_{DD} = 3.3V

V_{DD} =+3.3V±0.3V, Temp. = 0～60°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	V_{DD}	-	3.0	3.3	3.6	V
Supply voltage for LCD driving *1, *2	V_{CONT}= V_{OP}	0～60°C *3	1.30	1.80	2.30	V
Input signal voltage (FRM,LOAD,CP,DISP,D0-D7)	V_{IN}	"High" level	0.8V_{DD}	-	V_{DD}	V
		"Low" level	0	-	0.2V_{DD}	V
Input current	I_{IN}	Input signal	-100	-	100	µA
Rush current for logic	I_{RUSH}	When LCD turn on.	3.0A (Peak) × 1ms			
Clock frequency	f_{CP}	-	-	-	10.00	MHz
Frame frequency *4	f_{FRM}	-	70	75	80	Hz
Current consumption for logic	I_{DD}	*5	-	29	44	mA
Power consumption	P_{DISP}		-	96	144	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (V_{CONT}=V_{OP}) for driving the LCD.

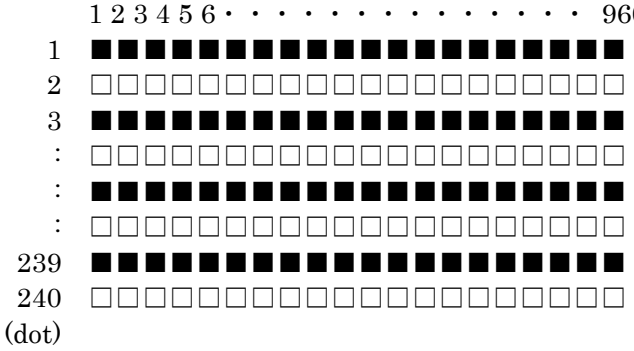
*2 Frame frequency : f_{FRM} = 75Hz

*3 The LCD module has a temperature compensation circuit.

*4 In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

*5 Display pattern:

V_{DD} = 3.3V, V_{CONT} = V_{OP}, f_{FRM} = 75Hz, f_{CP} = 2.16MHz, Temp. = 25°C



5-3. Touch panel

Item	Specification
Supply voltage for touch panel	5.0V
Terminal resistance	xL～xR : 200Ω～1,000Ω
	yU～yL : 200Ω～1,000Ω
Linearity	less than ±1.5%
Insulation resistance	100MΩ or more at DC25V

6. Optical characteristics

Measuring spot = ϕ 6.0mm, Temp. = 25°C

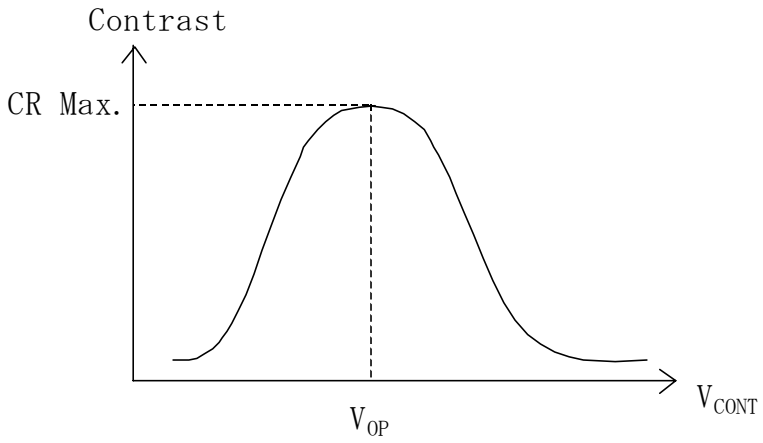
Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_r	$\theta = \phi = 0°$	-	250	360	ms
	Down	τ_d	$\theta = \phi = 0°$	-	200	290	ms
Viewing angle range		θ UPPER	CR≧2	-	65	-	deg.
		θ LOWER		-	70	-	
		ϕ LEFT		-	55	-	deg.
		ϕ RIGHT		-	55	-	
Contrast ratio		CR	$\theta = \phi = 0°$	30	60	-	-
Brightness		L	IL=4.0mArms	200	300	-	cd/m²
Chromaticity coordinates	Red	x	$\theta = \phi = 0°$	0.57	0.62	0.67	-
		y		0.29	0.34	0.39	
	Green	x	$\theta = \phi = 0°$	0.23	0.28	0.33	
		y		0.50	0.55	0.60	
	Blue	x	$\theta = \phi = 0°$	0.09	0.14	0.19	
		y		0.05	0.10	0.15	
	White	x	$\theta = \phi = 0°$	0.25	0.30	0.35	
		y		0.26	0.31	0.36	

Optimum contrast is obtained by adjusting the LCD driving voltage (V_{OP}) while at the viewing angle of $\theta = \phi = 0°$.

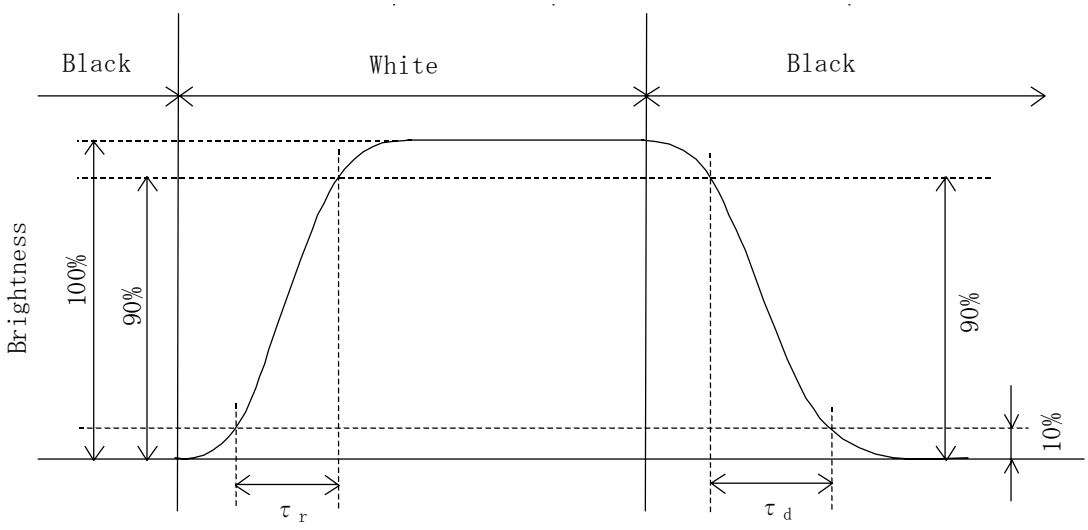
6-1. Definition of contrast ratio

$$\text{CR(Contrast ratio)} = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$

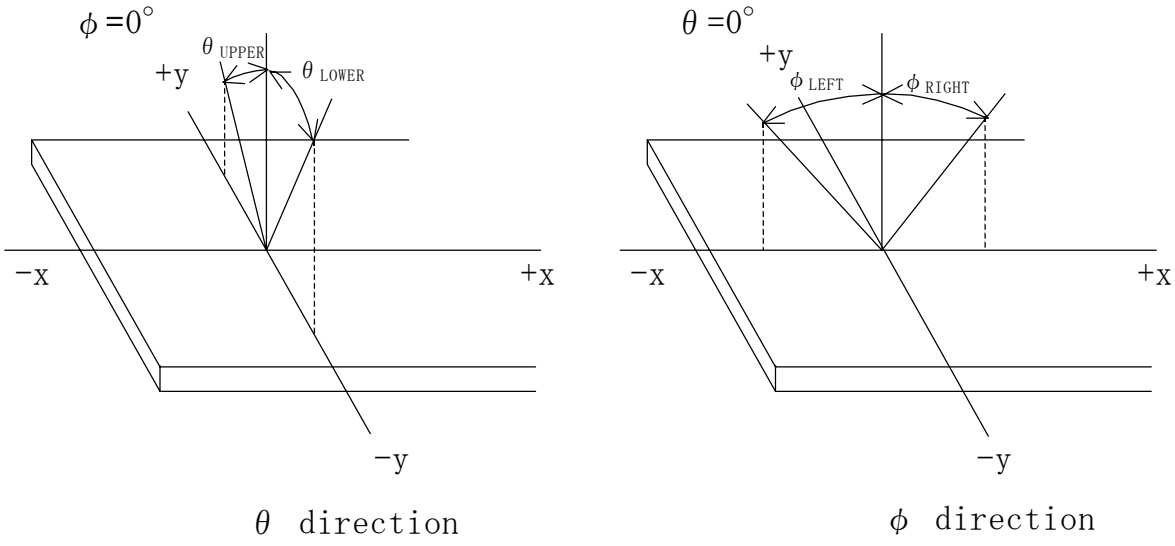
6-2. Definition of V_{OP}



6-3. Definition of response time

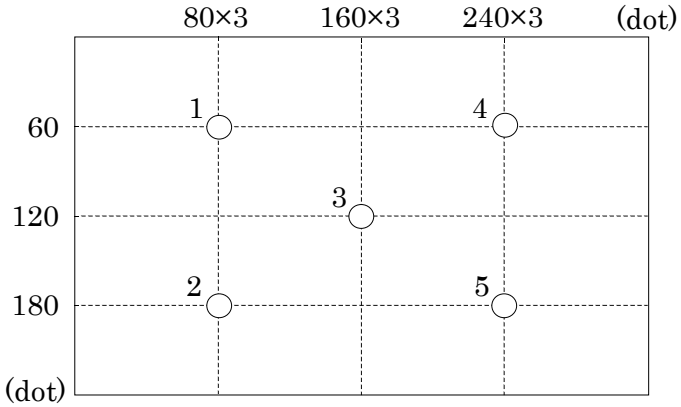


6-4. Definition of viewing angle



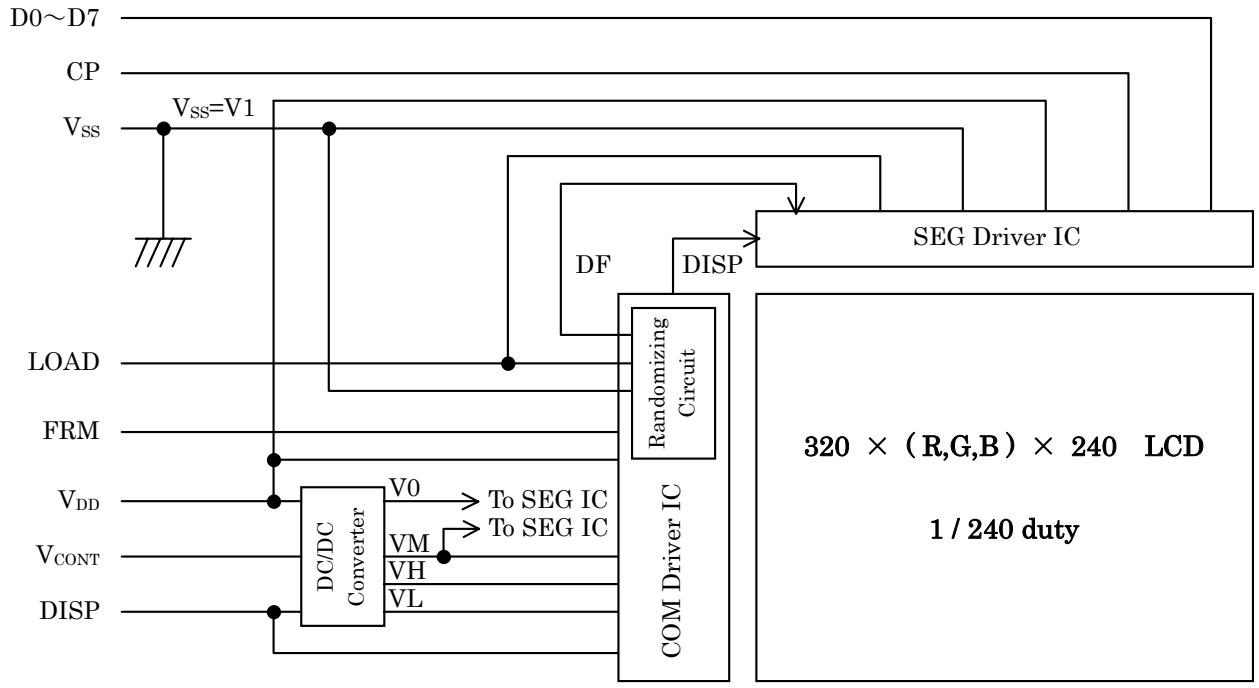
θ direction ϕ direction

6-5. Brightness measuring points

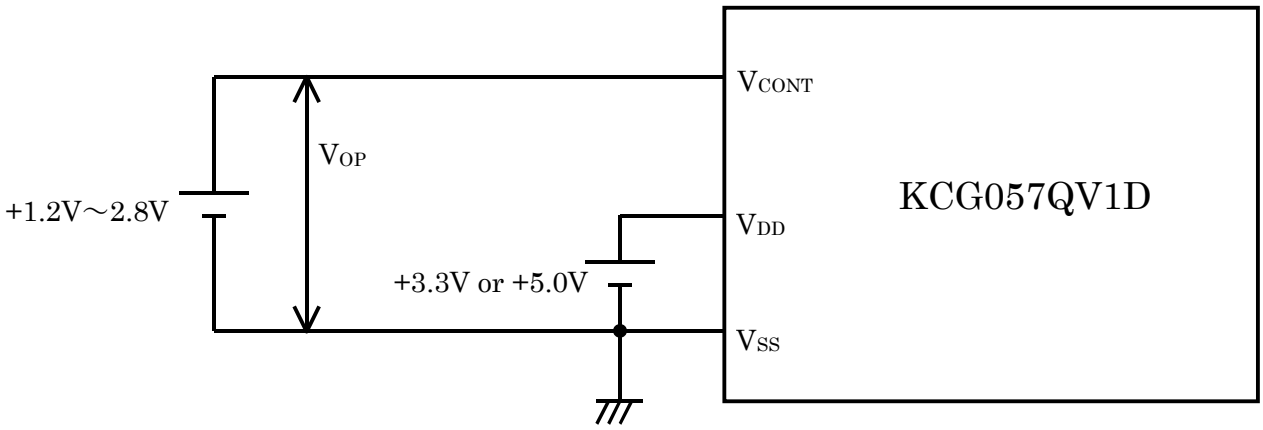


1) Rating is defined on the average in the viewing area.
2) Measured 30 minutes after the CFL is powered on. (Ambient temp. = 25°C)
3) The inverter should meet the CFL rating in the specification:
-Sine, symmetric waveform without spike in positive and negative.

7. Circuit block diagram



7-1. Power supply



8. Interface signals

8-1. Pin assignment of LCD panel

No.	Symbol	Description	Level
1	FRM	Synchronous signal for driving scanning line	H
2	LOAD	Data signal latch clock	H → L
3	CP	Data signal shift clock	H → L
4	DISP	Display control signal	H(ON),L(OFF)
5	V_{DD}	Power supply for logic	-
6	V_{SS}	GND	-
7	V_{CONT}	LCD adjust voltage	-
8	D7	Display data	H(ON),L(OFF)
9	D6		
10	D5		
11	D4		
12	D3		
13	D2		
14	D1		
15	D0		
16	V_{DD}	Power supply for logic	-
17	V_{DD}		
18	V_{SS}	GND	-
19	V_{SS}		
20	V_{SS}		

LCD connector : 08-6210-020-340-800+ (ELCO)
Recommended matching FFC or FPC : 0.5mm pitch

8-2. Pin assignment of CFL

No.	Symbol	Description
1	Hot	Inverter output high voltage side
2	N.C.	-
3	Cold	Inverter output low voltage side

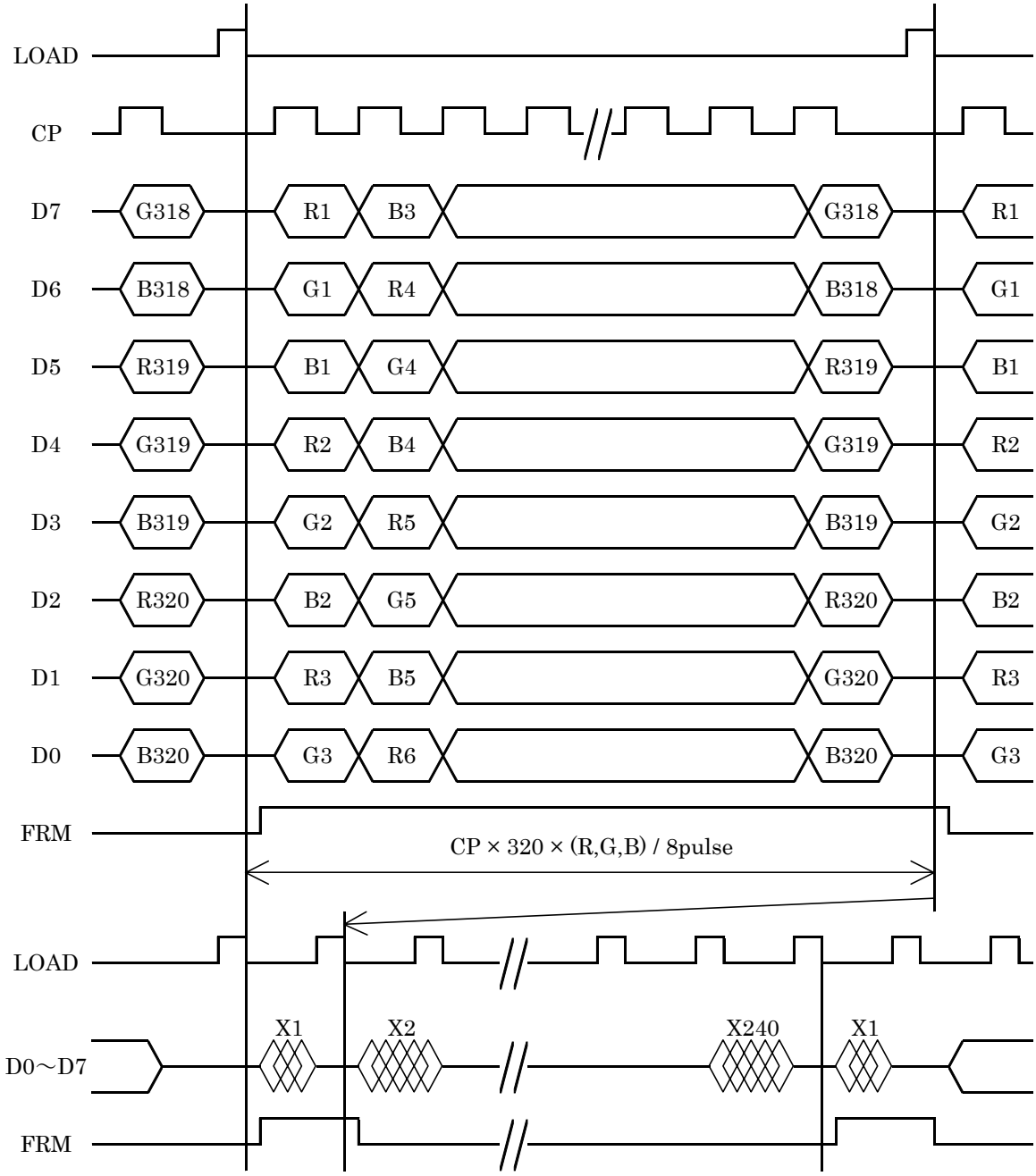
LCD side connector : BHR-03VS-1 (JST)
Recommended matching connector
: SM02(8.0)B-BHS-1 (JST)
: SM02(8.0)B-BHS-1-TB(LF)(SN) (JST)・・・(RoHS Compliant)

8-3. Pin assignment of touch panel

No.	Symbol	Description
1	yU	y-Upper terminal
2	xL.	x-Left terminal
3	yL	y-Lower terminal
4	xR	x-Right terminal

Touch panel side connector : 1.25mm pitch
Recommended matching connector : Series 6216 (ELCO)
: Series FE, FFS (JST)
: Series SFD (FCI)

9. Interface timing chart

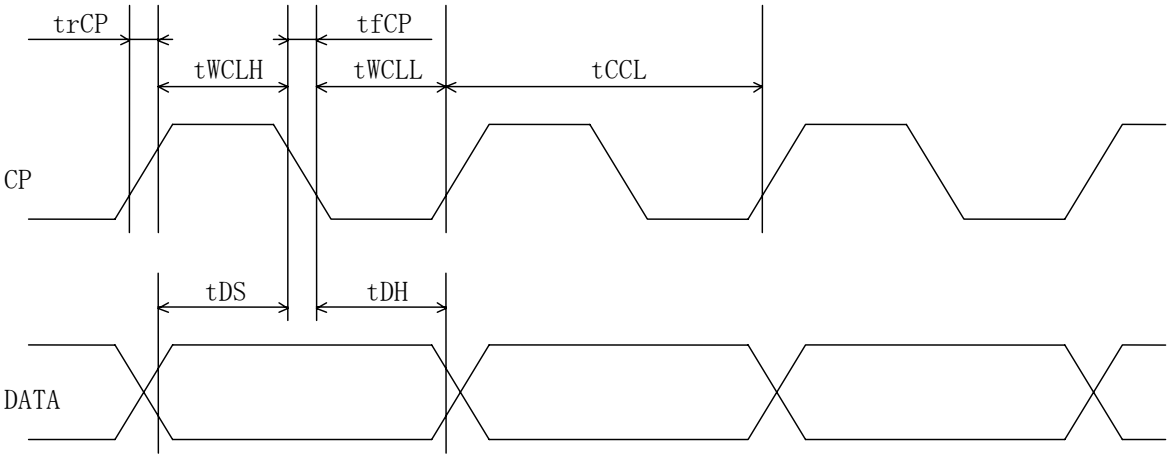


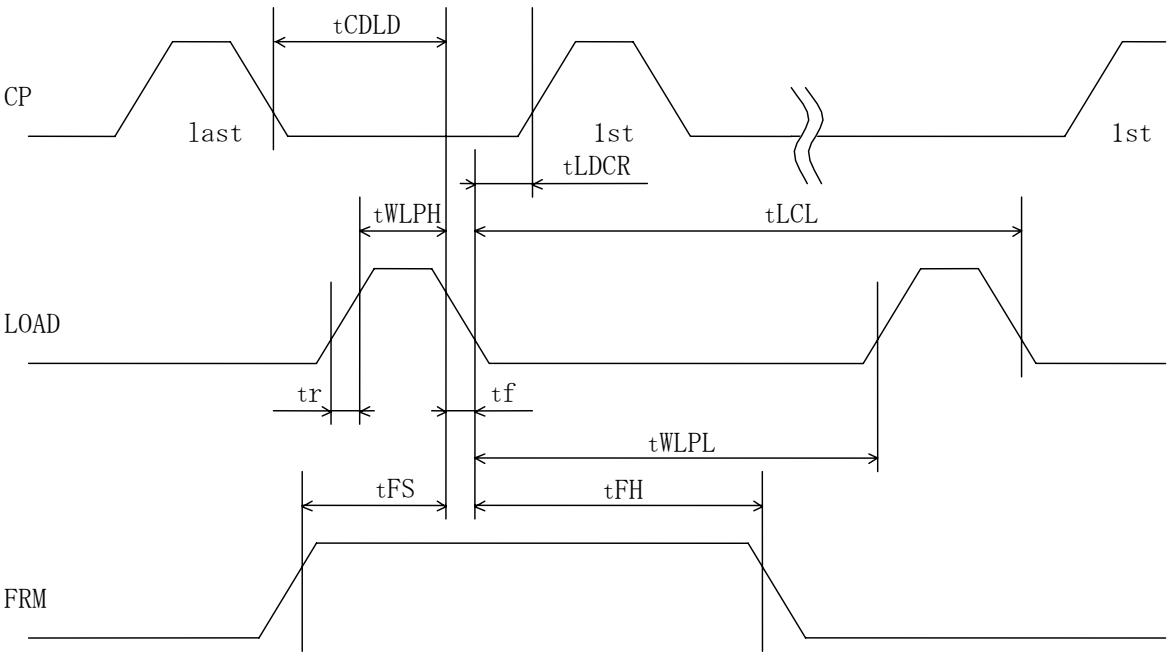
* The cycle of the LOAD signal should be stable and continuously applied without interruption.
* The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.

10. Data and screen

	Y1			Y2			Y3			···	Y320		
X1	D7 R1	D6 G1	D5 B1	D4 R2	D3 G2	D2 B2	D1 R3	D0 G3	D7 B3	···	D2 R320	D1 G320	D0 B320
X240													

11. Input timing characteristics





11-1. Switching characteristics (V_{DD} = 5.0V)

Input characteristics : V_{DD} = +5.0V ± 5%, Temp. = 0～60°C

Item		Symbol	Min.	Max.	Unit
CP cycle	*1	tCCL	100	-	ns
CP "H" pulse width		tWCLH	30	-	ns
CP "L" pulse width		tWCLL	30	-	ns
CP rise up time		trCP	-	15	ns
CP fall down time		tfCP	-	15	ns
Data set up time		tDS	25	-	ns
Data hold time		tDH	25	-	ns
LOAD "H" pulse width		tWLPH	40	-	ns
LOAD "L" pulse width		tWLPL	400	-	ns
LOAD cycle	*2	tLCL	500	-	ns
CP → LOAD delay time		tCDLD	60	-	ns
LOAD → CP delay time		tLDCR	60	-	ns
Input signal rise up time		tr	-	20	ns
Input signal fall down time		tf	-	20	ns
FRM data set up time		tFS	120	-	ns
FRM data hold time		tFH	30	-	ns

*1 CP cycle is adjusted so that FRM signal is 75Hz.

*2 LOAD cycle is constant.

11-2. Switching characteristics (V_{DD} = 3.3V)

Input characteristics : V_{DD} = +3.3V ± 0.3V, Temp. = 0～60°C

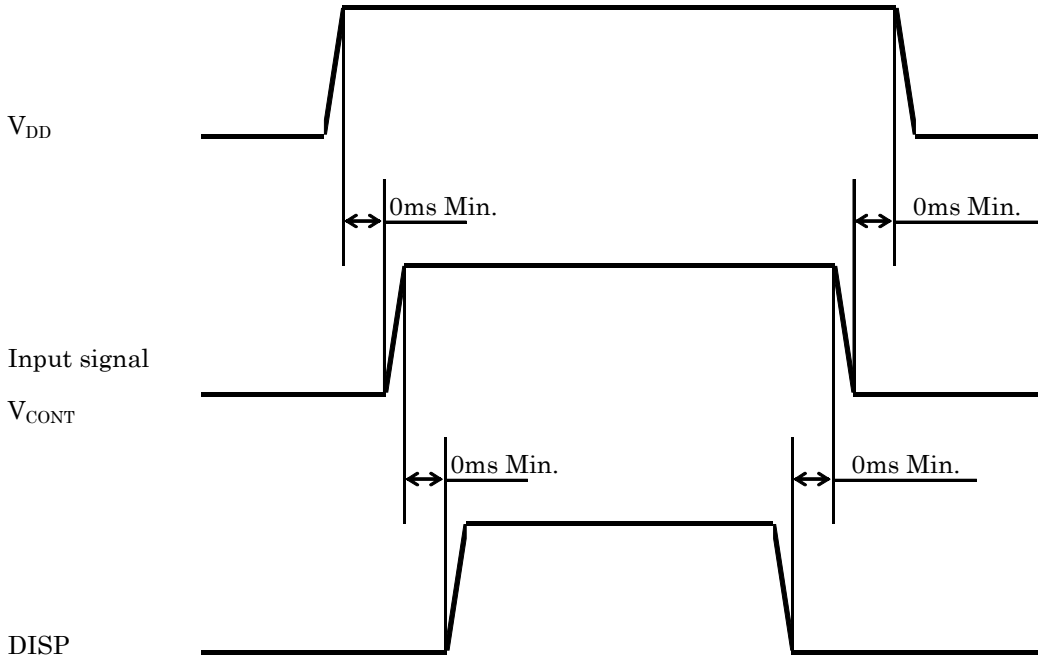
Item		Symbol	Min.	Max.	Unit
CP cycle	*1	tCCL	100	-	ns
CP "H" pulse width		tWCLH	40	-	ns
CP "L" pulse width		tWCLL	40	-	ns
CP rise up time		trCP	-	20	ns
CP fall down time		tfCP	-	20	ns
Data set up time		tDS	35	-	ns
Data hold time		tDH	35	-	ns
LOAD "H" pulse width		tWLPH	50	-	ns
LOAD "L" pulse width		tWLPL	400	-	ns
LOAD cycle	*2	tLCL	500	-	ns
CP → LOAD delay time		tCDLD	60	-	ns
LOAD → CP delay time		tLDCR	80	-	ns
Input signal rise up time		tr	-	20	ns
Input signal fall down time		tf	-	20	ns
FRM data set up time		tFS	120	-	ns
FRM data hold time		tFH	30	-	ns

*1 CP cycle is adjusted so that FRM signal is 75Hz.

*2 LOAD cycle is constant.

12. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on V_{CONT} outside of the time when the V_{DD} logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



* Input signal: FRM, LOAD, CP, D0～D7

* The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.

* Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.

13. Backlight characteristics

CFL ratings

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Starting discharge voltage *1	VS	-	-	1,550	Vrms	0°C
		-	-	1,035	Vrms	25°C
Discharge tube current *2	L	3.0	4.0	6.0	mArms	-
Discharge tube voltage	VL	-	685	-	Vrms	-
Operating life time *3	T	60,000	75,000	-	h	IL=4.0mArms
Operating frequency	F	30	-	100	kHz	-

*1 The non-load output voltage (VS) of the inverter should be at least 1.3 times the maximum VS at low temperatures to provide enough margins to assure that the CFL will start, because actual VS may increase due to leakage current from the CFL cables. (Reference value: 2,015 Vrms Min.)

*2 we recommend that you should set the discharging tube current lower than the typical value to prevent the accumulated heat of the CFL tube from deteriorating the performance of the LCD.

*3 Defined as when the luminance or quantity of light has decreased to 50% of the initial value. The average life of a CFL will decrease when the LCD is operating at lower temperatures.

* Prolonged storage in darkness and/or at low temperature may slow the ignition and rise to full brightness of the CFL. Please use an inverter designed to provide sufficient starting voltage for more than 1 second. Also a decreased starting discharge voltage or shortened ignition time may not turn ON the CFL lamp.

14. Design guidance for analog touch panel (T/P)

14-1 Electrical (In customer's design, please remember the following considerations.)

1 Do not use the current regulated circuit.

2 Keep the current limit with top and bottom layer. (Please refer to “Electrical absolute maxim um ratings” for details.)

3 Analog T/P can not sense two points touching separately.

4 A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.

5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

14-2 Software

1 Do the "User Calibration".

2 "User Caribration" may be needed with long term using. Include "User Caribration" menu in your software.

3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

14-3 Mounting on display and housing bezel

1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.

2 Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.

3 If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.

15. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

KCG057QV1DC-G770 - □□ - □□ - □ MADE IN □□□□□

↓↓ ↓ ↓ ↓

1 2 3 4 5

No1. - No5. above indicate
1. Year code
2. Month code
3. Date
4. Version Number
5. Country of origin (Japan or China)

Year	2007	2008	2009	2010	2011	2012
Code	7	8	9	0	1	2

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

16. Warranty

16-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

16-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

17. Precautions for use

17-1. Installation of the LCD

1) The LCD module has a grounding hole. Please ground the module to prevent noise and to stabilize its performance as circumstances demand.
2) The LCD shall be installed so that there is no pressure on the LSI chips.
3) The LCD shall be installed flat, without twisting or bending.
4) The display window size should be the same as the effective viewing area.
5) Please refer to the following our recommendable value of Clamp-down torque when installing.
   Clamp-down torque : 0.32±0.03N・m
   Please set up 'SPEED-LOW', 'SOFT START-SLOW' when using electric driver.
   Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 3.5±0.5mm
   Please be careful not to use high torque which may damage LCD module in installation.

17-2. Static electricity

1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
2) Workers should use body grounding. Operator should wear ground straps.

17-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2) Adjust the "Supply voltage for LCD driving (V_{CONT})" to obtain optimum viewing angle and contrast ratio.

17-4. Storage

1) The LCD shall be stored within the temperature and humidity limits specified.
   Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
2) Always store the LCD so that it is free from external pressure onto it.

17-5. Usage

1) DO NOT store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
6) Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.
7) Do not disassemble LCD module because it will result in damage.
8) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
10) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

18. Reliability test data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	70°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Low temp. atmosphere	-20°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Temp. cycle	-20°C 0.5h R.T. 0.5h 70°C 0.5h	10cycles	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. operation	60°C	500h	Display function : No defect Display quality : No defect Current consumption : No defect
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance : No defect Insulation resistance : No defect Linearity : No defect Actuation Force : No defect

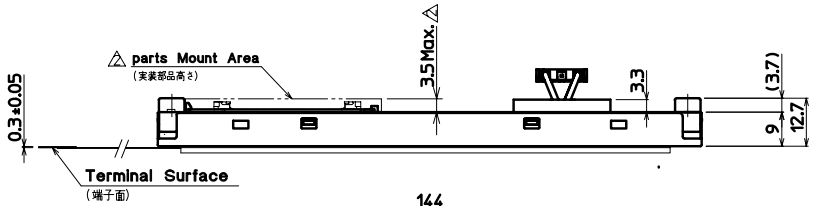
* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

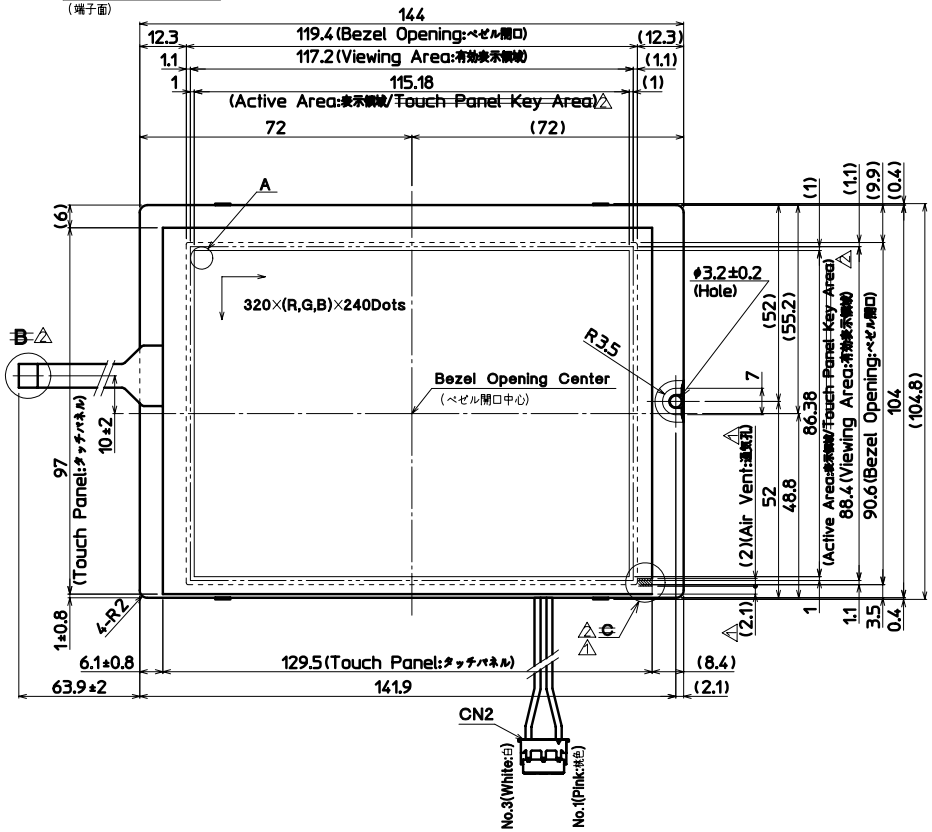
* The LCD is tested in circumstances in which there is no condensation.

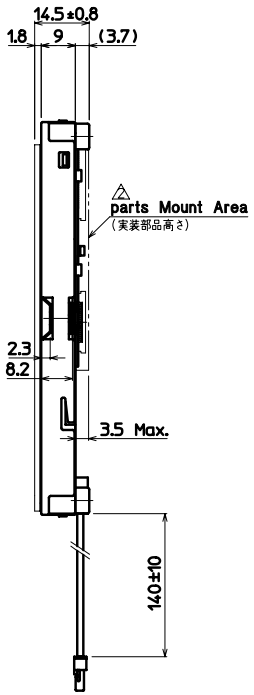
* The reliability test is not an out-going inspection.

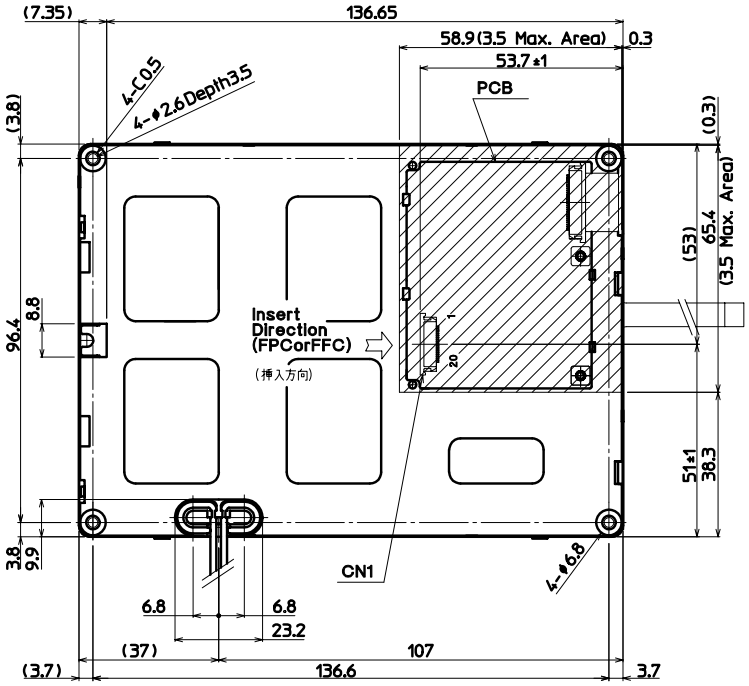
* The result of the reliability test is for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.

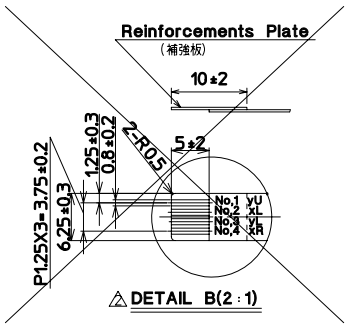
No	Description	Drawn	Checked	Checked	Approved
△1	·Add Air Vent position (DETAIL C) (Air Vent位置，詳細図追加)	'02.12.04 倉元	'02.12.05 今村	'02.12.05 朝倉	'02.12.05 竹本
△2	·Delete T.P. Key Area (タッチパネルキーエリア削除) ·Delete DETAIL B,C (DETAIL B,C削除) ·Delete T.P.Pin-assign (T.P.ピンアサイン図削除) ·Add dimension(寸法追記) ·Add Japanese comment (日本語併記)	'04.12.09 堀	'04.12.15 倉元		'04.12.15 阿部
△3	·Change Connector name (コネクター変更:鉛フリー品へ) ·Add Matching Connector(LEAD FREE) (適合コネクター追記:鉛フリー品)	'05.03.10 堀	'05.03.14 倉元		'05.03.14 阿部

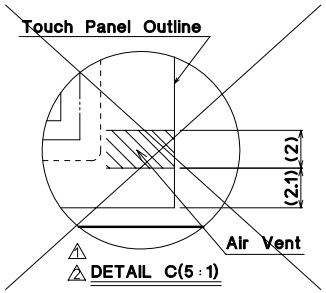


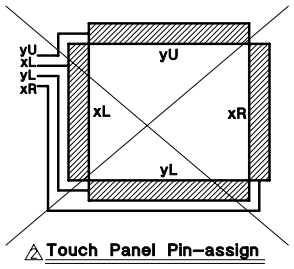


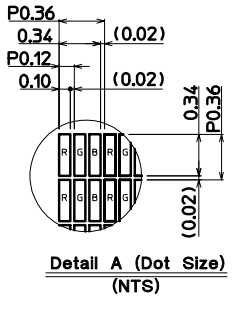












Note.(注記) ~~08-6210-020-340-800△~~

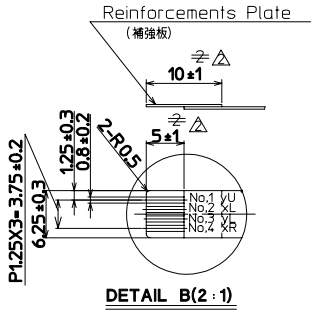
1. Connector CN1:08-6210-020-340-800+ (ELCO)
   (コネクター) CN2:BHR-03VS-1 (JST)
2. Matching Connector CN2:SM02(8.0)B-BHS-1 (JST)
   (適合コネクター) △SM02(8.0)B-BHS-1-TB(LF)(SN) (JST)
3. Recommended Matching Connecter(Touch Panel)
   (タッチパネルインターフェース推奨コネクター)
   :Series 6216 (ELCO) :Series FE,FFS (JST)
   :Series SFD (FCI)
4. The drawing is the front view of the LCD. Information is displayed starting at the upper left hand corner, moving right then down to the lower right hand corner.
   (LCDの正面図において、画像データの表示は左上コーナーから始まり、右へ進み下へ送られ右下コーナーへ向かう。)
5. Tolerance without indication : ±0.5
   (指示無き公差)

Material 材質	Treatment 処理	Approved	Checked	Checked	Drawn	Scale	Title		Year-Month-Day	Size
		'02.10.09 竹本	'02.10.08 朝倉	'02.10.08 阿部	今村	1:1(2:1,NTS)	KCG057QV1DC	KYOCERA	'02.10.08	2
Quantity 製作数	Description; 備考 RoHS 02006AAA						Outline Dimensions	Drawing No.	121A5019400-3	1/2

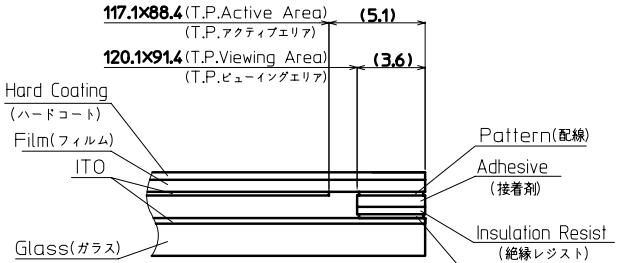
No	Description	Drawn	Checked	Checked	Approved
⚠1	1/2 Rev.up	'02.12.04 倉元	'02.12.05 阿部	'02.12.05 朝倉	'02.12.05 竹本
⚠2	・Add T.P. Outline Dimensions (タッチパネル外形図追加) ・Change Tolerance Reinforcements Plate ±2→±1 (補強板公差変更 ±2→±1) ・Change Tolerance Terminal length ±2→±1 (端子長さ公差変更 ±2→±1)	'04.12.09 堀	'04.12.15 倉元		'04.12.15 阿部
⚠3	1/2 Rev.up	'05.03.10 堀	'05.03.14 倉元		'05.03.14 阿部

Note(注記)

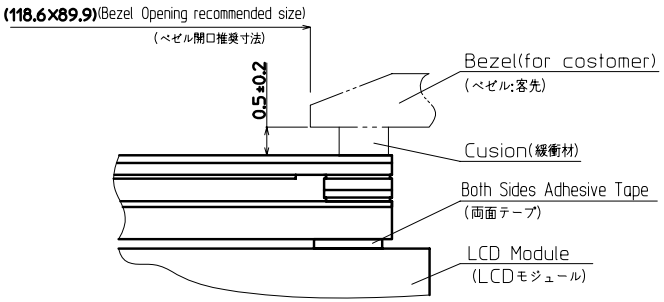
	Name(名称)	Explanation(説明)
1	T.P.	Touch panel (タッチパネル)
2	T.P. Active Area (T.P.アクティブエリア)	Operating area of touch panel (タッチパネルの動作範囲)
3	T.P. Viewing Area (T.P.ビューイングエリア)	Warranty area of touch panel's appearance (タッチパネルの外観(傷・異物等)保証範囲)
		By giving pressure between the active area and the effective viewing area of the touch panel, there is a possibility that the touch panel will operate. (タッチパネルアクティブエリアとタッチパネルビューイングエリア間は荷重をかけた場合は、タッチパネルが動作する可能性があります。)

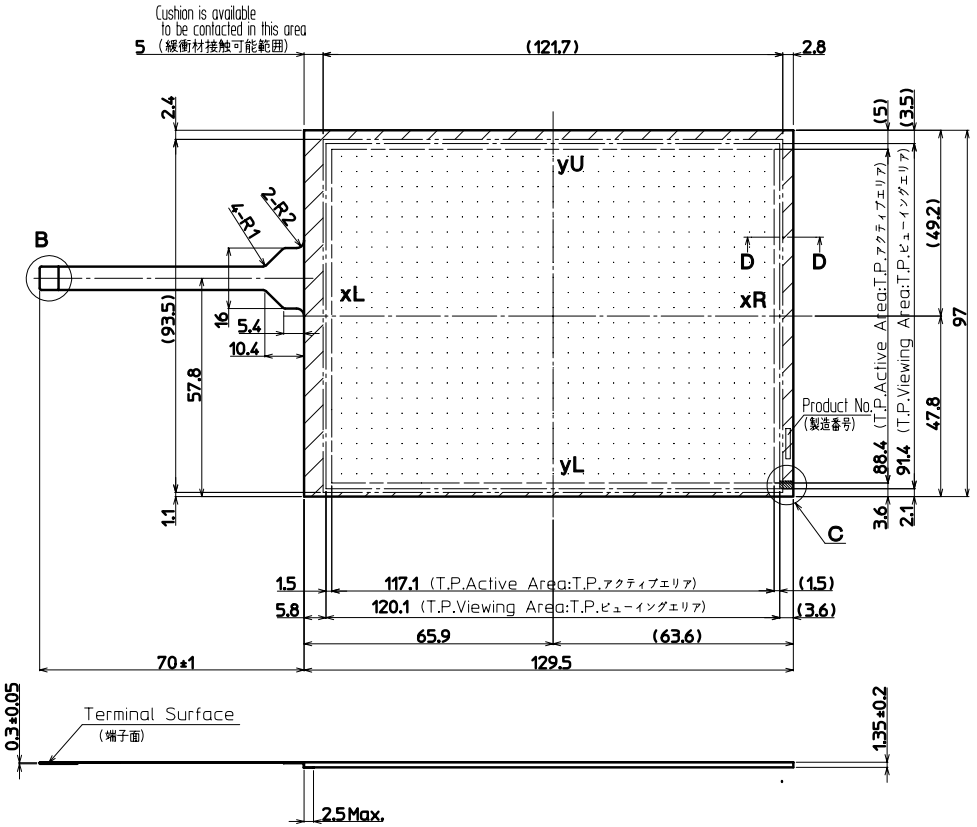


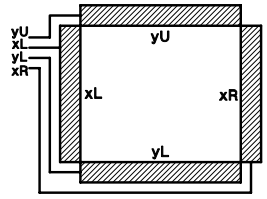
DETAIL B(2:1)



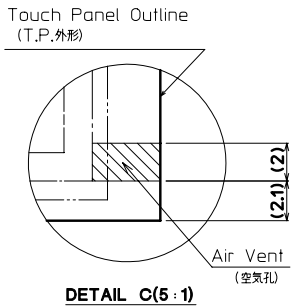
SECTION D–D(NTS)







Touch Panel Pin–assign
(タッチパネル ピンアサイン)



DETAIL C(5:1)

Precaution in use of touch panel.
(タッチパネル使用上の注意事項)

1.Fix touch panel at LCD module and the rear side of touch panel.
(タッチパネルの固定はLCDモジュール側とタッチパネル裏面とで行なうこと。)

2.Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.
(ベゼル内側とタッチパネルの接触厳禁。誤動作や電極破損の原因となります。)

3. Please inform us beforehand when you design window of your housing with the different size from recommended size.
(ベゼル開口を推奨寸法以外で製作される場合は必ず前もって相談願います。)

Precaution in use of touch panel.
(タッチパネル使用上の注意事項)

There is vent channel to equalize air pressure between the inner space of the touch panel and the atmosphere.

Please make sure it is not blocked by your housing and mounting method.

(タッチパネルの中には内圧と外圧を均一にするため通気孔を設けています。
取り付け時にこの通気孔を塞がないようにしてください。)

Material 材質	Treatment 処理	Approved	Checked	Checked	Drawn	Scale	Title		Year–Month–Day	Size
Quantity 製作数	Description; 備考 02006AAA	'02.10.09 竹本	'02.10.08 朝倉	'02.10.08 阿部	今村	1:1(2:1,5:1,NTS)	KCG057QV1DC T.P. Outline Dimensions	KYOCERA	'02.10.08	2
								Drawing No.	121A5019400–3	2/2

Spec No.	TQ3C 8EAC0 E2DDV13 00
Date	February 26, 2007

KYOCERA INSPECTION STANDARD

TYPE : KCG057QV1DC-G770

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved
February 26, 2007	Y. Asano	Y. Yamazoki	M. Fujitani	S. Higashi	

Revision record

Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved

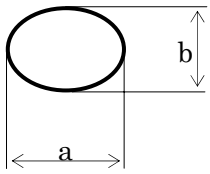
Rev.No.	Date	Page	Descriptions

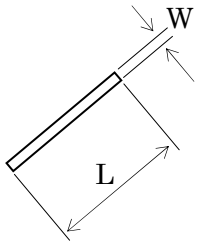
Visuals specification

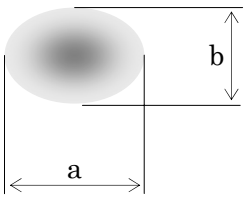
1) Note

Item	Note	
General	1.When defects specified in this Inspection Standards are inspected, operating voltage (V_{OP}) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-level INSPECTION) 2.This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area. 3.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera. 4.Inspection conditions Luminance : 500 Lux minimum. Inspection distance : 300 mm(from the sample) Temperature : 25±5°C Direction : right above	
Definition of inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.
	Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.

2)Standard

<table>
<tr><th>Inspection item</th><th>Judgement standard</th></tr>
<tr><td>Pinhole, Bright spot,
Black spot, Foreign particle</td><td>


d = (a + b) / 2
<table>
<tr><th>Category</th><th>Size(mm)</th><th>Acceptable number</th></tr>
<tr><td>A</td><td>d ≦ 0.2</td><td>Neglected</td></tr>
<tr><td>B</td><td>0.2 ＜ d ≦ 0.3</td><td>5</td></tr>
<tr><td>C</td><td>0.3 ＜ d ≦ 0.5</td><td>3</td></tr>
<tr><td>D</td><td>0.5 ＜ d</td><td>0</td></tr>
</table>
</td></tr>
<tr><td>Scratch, Foreign particle</td><td>


<table>
<tr><th></th><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable number</th></tr>
<tr><td>A</td><td>W ≦0.03</td><td>-</td><td>Neglected</td></tr>
<tr><td>B</td><td rowspan="3">0.03＜ W ≦0.10</td><td>L ≦2.0</td><td>Neglected</td></tr>
<tr><td>C</td><td>2.0＜ L ≦4.0</td><td>3</td></tr>
<tr><td>D</td><td>4.0＜ L</td><td>0</td></tr>
<tr><td>E</td><td>0.10＜ W</td><td>-</td><td>According to ‘Circular’</td></tr>
</table>
</td></tr>
<tr><td>Contrast variation</td><td>


d = (a + b) / 2
<table>
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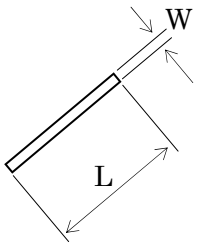
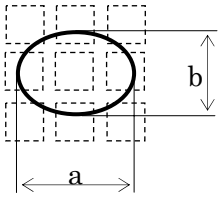
<tr><td>A</td><td>d ≦ 0.5</td><td>Neglected</td></tr>
<tr><td>B</td><td>0.5 ＜ d ≦ 0.7</td><td>3</td></tr>
<tr><td>C</td><td>0.7 ＜ d</td><td>0</td></tr>
</table>
</td></tr>
</table>

Inspection item	Judgement standard
Polarizer (Scratch, Bubble, Dent)	(1) Scratch



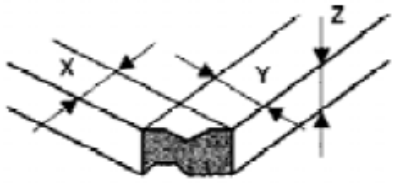
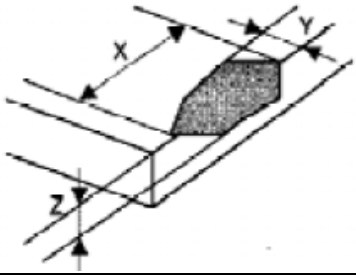
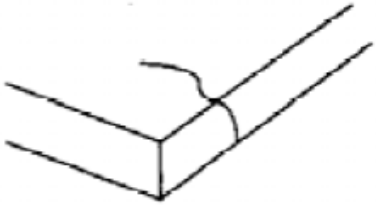
	Width (mm)	Length (mm)	Acceptable No.
A	W ≦0.1	-	Neglected
B	0.1< W ≦0.3	L ≦5.0	Neglected
C		5.0< L	0
D	0.3< W	-	0

(2) Bubble (dent)



d = (a + b) / 2

Category	Size (mm)	Acceptable number
A	d ≦ 0.2	Neglected
B	0.2 < d ≦ 0.3	5
C	0.3 < d ≦ 0.5	3
D	0.5 < d	0

<table>
<tr><th>Inspection item</th><th>Judgement standard</th></tr>
<tr><td>Scratch,
Foreign particle
(Touch screen portion)</td><td>(W = Width, L = Length, D = Diameter = (major axis＋minor axis)/ 2)
<table>
<tr><th>Item</th><th>Width(mm)</th><th>Length(mm)</th><th>Acceptable number</th></tr>
<tr><td rowspan="4">Scratch</td><td>d ≦ 0.03</td><td>L ≦ 20</td><td>Neglected</td></tr>
<tr><td>0.03 ＜ d ≦ 0.05</td><td>L ≦ 10</td><td>2pces within φ20mm</td></tr>
<tr><td>0.05 ＜ d ≦ 0.08</td><td>L ≦ 6</td><td>2pces within φ20mm</td></tr>
<tr><td>0.08 ＜ d ≦ 0.1</td><td>L ≦ 4</td><td>1pces within φ30mm</td></tr>
<tr><td rowspan="2">Foreign
(line like)</td><td>W ≦ 0.05</td><td>Neglected</td><td>Neglected</td></tr>
<tr><td>0.05 ＜ W ≦ 0.1</td><td>L ≦ 5</td><td>2pces within ϕ30mm</td></tr>
<tr><td rowspan="2">Foreign
(circle like)</td><td colspan="2">D ≦ 0.2</td><td>Neglected</td></tr>
<tr><td colspan="2">0.2 ＜ D ≦ 0.3</td><td>2pces within ϕ30mm</td></tr>
</table>
Above are applied to the visible area.
Unless there are foreign particle and damage affected seriously to the electrical performance in the visible area, we approve of this product.</td></tr>
<tr><td>Glass crack
(Touch screen portion)</td><td>
<table>
<tr><th>Item</th><th colspan="3">Size (mm)</th><th>Acceptable number</th></tr>
<tr><td>Conner crack</td><td rowspan="3"> 
</td><td>X</td><td>≦3</td><td rowspan="3">2 pcs
/panel</td></tr>
<tr><td>Y</td><td>≧3</td></tr>
<tr><td>Z</td><td>＜t</td></tr>
<tr><td>Crack in other area than in conner</td><td rowspan="3"> 
</td><td>X</td><td>≦5</td><td rowspan="3">2 pcs
/side</td></tr>
<tr><td>Y</td><td>≦1.5</td></tr>
<tr><td>Z</td><td>＜t</td></tr>
<tr><td>Progressive crack</td><td colspan="3">  </td><td>0 pcs
(NG even 1pcs)</td></tr>
</table>
Above are applied to the visible area.
Unless there are foreign particle and damage affected seriously to the electrical performance in the visible area, we approve of this product.</td></tr>
</table>