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SPEC. NO.	TQ3C-8EACO-E1CYE09-01
DATE	November 1, 2004

SPEC

FOR		
LOK	•	

TYPE: KCS077VG2EA-G43

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Issued
Date: NOV. 05.2004

KYUCERA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering

Original	Designed	by :Engineer	ing Dept.	Confirmed b	y :QA Dept.
Issue Data	Prepared	Checked	Approved	Checked	Approved
August 2.2002	T. Onodera	M. Fujitani	H. Ohno	J. Joshija	4. Matrumora

Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in medical applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such medical applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in medical applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

				Kevision	NCCO1 G			
D-+-		Design	ed by:	Engineering D	ept.	Confirmed by	: QA Dept.	
Date		Prepa	red	Checked	Approved	Checked	Approved	
Nov. 1,	2004	7. Onode	ra	M. FujiTani	H.Ohno	y. Yoslida	4. Matsumoto	
Rev. No.	Date		Page		Descripti			
01	Nov. 1	, 2004	1	2. Constructi ∼Change In	on and Outlin verter "KCI-1	e 3″ → "KCI-15″		
			3	4-2. Environm ∼Add comme	ental absolut nt "*2 (Pleas	e maximum rati e refers to••	ngs	
			4	5. Electrical	characterist high frequenc	ics		
			10	8-2. CFL ~Change SY ~Delete "L	MBOL "HV→HOT EVEL"	″, ″GND→COLD″		
			13	11. Input Tim ∼Change "F	ing Character RM"	istics		
			17	14. Lot Numbe ~Change "Y ~Change "C	r Identificat EAR" "2002~2 ODE" "2~7" -	ion 007" → "2004~ → "4~9"	~2009″	
:			18	18 16-1. Installation of the LCD ~Add comment "8. This Kyocera LCD···." 16-3. LCD Operation ~Change comment "2. Adjust "LCD" driving···. 16-4. Storage ~Change comment "2. Always store the···."				
					·			

1. Application

This data sheet defines the specification for a $(640 \times R.\,G.\,B) \times 480$ dot, STN Transmissive color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

 $(640 \times R. G. B) \times 480$ dots, COB type LCD with CFL backlight.

Backlight system : Side-edge type CFL (2 tubes).

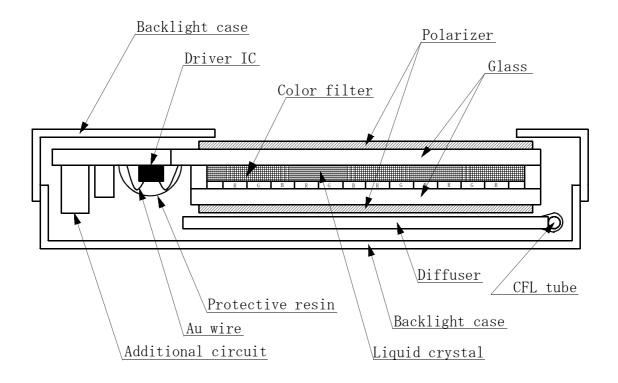
Inverter : Option

Recommended Inverter: KCI-15 (MINEBEA ELECTRONICS)

or equivalent

Polarizer : Glare treatment.

Additional circuit : Bias voltage circuit, Randomizing circuit.



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	199.0 (W) × 147.0 (H) × 10.0 (D)	mm
Effective viewing area	160.42 (W) × 121.06 (H)	mm
Dot number	(640×R. G. B) (W) × 480 (H)	Dots
Dot size	$0.062 \text{ (W)} \times 0.226 \text{ (H)}$	mm
Dot pitch	0.082 (W) \times 0.246 (H)	mm
Display color *1	White *2	_
Base color *1	Black *2	_
Mass	330	g

^{*1} Due to the characteristics of the LC material, the color vary with environmental temperature.

*2 Negative-type display

Display data "H" :R.G.B Dots ON : White Display data "L" :R.G.B Dots OFF : Black

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	7. 0	V
Supply voltage for LCD driving	VEE	0	36. 0	V
Input signal voltage *1	Vin	0	VDD	V

^{*1} Input signal : CP, LOAD, FRM, DISP, HD0 \sim HD7, LD0 \sim LD7

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	MIN	MAX	UNIT
Operating temperature	*1	Тор	0	50	$^{\circ}\!$
Storage temperature	*2	Тѕто	-20	60	$^{\circ}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Н _{ято}	10	*4	%RH
Vibration		_	*5	*5	_
Shock		_	*6	*6	_

- *1 LCD's display quality shall not be guaranteed at the temperature range of : below $0^{\circ}\!\!\mathrm{C}$ and upper $40^{\circ}\!\!\mathrm{C}$
- *2 Temp. = -20° C < 48 h , Temp = 60° C < 168 h

Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock.

LCD panel that is kept at low or high temperature for a long time can be defective due to the other condition, even if the temperature satisfies standard.

(Please refers to 16. Precautions for use.)

- *3 Non-condensation.
- *4 Temp. $\leq 40^{\circ}$ C, 85% RH Max.

Temp. $> 40^{\circ}$ C, Absolute Humidity shall be less than 85%RH at 40° C.

*5

Frequency	10∼55 Hz	Converted to	
Vibration width	0.15 mm	$(0.3 \sim 9 \text{ m/s}^2)$	
Interval	10-55-10	O Hz 1 minute	

2 hours in each direction $\rm\,X/Y/Z$ (6 hours as total) EIAJ ED-2531.

*6 Acceleration: 490 m/s^2 Pulse width: 11 ms

3 times in each direction : $\pm X/\pm Y/\pm Z$.

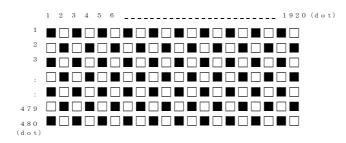
EIAJ ED-2531.

5. Electrical Characteristics

	VDD =	+5.0V	\pm	5%,	Temp.	= 0~50°C
--	-------	-------	-------	-----	-------	----------

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	_	4. 75	5. 00	5. 25	V
Supply voltage for LCD driving	VEE	_	32. 0	34. 0	36.0	V
	Von -	0 ℃	26. 1	27. 1	28. 1	V
LCD driving voltage *1	Vop =	25 ℃	25. 1	26. 1	27. 1	V
	VCONT	40 ℃	24. 5	25. 5	26. 5	V
Input veltere	Vin	"H" level	0.8VDD	_	VDD	V
Input voltage	Vin	"L" level	0		0. 2VDD	V
Clock frequency	f cp	_	4.03	4. 32	16.0	MHz
Frame frequency *2	f frm	_	70	75	_	Hz
Current consumption for logic	IDD		_	3. 2	4.8	mA
Current consumption for LCD driving	IEE	*3		11.5	17. 3	mA
Power consumption	Pdisp		_	316	493	mW

- *1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (Vop= VCONT) for driving LCD.
- *2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.
- *3 Display high frequency pattern, (see below). VDD = 5.0V , Vop = VCONT , f $_{\rm FRM}$ = 75 Hz , fcp = 4.32MHz, Temp. = 25°C Pattern:



6. Optical Characteristics

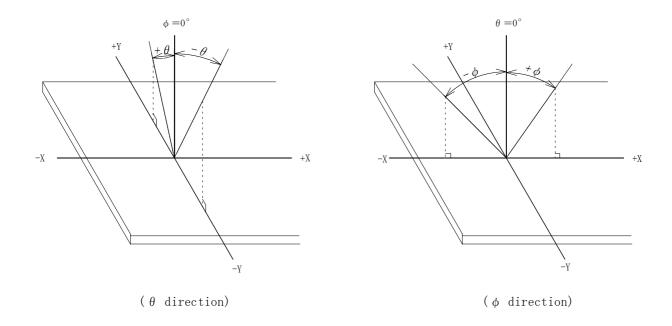
Temp. = 25° C, Measuring spot = ϕ 6mm

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Response	Rise	Tr	$\theta = \phi = 0^{\circ}$		_	160	260	ms
time	Down	Td	$\theta = \phi$	=0°	_	140	240	ms
Viewing angle range		θ	CD > 0	φ =0°	-30	_	40	deg.
		φ	CR≧2	$\theta = 0^{\circ}$	-50	-	50	deg.
Contrast ratio		CR	$\theta = \phi$	=0°	15	25	_	_
Brightness		L	IL=5.0mA × 2		150	200	_	cd/m^2
	Red	X	$\theta = \phi = 0^{\circ}$		0. 51	0. 56	0.61	
	rea	у	θ – φ	-U	0. 29	0. 34	0.39	
	Green	X	$\theta = \phi = 0^{\circ}$		0. 25	0.30	0.35	
Chromaticity coordinates	Green	у			0.49	0. 54	0. 59	
	Blue	X	$\theta = \phi = 0^{\circ}$		0. 11	0.16	0.21	
	blue	у			0. 11	0.16	0.21	_
	WIL: 4	X	0 . 00	-0°	0. 27	0.32	0.37	
	White	у	$\theta = \phi = 0^{\circ}$		0.31	0. 36	0.41	
	Black y	X	$\theta = \phi$	-0°	0. 24	0. 29	0.34	
		у	σ – φ	-U	0. 24	0. 29	0.34	

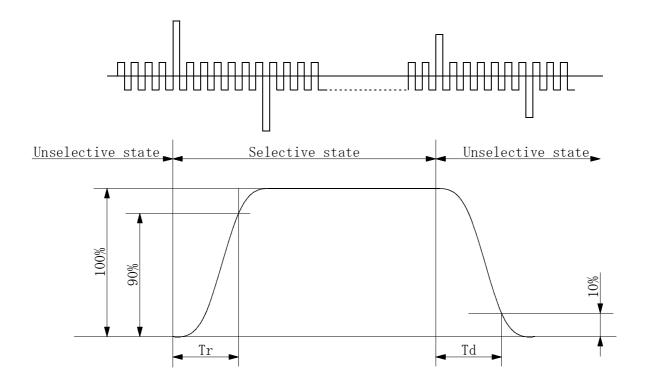
Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $\theta = \phi = 0^{\circ}$.

6-1. Contrast ratio is defined as follows:

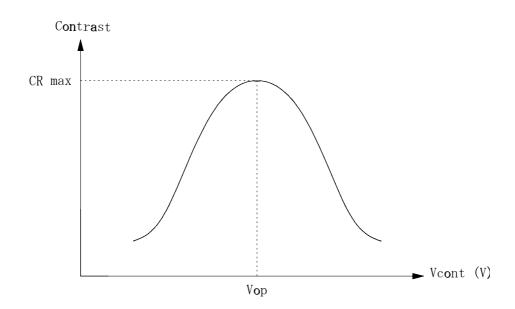
6-2. Definition of viewing angle



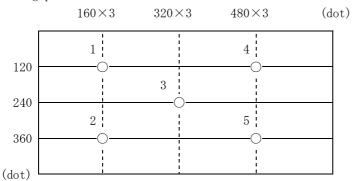
6-3. Definition of response time



6-4. Definition of Vop

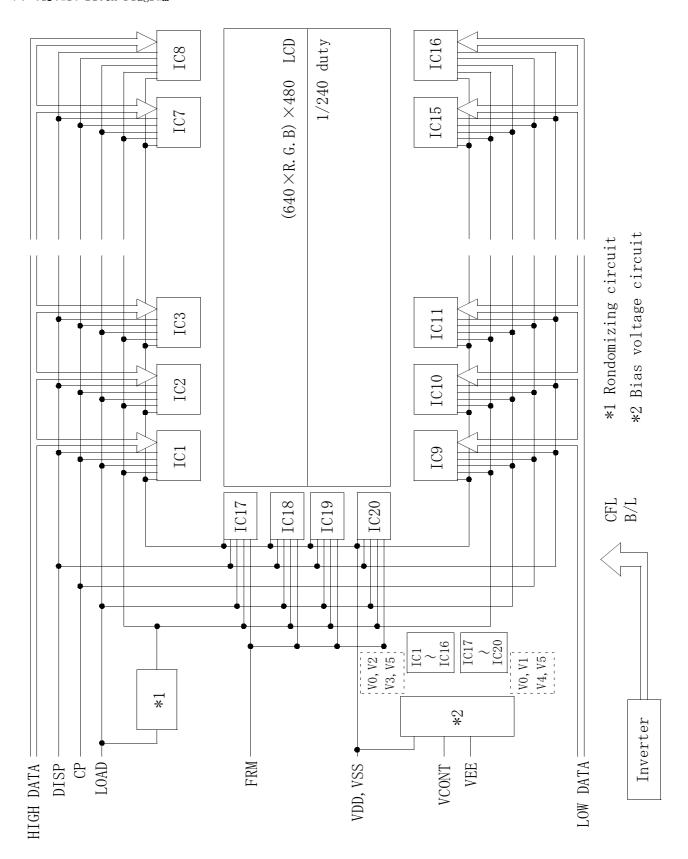


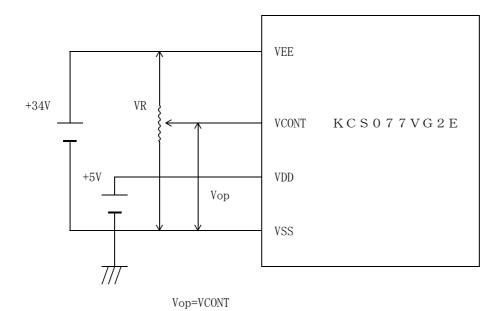
6-5. Measuring points



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp. =25°C)
- 3) The inverter should meet the eccentric conditions;-Sine, symmetric waveform without spike in positive and negative.
- 4) Measuring inverter : KCI-15 (MINEBEA ELECTRONICS)

7. Circuit Block Diagram





8. Interface Signals

8-1. LCD

CN1 : 53261-1510 (Molex)

	olo (molex)		
PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	FRM	Synchronous signal for driving scanning line	Н
2	NC	No connect	_
3	DISP	Display control signal	H(ON), L(OFF)
4	LOAD	Data signal latch clock	$H \rightarrow \Gamma$
5	VSS	GND	_
6	CP	Data signal shift clock	$H \rightarrow \Gamma$
7	VSS	GND	_
8	HD0		H(ON), L(OFF)
9	HD1		
10	HD2		
11	HD3	Display data (Upper column)	
12	HD4		
13	HD5		
14	HD6		
15	HD7		

Recommended matching connector : 51021-1500(Molex)

CN2 : 53261-1410 (Molex)

PIN No	SYMBOL	DESCRIPTION	LEVEL
1	LD0		H(ON), L(OFF)
2	LD1		
3	LD2		
4	LD3	Display data (Lower column)	
5	LD4		
6	LD5		
7	LD6		
8	LD7		
9	VDD	Power supply for logic	_
10	VSS	GND	_
11	NC	No connect	_
12	VEE	Power supply for LCD	_
13	VEE	Power supply for LCD	_
14	VCONT	LCD adjust voltage	_

Recommended matching connector : 51021-1400 (Molex)

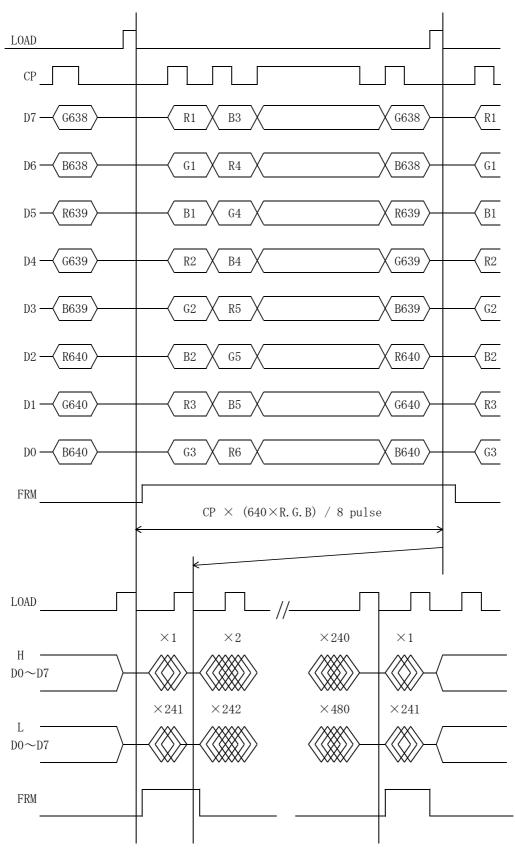
8-2. CFL

CN3 : QZ-19-3F01 (HONDA) \times 2

PIN NO.	SYMBOL	DESCRIPTION
1	COLD	Inverter output low voltage side
2	NC	_
3	НОТ	Inverter output high voltage side

Recommended matching connector $\,$: QZ-19-A3MYL#02 (HONDA) $\,\times\,$ 2

9. Interface Timing Chart

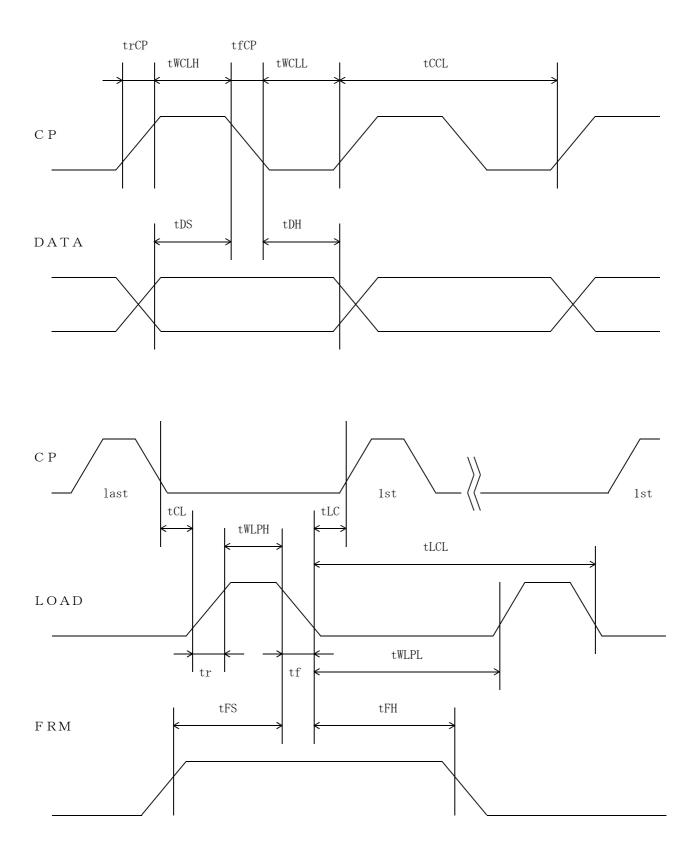


- * The cycle of load signal should be stable and continuously applied without interruption.
- * The above-mentioned timing chart shows a reference to set up a LCD module, not an electrical rating.

10. Data and Screen

			Y1			Y2			Y 3		•••		Y640	
; [X1	HD7 R1	HD6 G1	HD5 B1	HD4 R2	HD3 G2	HD2 B2	HD1 R3	HD0 G3	HD7 B3		HD2 R640	HD1 G640	HD0 B640
•			1									1	1	
	240 241													
	X480	LD7 R1	LD6 G1	LD5 B1	LD4 R2	LD3 G2	LD2 B2	LD1 R3	LD0 G3	LD7 B3		LD2 R640	LD1 G640	LD0 B640

1 1. Input Timing Characteristics



11-1. Switching characteristics

Input characteristics ; VDD = +5.0V \pm 5%

Temp. = 25° C

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1, *2	tCCL	62	_	ns
CP "H" Pulse Width *2	tWCLH	25	_	ns
CP "L" Pulse Width *2	tWCLL	25	_	ns
CP Rise Up Time *2	trCP	_	30	ns
CP Fall Down Time *2	tfCP	_	30	ns
Data Set Up Time	tDS	25	_	ns
Data Hold Time	tDH	20	_	ns
Load "H" Pulse Width	tWLPH	50	_	ns
Load "L" Pulse Width	tWLPL	370	_	ns
LOAD Cycle *3	tLCL	420	_	ns
CP → LOAD Delay Time	tCL	0	_	ns
LOAD → CP Delay Time *4	tLC	120-tWLPH	_	ns
Input Signal Rise Up Time	tr	_	30	ns
Input Signal Fall Down Time	tf	_	30	ns
FRM Data Set Up Time	tFS	100	_	ns
FRM Data Hold Time	tFH	30	_	ns

 $^{*1\ \}mbox{CP}$ Cycle is adjust so that FRM signal is 75Hz.

①
$$trCP + tfCP \le tCCL - (tWCLH + tWCLL)$$

Please use on condition that ①, ② are filled.

^{*2} The formula of condition

② trCP, tfCP \leq 30 ns

^{*3} LOAD cycle is const.

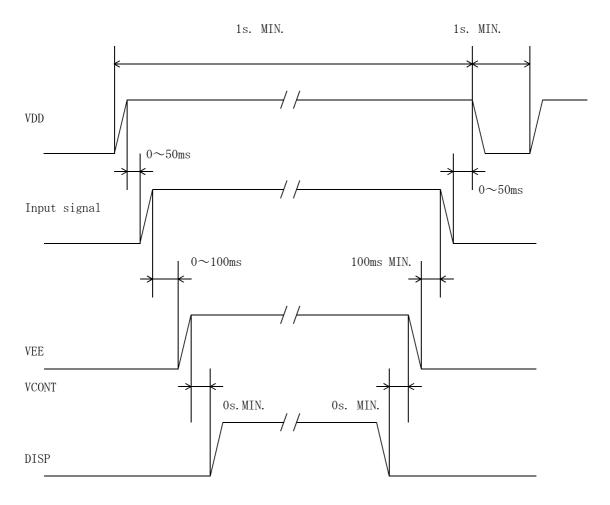
^{*4} tLC \geq 0

12. Supply Voltage Sequence Condition

DO NOT apply DC voltage to the LCD panel.

DC voltage induce irreversible electrochemical reactions and reduce LCD life.

Always follow the power supply ON/OFF sequence of VDD first, input signal second, VCONT third and finally DISP. This will prevent DC driving of the LCD or CMOS LSI latch up as shown below.



- * Input signal : CP, LOAD, FRM, HDO~HD7, LDO~LD7
- * The above sequence should be designed as to keep each normal figure on condition that liquid crystal module is loaded on your system.
- * Control the input signal and VEE to the above ON-OFF timing when you switch ON/OFF the display during VDD and DISP are on. And also design the circuit as VEE's OFF level become GND level.
- * Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

13. Backlight Characteristics

CFL ratings

Temp. = 25° C

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Stanting Discharge Valtage W1	VS	_	_	980 Vrms.	0 ℃
Starting Discharge Voltage *1	VS	1	1	655 Vrms.	25 °C
Discharging tube current *2,*3	IL	2.0 mArms.	5.0 mArms.	6.0 mArms.	_
Discharging tube voltage	VL		400 Vrms.	_	_
Operating life (IL=5.0 mArms.) *4	Т	25,000 h	40,000 h	_	_
Operating frequency	F	40 kHz	_	100 kHz	_

- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value : 1280 Vrms MIN.)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 Do not apply more than 6.0 mA discharging tube current. Because CFL maybe broken due to over current.
- *4 When the illuminance or quantity of light has decreased to 50 % of the initial value.

 Average life time of CFL will be decreased when LCD is operating at lower and higher temperature.

14. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



YEAR	2004	2005	2006	2007	2008	2009
CODE	4	5	6	7	8	9
MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6
MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	X	Y	Z

15. Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

16. Precautions for use

16-1. Installation of the LCD

- 1. Please ground either of the mounting (screw) holes located at each corner of an LCD module, in order to stabilize brightness and display quality.
- 2. A transparent protection plate shall be added to protect the LCD and its polarizers.
- 3. The LCD shall be installed so that there is no pressure on the LSI chips.
- 4. The LCD shall be installed flat, without twisting or bending.
- 5. The display window size should be the same as the effective viewing area.
- 6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 7. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.
- 8. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

 Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

16-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

16-3. LCD Operation

- The LCD shall be operated within the limits specified.
 Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Adjust "LCD driving voltage" to obtain optimum viewing angle and contrast.
- 3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal.
 <u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

16-4. Storage

- 1. The LCD shall be stored within the temperature and humidity limits specified.

 Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

16-5. Screen Surface

- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizers may result.
- 2. The front polarizer is easily scratched or damaged.

 Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer.

 Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.

17. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	70℃	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	−20°C	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90%RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-20°C 0.5 h R. T. 0.5 h 70°C 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50℃ Vop	500 h	Display Quality : No defect Current Consumption : No defect

st Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

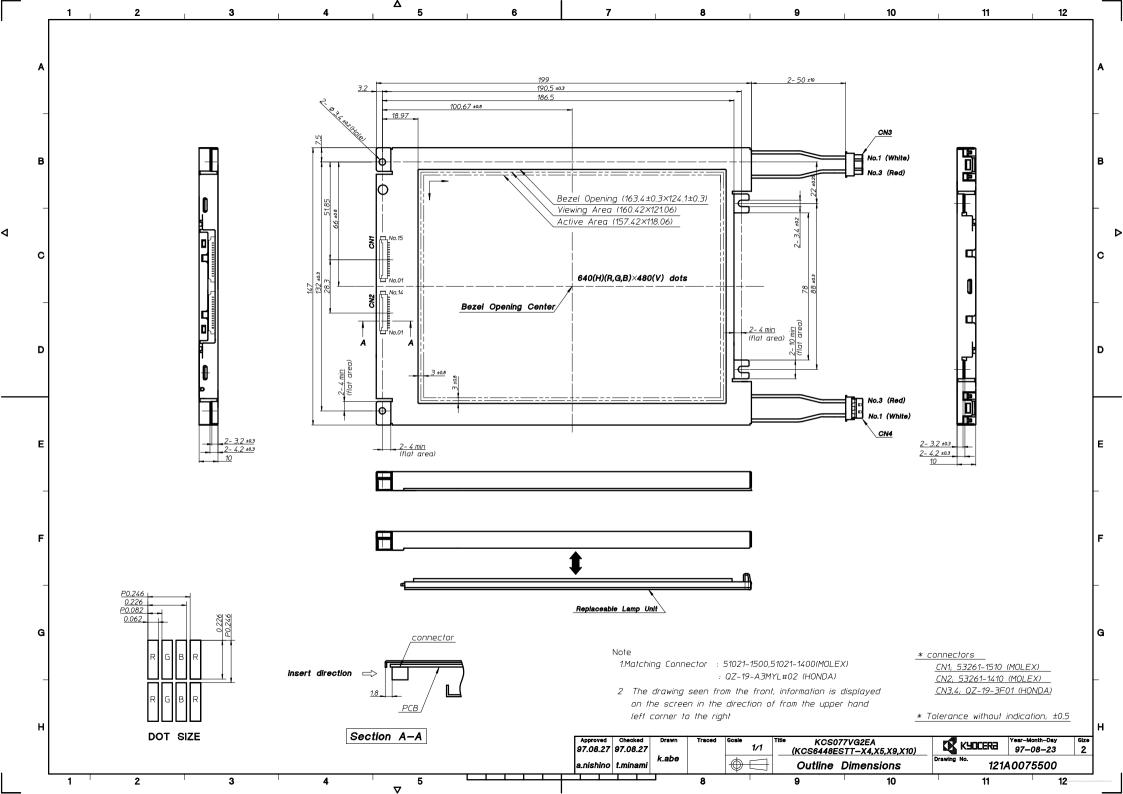
st The LCD is tested in circumstances in which there is no condensation.

^{*} The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

^{*} The reliability test is not an out-going inspection.

^{*} The results of the reliability test are for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.



SPEC.NO.	TQ3C-8EACO-E2CYE10-00
DATE	August 2.2002

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1	V	ΤĻ	٠	

KYOCERA INSPECTION STANDARD

TYPE: KCS077VG2EA-G43

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	ing Dept.	Confirmed b	y :QA Dept.
Issue Data	Prepared	Checked	Approved	Checked	Approved
August 2.2002	2. Midrico	Matsumot	H. OHND	S. Hopski	J. Joshita

Revision Record

roved

Visuals specification

1)Note

Item		Note				
General	1. When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION)					
	2. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area.					
	3. Should any defects which are not specified in this standard happen, additional standard shall be determine by mutual agreement between customer and Kyocera.					
	4. Inspection conditions					
	Luminance : 500 Lux minimum . Inspection distance : 300 mm (from the sample) Temperature : 25 \pm 5 $^{\circ}{\rm C}$ Direction : right above					
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.				
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.				
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.				

2)Standard

	a Size d 0.2 < d 0.3 < d 0.5 < d d 0.5 < d d	$d \leq 0.2$ $d \leq 0.3$ $d \leq 0.5$	neg	b) / 2 le number lected 5 3 0						
A B C D	0. 2 < d 0. 3 < d 0. 5 < d	$d \le 0.2$ $d \le 0.3$ $d \le 0.5$	neg	lected 5						
A B C D	0. 2 < d 0. 3 < d 0. 5 < d	$d \le 0.2$ $d \le 0.3$ $d \le 0.5$	neg	lected 5						
C D Wid	0.2 < d 0.3 < d 0.5 < d	$d \leq 0.3$ $d \leq 0.5$		3						
D	0.3 < d	d ≤ 0.5								
Wid	<		W	0						
	th (mm)	L	W							
	th (mm)	L	W							
	Ith (mm)		L							
		Width (mm) Length (mm) Acceptable No.								
	$W \leq 0.03$	Len	<u></u>	neglected						
		L ≦ 2.0		neglected						
0.03<	$<$ W ≤ 0.1	2.0 <	L ≦ 4.0	3						
		4.0 <	L	0						
0.1 <	< W	-		According to Circular						
	a	Q V	d = (a +							
		<u>a</u>								

Inspection item	Judgement standard							
Polarizer (Scratch, Bubble, Dent)	(1) Scratch W							
	Widt	h (mm)	Len	gth (mm)	Acceptable No.			
	A	W ≦ 0.1			neglected			
	B 0.1 <	< W ≤ 0.3		L ≦ 5.0	neglected			
	С		5.0 <	C L	0			
	D 0.3 <	< W			0			
	d = (a + b) / 2							
	Category	Size	(mm)	Acceptab	le number			
	A	d	≦ 0.2	neg	glected			
	В	0.2 < d			5			
	С	0.3 < d			3			
	D 0.5 < d 0							