

KD16SF60A

TRIACs

600V, 16A

Feature

- Full molded
- High voltage
- High sensitivity
- $T_j=150^{\circ}\text{C}$
- Stable surge-on current capability
- Pb free terminal
- RoHS:Yes

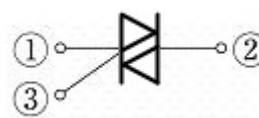
OUTLINE

Package (House Name): FTO-220A

Package (JEITA Code): SC-91



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : T_c=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Junction temperature	T _j		-40 to 150	°C
Repetitive peak off-state voltage	V _{DRM}		600	V
Non-repetitive peak off-state voltage	V _{DSM}	※	720	V
R.M.S. on-state current	I _{T(RMS)}	T _c =125°C, Commercial frequency, sine wave, Conduction angle θ=360°C, With heatsink	16	A
Surge on-state current	I _{TSM}	T _j =25°C, 60Hz sine wave, Non-repetive 1 cycle peak	120	A
Current squared time	I ² t	T _j =25°C, t=8.33ms, Non-repetitive	60	A ² S
Critical rate of rise of on-state current	di/dt		50	A/μs
Peak gate dissipation	P _{GM}	f=60Hz, Duty≤10%	5	W
Average gate dissipation	P _{G(AV)}		0.5	W
Peak gate current	I _{GM}	f=60Hz, Duty≤10%	2	A
Peak gate voltage	V _{GM}		10	V
Dielectric strength	V _{dis}	Terminals to case, AC 1 minute	2	kV
Mounting Torque	TOR	(Recommended torque:0.3N·m)	0.5	N·m

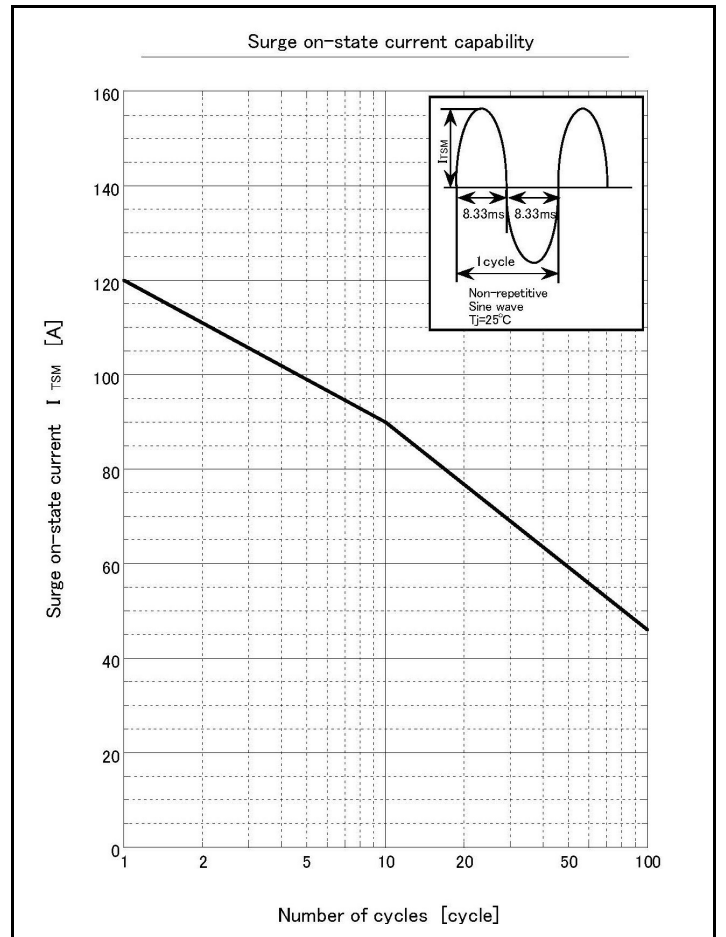
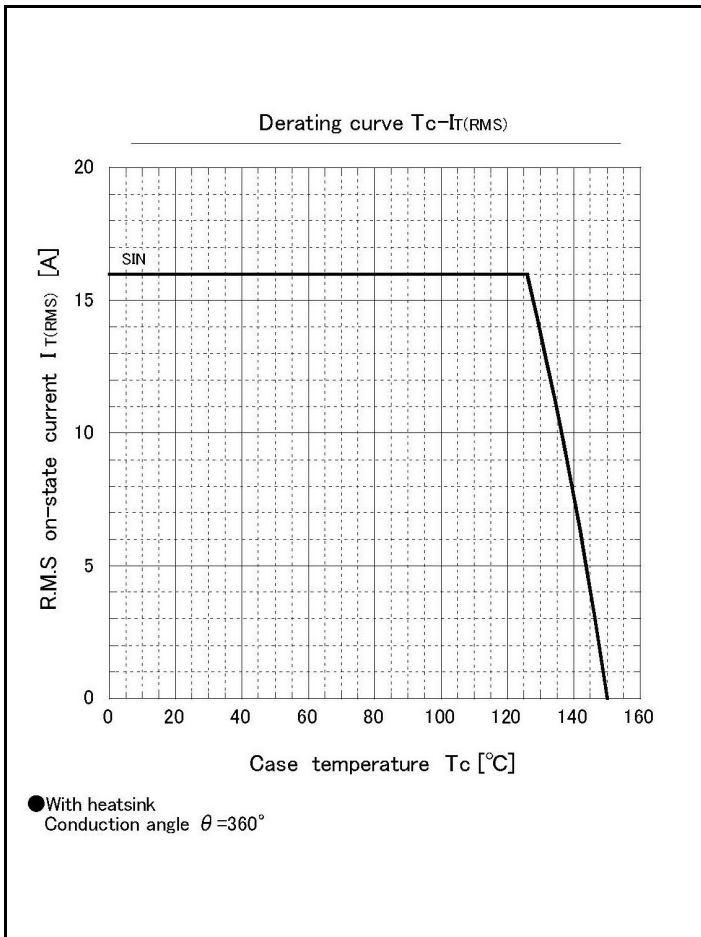
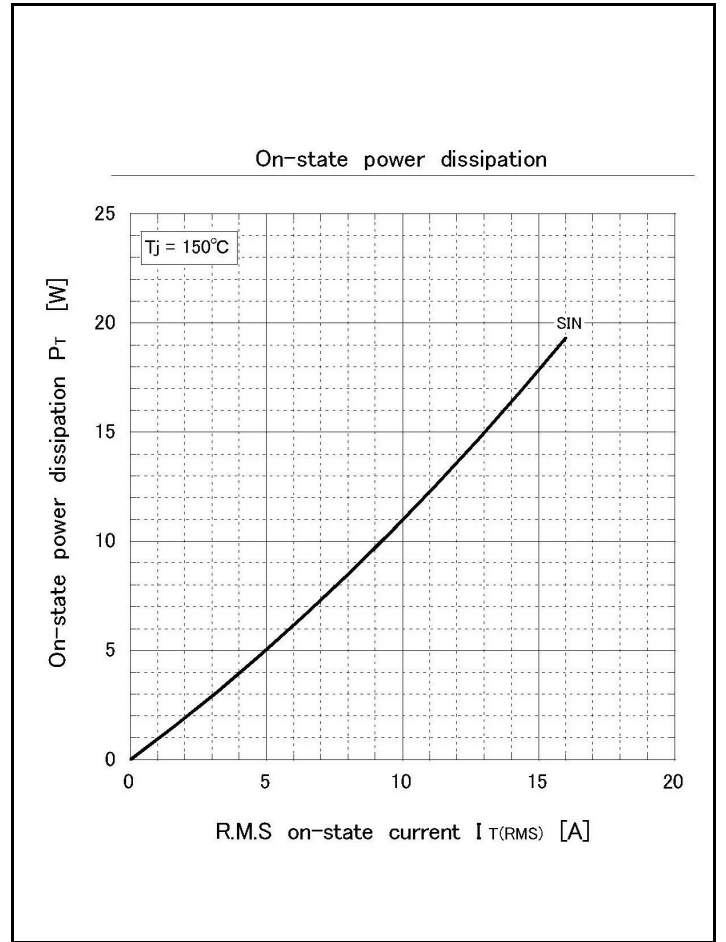
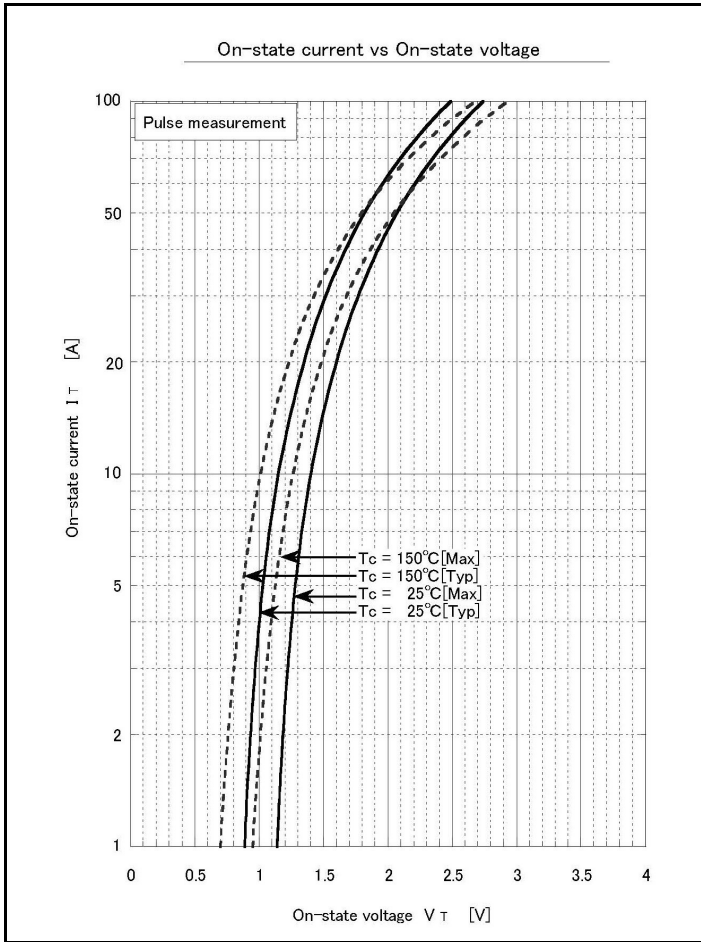
※ :See the original Specifications

Electrical Characteristics (unless otherwise specified : Tc=25°C)

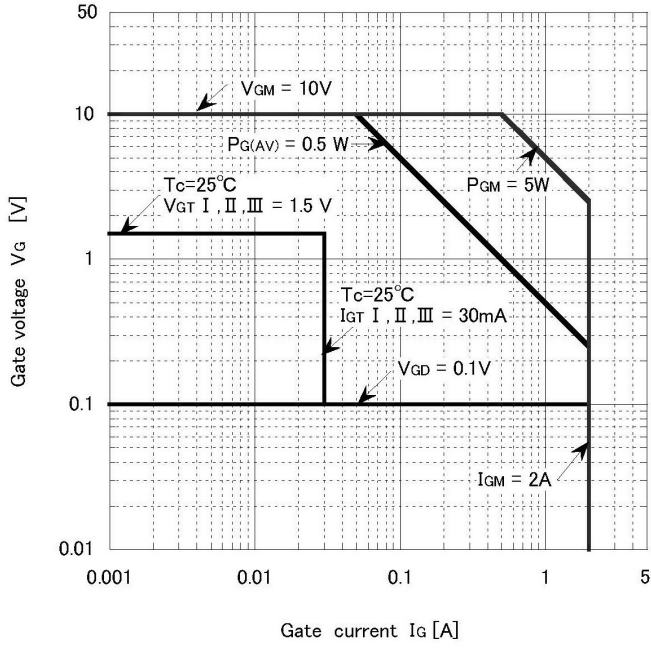
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Off-state current	I_{DRM}	VD=600V, Pulse measurement			10	μ A
On-state voltage	V_{TM}	ITM=20A, Pulse measurement			1.6	V
Gate trigger voltage	V_{GTI}	VD=6V, RL=10 Ω , T1-, T2+, G+			1.5	V
Gate trigger voltage	V_{GTII}	VD=6V, RL=10 Ω , T1-, T2+, G-			1.5	V
Gate trigger voltage	V_{GTIII}	VD=6V, RL=10 Ω , T1+, T2-, G-			1.5	V
Gate trigger voltage	V_{GTIV}	VD=6V, RL=10 Ω , T1+, T2-, G+			- *	V
Gate non-trigger voltage	V_{GD}	Tj=150°C, VD=1/2VDRM	0.1			V
Gate trigger current	I_{GTI}	VD=6V, RL=10 Ω , T1-, T2+, G+			30	mA
Gate trigger current	I_{GTII}	VD=6V, RL=10 Ω , T1-, T2+, G-			30	mA
Gate trigger current	I_{GTIII}	VD=6V, RL=10 Ω , T1+, T2-, G-			30	mA
Gate trigger current	I_{GTIV}	VD=6V, RL=10 Ω , T1+, T2-, G+			- *	mA
Latching current	I_{LI}	IG=0.1A, T1-, T2+, G+			100	mA
Latching current	I_{LII}	IG=0.1A, T1-, T2+, G-			100	mA
Latching current	I_{LIII}	IG=0.1A, T1+, T2-, G-			100	mA
Latching current	I_{LIV}	IG=0.1A, T1+, T2-, G+			- *	mA
Holding current	I_H	IT=1A			100	mA
Critical rate of rise of off-state voltage	dv/dt	Tj=150°C, VD=2/3VDRM	100			V/ μ s
Critical rate of rise of commutating voltage	(dv/dt)c	Tj=150°C, VD=2/3VDRM, (di/dt)c=-6A/ms	1			V/ μ s
Thermal resistance	Rth(j-c)	Junction to case with heatsink			1.25	°C/W

* :See the original Specifications

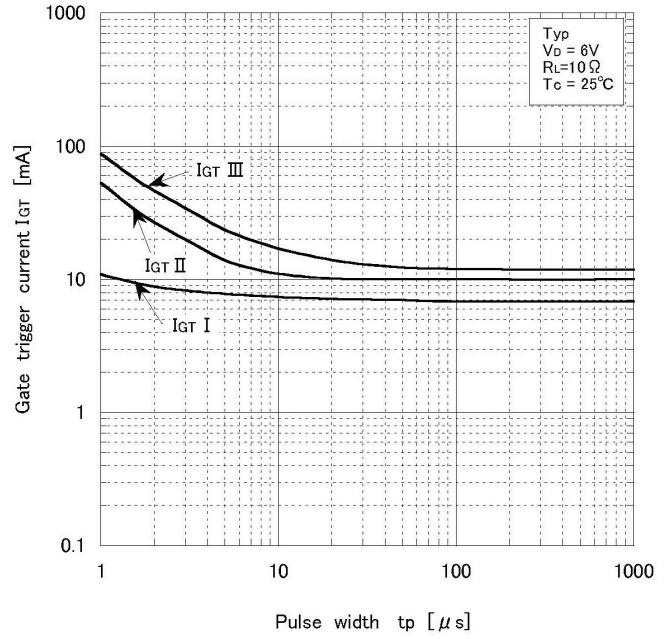
CHARACTERISTIC DIAGRAMS



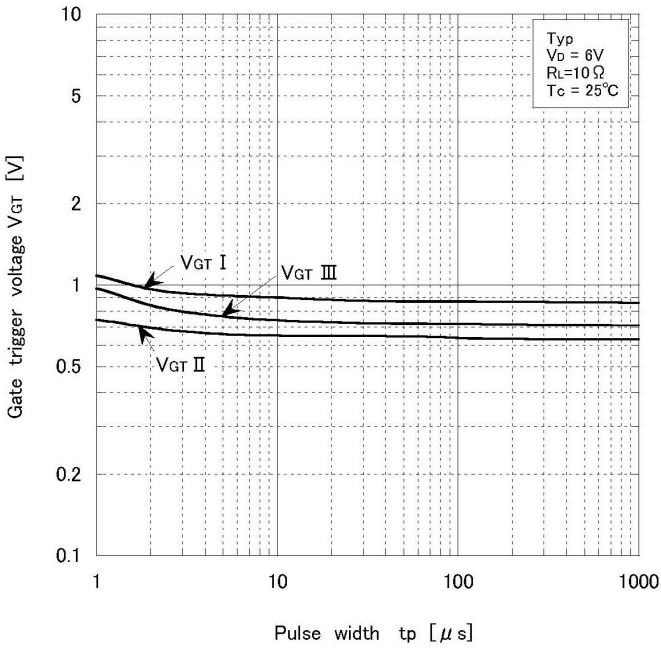
Gate characteristic



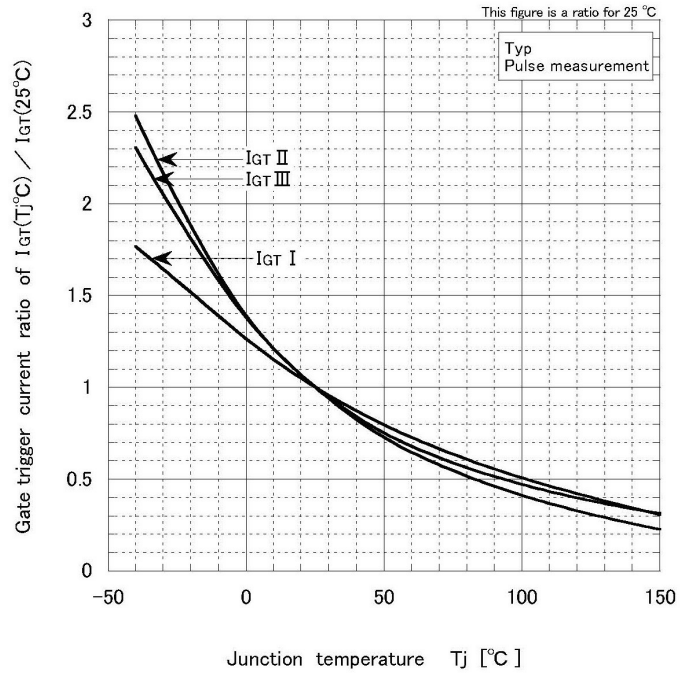
Gate trigger current vs Pulse width



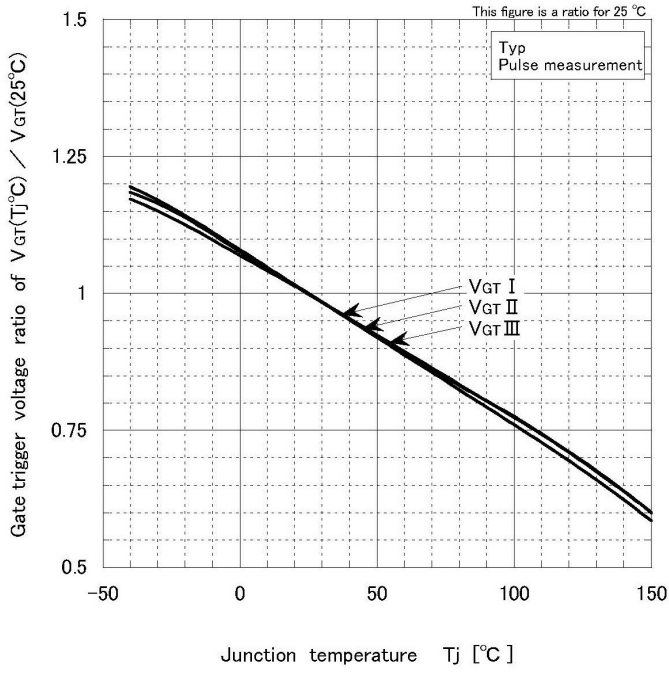
Gate trigger voltage vs Pulse width



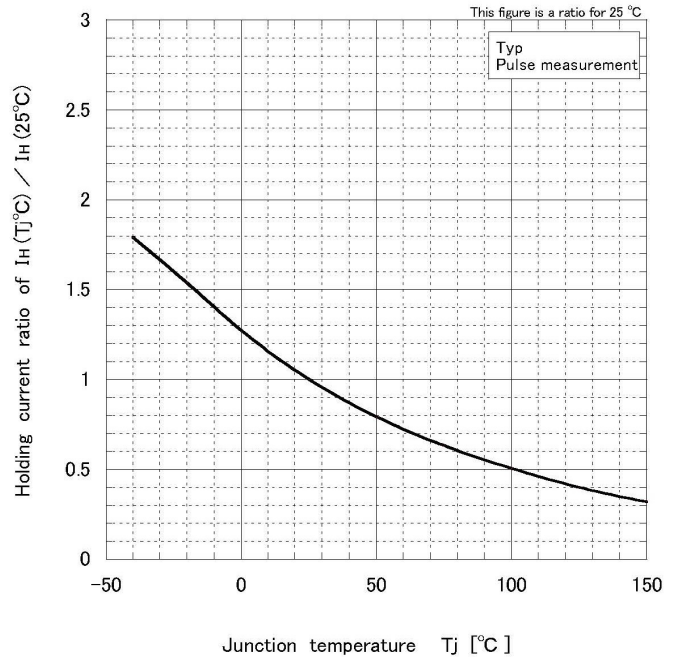
Gate trigger current ratio vs Junction temperature



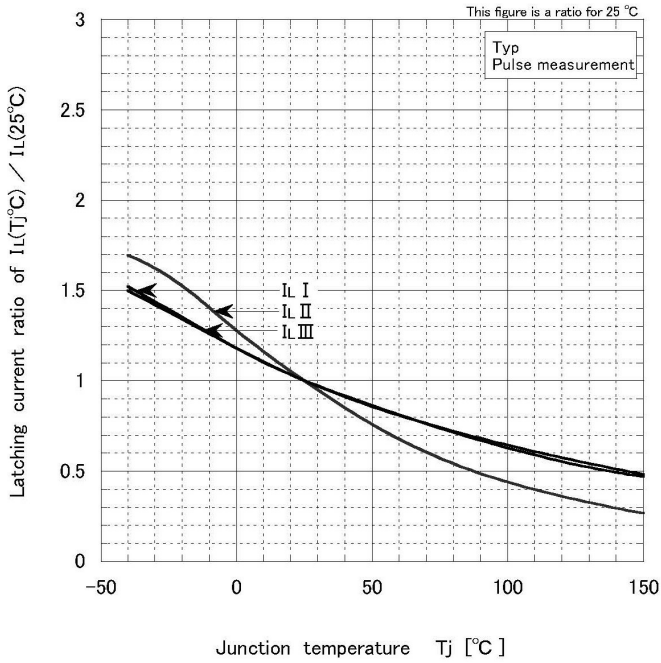
Gate trigger voltage ratio vs Junction temperature



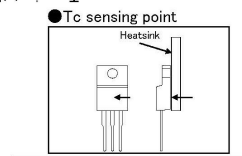
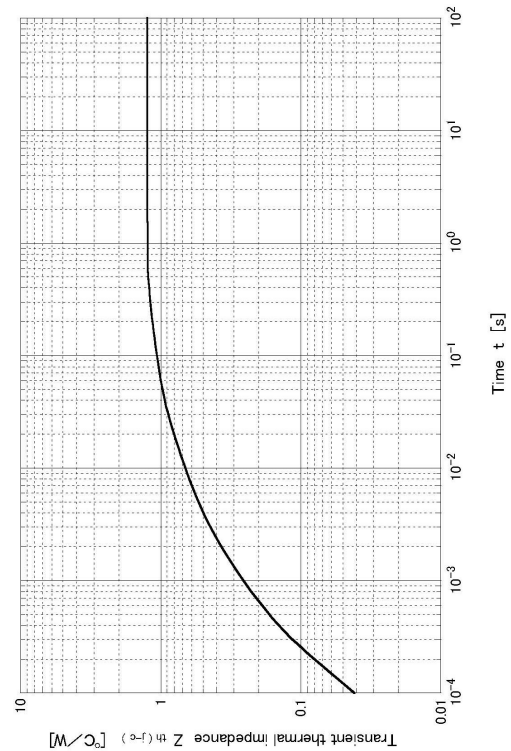
Holding current ratio vs Junction temperature



Latching current ratio vs Junction temperature

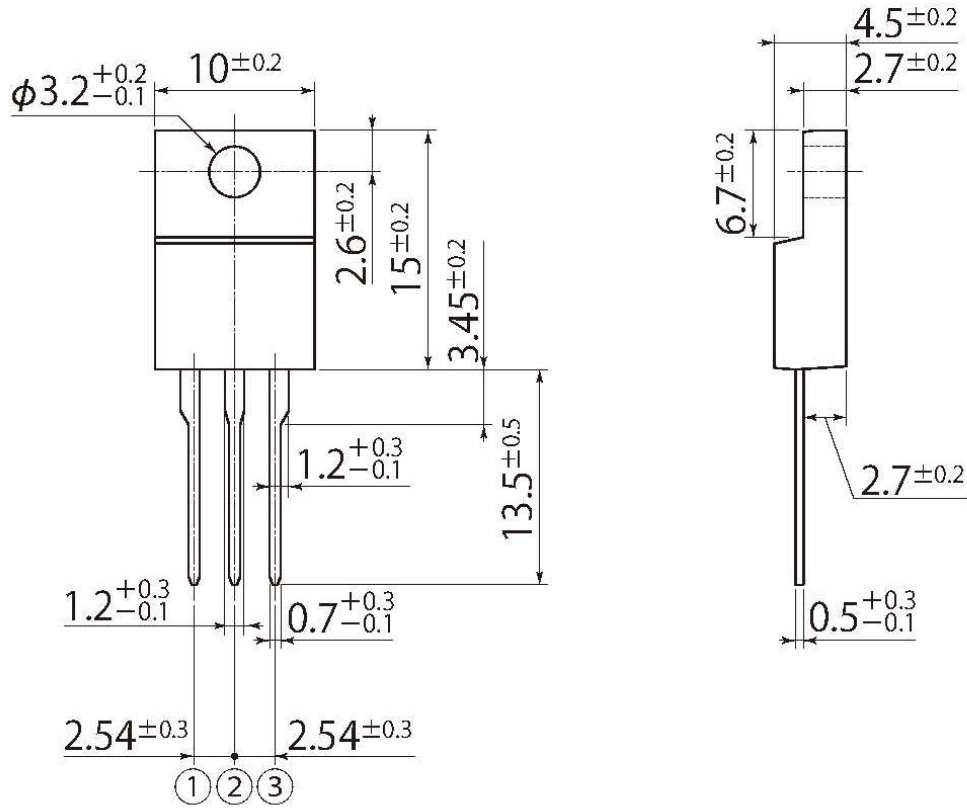


Transient thermal impedance



J7

JEDEC Code	-
JEITA Code	SC-91
House Name	FTO-220A(3pin)



Notes

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