

# Shenzhen K&D Technology Co.,Ltd

# SPECIFICATION FOR LCD MODULE

Customer :				
Product Model:		KD50G6-40NM-A3		
Sample co	ode:			
Designed by		Checked by		Approved by
Final Appro	oval by C	ustomer		
LCM Mac		LCM O	K	
Checked By			NG, Pi	roblem survey:
LCM Display OK  Checked By		Approve	d By	

<sup>\*</sup>The specification of "TBD" should refer to the measured value of sample. If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

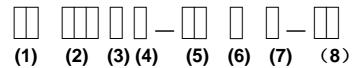
# **Revision History**

Version	Contents	Date	Note
Α	Original	2008.12.15	
<u> </u>			

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# 1 Numbering System



No	Definition	Specifications
(1)	TFT LCM Productor No.	KD Kingdisplay technologiy Co.,Ltd
(2)	Display monitor opposite angle line size	Unit :mm or mmm (size <10 inch: takes two integers; size >=10 inch: takes three integers)
(3)	Productor Types	D Digital photo frame / DVD GGPS MMP PMobil-Phone
(4)	Productor Development Series No.	By two figures characters expression from 01 to 99
(5)	Interface PIN Number	By two figures characters expression from 01 to 99
(6)	With Touch Panel Or Not	TWith T/P; NWithout T/P
(7)	LCD Type	AAUO; MCMO; CCPT; PPVI; LLG; WWintek; HHSD; TTopply; YHydis; IHitach; SSharp。。
(8)	Productor Development edition No.	By The English litters : A 1~ Z9

### 2 Scope

This specification applies to the TFT LCD module which is designed and manufactured by LCM Factory of Shenzhen K&D Technology Co.,Ltd.

#### 3 Normative Reference

GB/T4619-1996 《 Liquid Crystal Display Test Method》

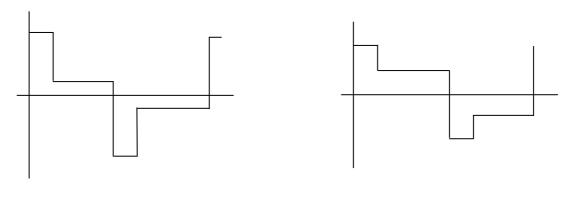
GB/T2424 《Basic environmental Testing Procedures for Electric and Electronic Products.》

GB/T2423 《Basic Testing Procedures for Electric and Electronic Products》 IEC61747-1 《SIXTH PARTGB2828`2829-87《National Standard of PRC》

#### 4 Definitions

### 4.1 Definitions of Vop

The definitions of threshold voltage Vth1, Vth2 the following typical waveforms are applied on liquid crystal by the method of equalized voltage for each duty and bias.



[ selected waveform ]

I non-selected waveform 1

① Vth1: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of selected waveform ( $f_f$ =80Hz,  $\Phi$ =10°  $\theta$ =270° at 25°C)

② Vth2: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of non-selected waveform

(f<sub>f</sub>=80Hz,  $\Phi$ =10°  $\theta$  =270° at 25°C)

③ Vop: (Vth1(50%)+Vth2(50%))/2  $(f_f=80Hz, \Phi=10^{\circ} \theta=270^{\circ} at 25^{\circ}C)$ 

#### 4.2 Definition of Response Time Tr, Td

①Tr: The time required which the brightness of segment becomes 10% from 100% when waveform is switched to selected one from non-selected one. ( $f_f=80Hz$ ,  $\Phi=10^{\circ}\theta=270^{\circ}at$  25 °C)

②Td: The time required which the brightness of segment becomes 90% from 10% when waveform is switched to

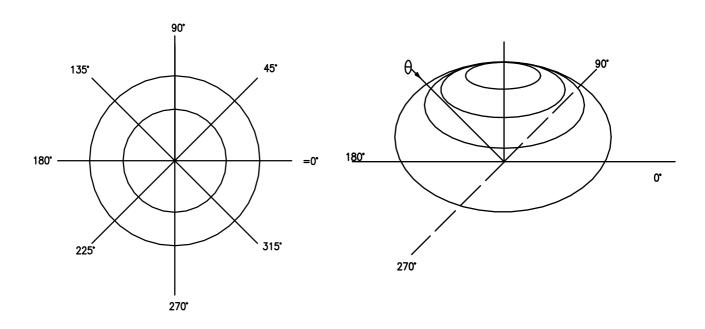
selected one from selected one. (f<sub>f</sub>=80Hz,  $\Phi$ =10°0=270°at 25°C)

### 4.3 Definition of Contrast Ratio Cr

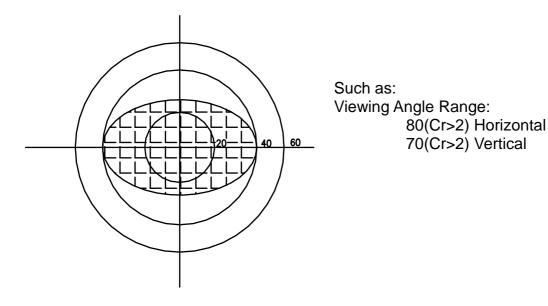
Cr=A/B

- ① A: Segments brightness in case of non-selected waveform
- ② B: Segments brightness in case of selected waveform

### 4.4 Definition of Angle and Viewing Range



Angular Graph: Constrast Ratio



# **5 Technology Specifications**

#### 5.1 Feature

This single-display module is suitable for use in Multidedia Player products.

The LCD adopts one backlight with High brightness 12-lamps white LED.

- 1) Construction: 5.0" a -Si color TFT-LCD, White LED backlight.
- 2) LCD:
  - 2.1 Amorphous-TFT 5.0-inch display, transmissive, normally white type.
  - 2.2  $800(RGB) \times 480$  dots Matrix.
  - 2.3 Narrow-contact ledge technique.
  - **2.4 LCD Driver IC: HX8258A**  $\times$  2 and HX8662C  $\times$  1.
- 3) Low cross talk by frame rate modulation.
- 4) RGB interface.
- 5) Video signal interface: Parallel RGB or serial.

5.2 Mechanical Specifications

Item	Specifications	Unit
Dimensional outline	120.7(W) ×75.8(H)×3.1 Max(T)	mm
Active area	108.0(W) ×64.8 (H)	mm
Pixel size	135(W) ×135(H)	um
Resolution	<b>800(RGB)</b> × 480	pixel

#### 5.3 Absolute Max. Rating

ltem	Symbol	Value			Unit	Remark	
пеш		Min	typ	Max	Offic	Remaik	
Logic power supply	DVDD	3.0	3.3	3.6	V		
Driver power supply	AVDD	11	12	13	V		
Input high voltage	$V_{IH}$	0.7DVDD	-	DVDD	V		
Input low voltage	$V_{IL}$	0	-	0.3DVDD	V		
Operating temperature	$T_{OPR}$	-20		+70	$^{\circ}$		
Storage temperature	T <sub>STG</sub>	-30		+80	${\mathbb C}$		

# 5.4 Electrical Characteristics (VSS=0V,Ta=-20 to 70℃)

( VDD=2.25V to 3.6V, VDC=5V or AVDD=10V, VSS=0V, VGH=4\*VDC, VGL=-2\*VDC, VCL=-1\*VDC)

Parameter	Symbol	Condition		Spec.			
Faranietei	Symbol	Condition	Min.	Тур.	Max.	Unit	
Input H voltage	V <sub>IH</sub>	CKV, STV, R/L,	0.8VDD	-	VDD		
Input L voltage	V <sub>IL</sub>	OE	VSS	-	0.2VDD	V	
Output H voltage	VoH	I <sub>OH</sub> =200μA	VDD-0.3	-	VDD	•	
Output L voltage	V <sub>OL</sub>	I <sub>OL</sub> =200µA	VSS	-	VSS+0.3		
Output H resistance	R <sub>OH</sub>	V <sub>OUT</sub> = VGH-1.0V	-	- (	1000	Ω	
Output L resistance	R <sub>OL</sub>	V <sub>OUT</sub> = VGL+1.0V	-		1000	Ω	
Pull high resistance	R <sub>PU</sub>	TC1, 2 SEQ1,2 TEST1, 2, 3 VGHS1, 2, 3 VGLS1, 2, 3 STB CH_SEL	160			kΩ	
VGR output current	$I_{VGR}$		-		2	mΑ	
Regulator output voltage	VGR	No load	4.4	4.5	4.6	V	
VGH voltage	VGH	External C=1µF		4VDC	-	V	
VGL voltage	VGL	External C=1µF	(J-)	-2VDC	-	V	
VCL voltage <sup>(1)</sup>	VCL	External C=4.7µF	<u> </u>	-VDC	-	V	
VCOMH output voltage	усомн1	No load. LNIV=VDD, COMC=0.65V, COMPP=3.65V	4.2	4.3	4.4	V	
VCOML output voltage	VCOML1	No load. LNIV=VDD, COMC=0.65V, COMPP=3.65V	-3.1	-3.0	-2.9	٧	
VCOMH output voltage	VCОМН2	COMPP=VSS	5.4	5.5	5.6	٧	
Input leakage current	I <sub>IN</sub>	V <sub>I</sub> =0∨ or 3.6∨	-1.0	-	+1.0	μΑ	
VDD Power consumption <sup>(2)</sup>	I <sub>VDD1</sub>	No load, LNIV=VDD, VDD=2.5V,	-	150	200	μΑ	
VDC Power consumption <sup>(2)</sup>	I <sub>VDC1</sub>	VDC=5.0V F <sub>CPV</sub> =20KHz, OE =V <sub>IL</sub>	-	1800	2500	μΛ	
VDD Power consumption <sup>(3)</sup>	I <sub>VDD2</sub>	No load, LNIV=VSS, VDD=2.5V,	-	150	200	4	
AVDD Power consumption <sup>(3)</sup>	I <sub>AVDD1</sub>	AVDD=10.0V F <sub>CPV</sub> =20KHz, OE =V <sub>IL</sub>	-	TBD	TBD	μA	
Digital standby current	I <sub>VDD3</sub>	STB = "L", all function shut down	-	-	100	μΑ	

Analog standby current	I <sub>VDC3</sub>	-	-	200	

#### Note:

(1)VCL voltage will be disabled for Dot inversion setting.

(2)Power consumption with the following condition:

Output no load, VGH =20V, VGL = -10V, VDD =2.5V, VDC =5.0V, V<sub>IH</sub>=VDD, V<sub>IL</sub>=VSS, F<sub>CPV</sub>=20KHz, OE =V<sub>IL</sub>,

(3)Power consumption with the following condition:

Output no load, VGH =20V, VGL = -10V, VDD =2.5V, AVDD =10.0V, V<sub>IH</sub>=VDD, V<sub>IL</sub>=VSS, F<sub>CPV</sub>=20KHz, OE =V<sub>IL</sub>,

### 5.5 Optical specifications

Item		Symbol	Conditions	Spe	ecificati	ons	Unit	Note	
item			Conditions	Min.	Typ.	Max.	Unit		
Transmittance	÷	T%			7.5		%		
Contrast Ratio	0	CR		150	250			All loss aide dete	
Response Tin		TR			15	20	ms	All left side data are based on	
response iiii	ie	Tr			35	50	ms	CMO's following	
	Red	XR		0.585	0.615	0.645		condition -T6	
	Neu	YR	Viewing normal angle	0.314	0.344	0.374		NTSC: 50%	
	Green	X <sub>G</sub>	$\theta_X = \theta_Y = 0^\circ$	0.277	0.307	0.337		LC:5091 Light : C light (Machine:BM5A)	
Chromaticity	Green	Y <sub>G</sub>	0x - 0y -0	0.532	0.562	0.592			
Chromaticity	Blue	X <sub>B</sub>		0.103	0.133	0.163			
	Diue	YΒ		0.120	0.150	0.180		Normal Polarizer Without DBEF	
	White	X <sub>w</sub>		0.279	0.279 0.309 0.339		Simulation		
	vvnite	Yw		0.320	0.350	0.380			
	Har	θ <sub>X+</sub>			45			Data Reference	
Viewing Angle	Hor.	θ <sub>X</sub> .	Center		45		-1	Only	
		θ <sub>Y+</sub>	CR≥10		15		deg.		
	Ver.	θy.			35				

<sup>\*</sup>Note (1) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63/L0

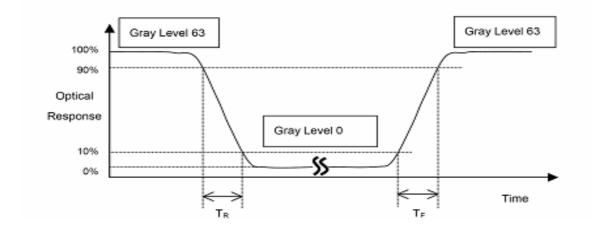
L63: Luminance of gray level 63

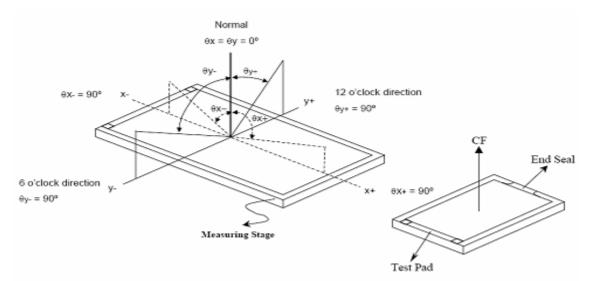
L0: Luminance of gray level 0

CR = CR(10)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).

\*Note (2) Definition of Response Time (TR, TF):

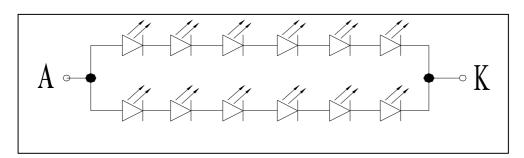




5.6 LED back light specification (12 White Chips)

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	Vf	lf=40mA	18	19.2	20.4	V
Uniformity (with L/G)	∆ <b>B</b> p	lf=40mA	80	-	-	%
Luminance for LCD	$L_V$	If=40mA	5200	-	-	cd/m <sup>2</sup>

### **LED CIRCUIT**



### **5.7 Interface Pin Connections**

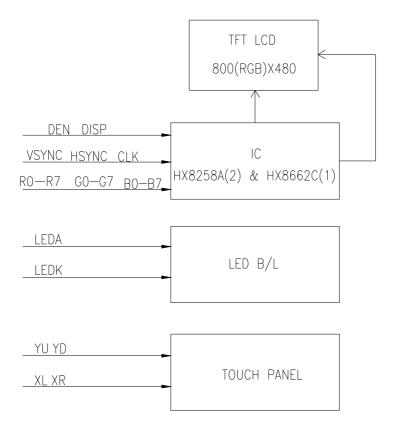
PIN NO.	Symbol	Description
1	LED-K	LED backlight(Cathode)
2	LED-A	LED backlight(anode)
3	GND	Ground
4	VDD	Power supply (Digital +3.0V)
5-12	R0-R7	Red Data
13-20	G0-G7	Green Data
21-28	B0-B7	Blue Data
29	GND	Ground
30	CLK	Clodk
31	DISP	Display on/off

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32	HSYNC	Horizontal sync input in RGB mode (short to GND if not used)
33	VSYNC	Vertical sync input in RGB mode (short to GND if not used)
34	DEN	Data Enable
35	AVDD	Power supply (12V)
36	GND	Ground
37	NC	NC
38	NC	NC
39	NC	NC
40	NC	NC

# 6 Signal timing diagram and Circuit block diagram

# 6.1 Circuit block diagram

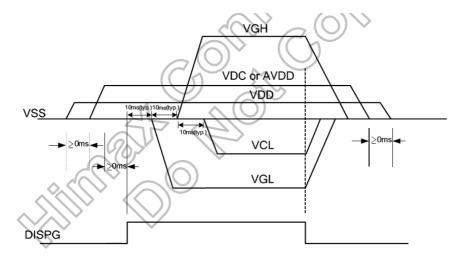


### 6.2 Signal Timing Diagram

### 6.2.1 Power ON/OFF Sequence

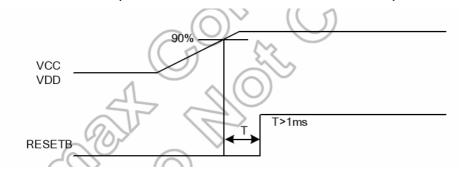
To prevent the device damage from latch up, the power ON/OFF sequence shown below must be followed.

Power ON: VCC, GND  $\rightarrow$  VDDA, VSS  $\rightarrow$  V1 to V10 Power OFF: V1 to V10  $\rightarrow$  VDDA, VSS  $\rightarrow$  VCC, GND



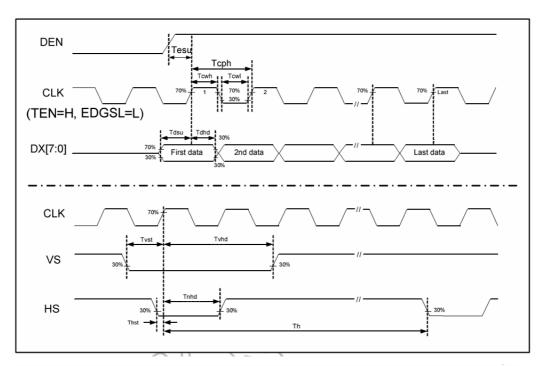
### 6.2.2 Reset timing

The reset input must be held for at least 1ms after power is stable.



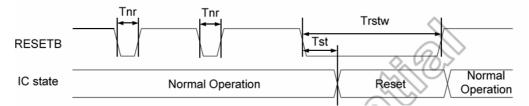
### 6.2.3 Timing Diagram of interface Signal

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#### Hardware reset timing

PARAMETER	Symbol		Unit		
PARAMETER	Syllibol	Min.	Тур.	Max.	Oilit
RESETB low pulse width	$T_{rstw}$	10	-	-	μs
Negative noise pulse width	$T_{nr}$		-	2	μs
Reset start time	$T_{st}$	2	-		μs



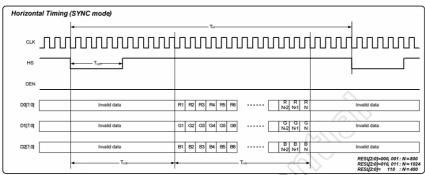


Figure 8. 2 SYNC Mode Horizontal Data Format

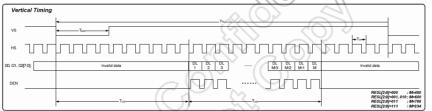


Figure 8. 3 SYNC Mode Vertical Data Format

# sync mode

PARAMETER	Symbol	Spec.			Unit	
PARAMETER	Syllibol	Min.	Тур.	Max.	Ollit	
CLK frequency	F <sub>CPH</sub>	-	33.26	-	MHz	
CLK period	T <sub>CPH</sub>	-	30.06	-	ns	
CLK pulse duty	$T_{CWH}$	40	50	60	%	
HS period	T <sub>H</sub>	1	1056	1	$T_{CPH}$	
HS pulse width	T <sub>WH</sub>	1	128	-	T <sub>CPH</sub>	
HS-first horizontal data time	T <sub>HS</sub>	ST	HD[7:0]+8	8 <sup>(i)</sup>	$T_{CPH}$	
HS Active Time	T <sub>HA</sub>	-	800	-	$T_{OPH}$	
VS period	$T_V$	1	525	\(\frac{1}{2}\)	T <sub>H</sub>	
VS pulse width	T <sub>WV</sub>	1	2	V*£/(	T-	
VS-DEN time	T <sub>VS</sub>	S	TVD[6:0]+	8	$T_H$	
VS Active Time	$T_VA$	-	480		$T_H$	

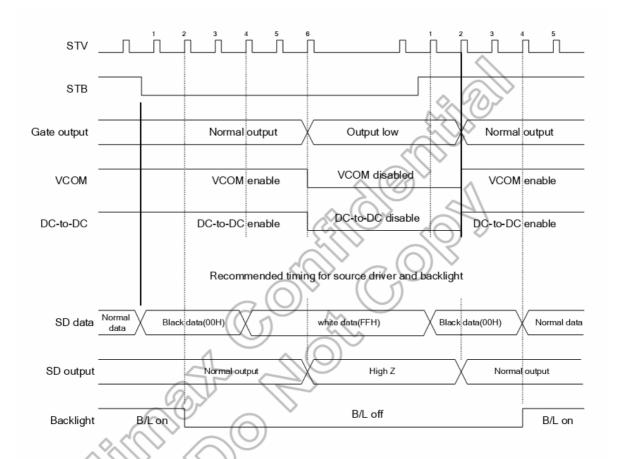
# DE mode

PARAMETER	Cymphal		Spec.				
PARAMETER	Symbol	Min.	Тур.	Max.	Unit		
CLK frequency	F <sub>CPH</sub>	O <sub>^</sub>	33.26		MHz		
CLK period	T <sub>CPH</sub>	(-6/)	30.06	۷(-)) ،	ns		
CLK pulse duty	$T_{CWH}$	40	50	60	%		
DE period	$T_{DEH}+T_{DEL}$	1000	1056	1200	$T_{CPH}$		
DE pulse width	T <sub>DH</sub>	11-	800	-	T <sub>CPH</sub>		
DE frame blanking	T <sub>HS</sub>	10	45)	110	$T_{DEH}+T_{DEL}$		
DE frame width	T <sub>EP</sub>		480	-	$T_{DEH}+T_{DEL}$		

PARAMETER	Symbol		Unit		
PARAMETER	Syllibol	Min.	Тур.	Max.	Ollit
OEV pulse width	$T_{OEV}$	)-	150	-	$T_{CPH}$
CKV pulse width	T <sub>CKV</sub>	}	133	•	$T_{CPH}$
DE(internal)-STV time	$T_1$	-	4	-	$T_{CPH}$
DE(internal)-CKV time	$T_2$	-	40	•	$T_{CPH}$
DE(internal)-OEV time	$ T_3$	-	23	-	$T_{CPH}$
DE(internal)-POL time	$T_4$	-	157	-	$T_{CPH}$
STV pulse width	-	-	1	-	$T_H$

<sup>(</sup>i).  $T_{HS} + T_{HA} < T_H$ 

# **6.2.4 Gate Driver Timing Control**



# 7 Reliability Test Conditions And Methods

NO	Item	Condition	Method
1	High / Low Temperature Storage	80℃/-30℃ 120hrs	Check and record every 48Hrs
2	High / Low Temperature Life	70°C/-20°C 120hrs (operating mode)	Check and record every 48Hrs
3	High Temperature、 High Humidity Operating	60℃,90% RH, 96Hrs	Check and record every 48hrs
4	Thermal Shock	-30°C(30Min) → 25°C(5Min) 80°C(30Min) (conversion time, : 5 sec ) 20 cycles	Each 10 cycles end , check

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5	Vibration	10Hz~55Hz~10Hz Amplitude: 1.5mm 2hrs for each direction(X,Y,Z)	Each direction end, Check the Appearance and Electrical Characteristics
6	Static Electricity	Gap mood: ±1KV~±8KV (10 times air discharge with positive/negative voltage voltage gap : 1kv) Touch mood: ±1KV~±4KV	Each discharge end, Check the Electrical Characteristics
7	Curve	60 Thousand times, 40 times/min 150° (according to die if exist)	Check and record every 2~4 thousand times
8	Slump	Free faller movement for each side cording angle (75cm High 6 sides 2 angle 2 cording)	End

# 8 Inspection standard

No	Item	Criterion	
01	Outline Dimension	In accord with drawing	
02	Position-fin ding Dimension Assemble Dimension	In accord with drawing	

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		Round type: no 3.1 Small area  y  X		0.1	Unit : mm Dimension  D≤0.1 <d≤0.15 d="">0.15</d≤0.15>	Qualified Quantity  Ignore  2	
03	LCD black spots, white spots (Round type)	3.2Large area LCD		Dimension  D≤0.1  0.1 <d≤0.15 0.15<d≤0.20="" d="">0.20</d≤0.15>		Qualified Quantity  Ignore  2  1	
		Unit : mm	4.′	1	Small	area LCD	
			Leng	gth	Width	Qualified Quantity	
	04 LCD black spots, white spots (Line Style)	w w	-		≤0.015	Ignore	
04			≤1.	.0	0.015 <w≤< td=""><td><u>2</u></td><td></td></w≤<>	<u>2</u>	
	(Line Otyle)		≤2.	.0	0.025	1	
			≤1.	.0	0.025 <w< 0.05</w< 	1	
			-		D>0.05	According to circle	

			4.2Larç	ge area LCD		
		<u> </u>	Length	Width	Qualified Quantity	
		<u>, , , , , , , , , , , , , , , , , , , </u>	-	≤0.015	Ignore	
		<del>                                   </del>	₹2.0	0.015 <w≤ 0.025</w≤ 	2	
			≤1.0	0.025 <w≤ 0.05</w≤ 	1	
			-	D>0.05	According to circle	
	LCD	Same to NO.3 o	circle		015 , unqualified and viewing area	
05	Scratch \ Threadlike Fiber	sightline and surface of LCD is vertical (2)Same to NO.3 line style				
06	POL	It is not admissible that POL is beyond the edge of glass, else, unqualified. It is essential that POL is over the 50 percent of width of frame, else, unqualified. According to the drawing in case of special definition.				
		Scratch		Reject		
07	IC/FPC Bonding	Intensity Of Adhesion	THE IT IOWAL THAN SHACITICATION TAIACT			
		Gold Fold Twis	st	Reject		
07	IC/FPC	Silicon	Silicon  According to outline, no gold outside seal can not be higher than LCD		•	
O1	Bonding	FPC Gold Seve	er	Reject		
08	SMT	Lack of Component Polarity Invers	e	If exist, rej	ect	

T	<u></u>
Leak Solder Virtual Solder	If exist, reject
Short Circuit In Solder Point	If exist, reject
Tin Ball	If exist, reject
Tin Acumination	If visual, reject
Height Solder Point	If higher 0.5mm than component. reject
Height of component	Either side higher 0.5mm than component, reject
Component Shift	X Solder Pad component  Y  X<3/4Z y>1/3D reject reject

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		Few Tin	PCB pad PCB  If θ≤20° reject
08	SMT	Component Deflection	Component Pad  If Y >1/3D reject
		Component Carcass Sideways	Reject
		Component Carcass Sideways	If exist with visual inspection , reject
		Lot Tin	A: Tin accrete the solder side completely, hollowly, Ok B: Tin accrete the solder side completely, full circle arc, ok C: Jointing include whole solder side, height of tin>50 percent of height of component, reject
		Few Tin	A: Tin accrete the solder side completely , hollowly ,Ok B: height of tin > 1/3 of solder side of component , ok C: height of tin ≤ 1/3 of solder side of component, reject

08	SMT	Normal  Jointing side			
09	Light	forbid 2 Differe Quality of CSTN with visua Display green b			Forbid  strake with visual inspection,  intness of color in viewing area inspection (full white red ue), forbid play change with visual , forbid
10	Color Of CIE Coordinate		Red ±0.05  Green ±0.05  Blue ±0.05  according to the spreading results and the spreading results are specified.		Drive LCD under normal condition, 25°C Φ=0 θ=0 Test white red green blue with DMS Record
11	Brightness	In acco prod specific	luct	specification Measure to 3. Adjust to burrow ag press "mea display is s	ocation is in Follow Picture brightness instrument tozero , gainst the surface of LCD , easure" , record when the

			Measure location
12	CR (Max)	According to specification	According to product specification Measure instrument ( DMS-501 )
13	Response time	According to specification	According to product specification Measure instrument ( DMS-501 )
14	Viewing angle	According to specification	According to product specification Measure instrument ( DMS-501 )
15	Vibration、 Ring	Compare with the sample customer supply	Compare with the sample customer supply when assemble
16	Frequency Of FPC Bend	According to the use of product ( main FPC of foldaway cell phone ≥6 thousand )	Measure instrument Bend angle : 150° Fix FPC in the casement when customer supply

# 9 Handling Precautions

#### 9.1 Mounting method

The LCD panel of Daxian LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 9.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the

ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI) , Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

### 9.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

#### 9.4 packing

- Module employ LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 9.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  - Usage under the maximum operating temperature, 50%Rh or less is required.

#### 9.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

#### 9.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 10 Precaution for use

#### 10.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 10.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to Daxian , and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

### 11 Dimensional Outline

