

N-channel MOSFET

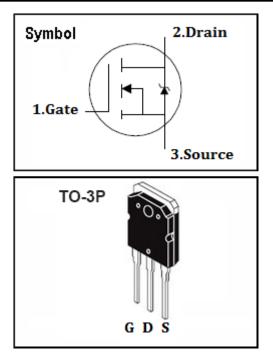
Features

- 900V,9A
- R_{DS(on)}=1.05Ω @V_{GS}=10V,I_D=4.5A
- High speed switching
- High ruggedness
- 100% avalanche tested
- Improved dv/dt capability

General Description

KDF9N90A is well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

Absolute Maximum Ratings



Symbol	Parameter	Value	Units	
V _{DSS}	Drain-Source Voltage	900	V	
V _{GS}	Gate-Source Voltage	<u>+</u> 25	V	
I _D	Continuous Drain Current(TC=25°C) 9		A	
I _{DM}	Pulsed Drain Current(Note 1)	36	A	
EAS	Single Pulsed Avalanche Energy(Note 2)	900	mJ	
dV/dt	Peak Diode Recovery dv/dt(Note 3)	4	V/ns	
P	Maximum Power Dissipation ($T_{C} \mbox{=} 25^{\circ}\mbox{C}$)	280	W	
P _D	Maximum Power Dissipation (T_c =100 °C)	112	W	
TJ	Operating Junction Temperature Range	-55 to +150	°C	
T _{STG}	Storage Temperature Range	-55 to +150	°C	

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. Starting T_J=25 $^\circ\!\mathrm{C}$,L=21mH,R_G=50 Ω ,I_D=9A,V_GS=10V

3. $I_{SD} \leqslant$ 9A, di/dt \leqslant 200A/us, $V_{DD} \leqslant$ BV_{DSS}. Starting T_J=25°C

Thermal data

Symbol	Parameter	Max.	Units
R _{th J-C}	Thermal Resistance, Junction to case	0.45	°C / W
R _{th J-A}	Thermal Resistance, Junction to ambient	40	°C / W



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	900	-	-	V
I _{DSSS}	Drain-Source Leakage Current	V_{DS} =900V, V_{GS} =0V	-	-	10	uA
1	Gate Leakage Current, Forward	V _{GS} =25V, V _{DS} =0V	-	-	100	nA
GSS	Gate Leakage Current, Reverse	V _{GS} = -25V, V _{DS} =0V	-	-	-100	nA
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_{D}=250uA$	2	-	4.5	V
R _{DS(on)}	Collector-Emitter Saturation Voltage	V _{GS} =10V, I _D =4.5A	-	1.05	-	Ω
Q _g	Total Gate Charge	V _{DD} =720V V _{GS} =10V I _D =9A	-	45	-	nC
Q _{gs}	Gate-Source Charge		-	14	-	nC
Q_{gd}	Gate-Drain Charge		-	18	-	nC
t _{d(on)}	Turn-on Delay Time	V_{DS} =450V V_{GS} =10V I_{D} =9A R_{G} =25 Ω	-	50	-	ns
t _r	Turn-on Rise Time		-	120	-	ns
t d(off)	Turn-off Delay Time		-	100	-	ns
t _f	Turn-off Fall Time		-	80	-	ns
C _{iss}	Input Capacitance	V _{DS} =25V V _{GS} =0V f = 100kHz	-	2200	-	pF
C _{OSS}	Output Capacitance		-	180	-	pF
C _{rss}	Reverse Transfer Capacitance		-	15	-	pF

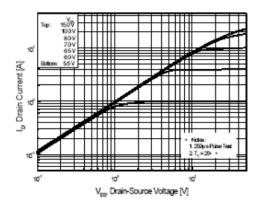
Electrical Characteristics (T_c=25°C unless otherwise noted)

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Source-Drain Ratings and Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _{SD}	Forward On Voltage	V _{GS} =0V,I _S =9A	-	-	1.5	V
I _S	Continuous Diode Forward Current		-	-	9	А
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				36	А
t _{rr}	Reverse Recovery Time	V _{GS} =0V,I _S =9A	-	550		ns
Q _{r r}	Reverse Recovery Charge	dI _F /dt=100A/us	-	6.5		uC







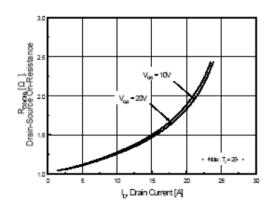


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

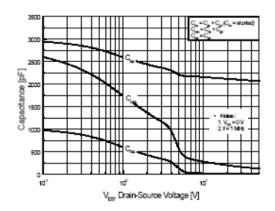


Figure 5. Capacitance Characteristics

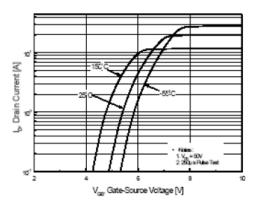
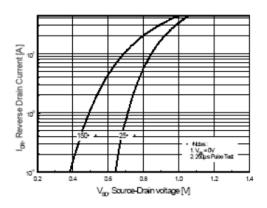


Figure 2. Transfer Characteristics





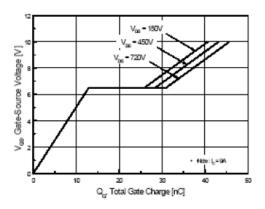
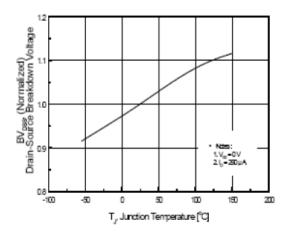


Figure 6. Gate Charge Characteristics







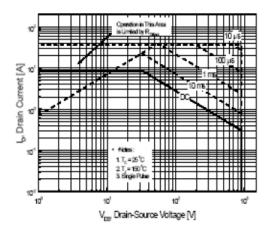
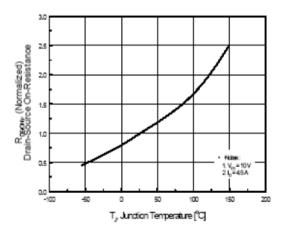
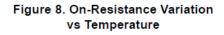


Figure 9. Maximum Safe Operating Area





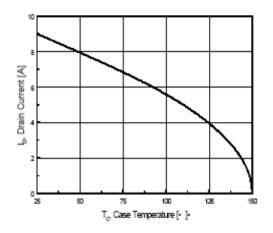


Figure 10. Maximum Drain Current vs Case Temperature

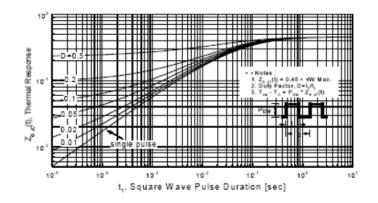
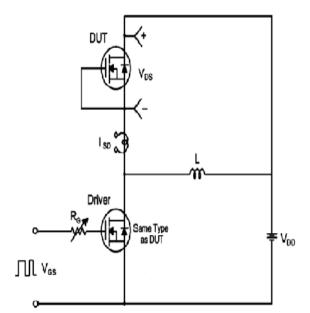


Figure 11. Transient Thermal Response Curve





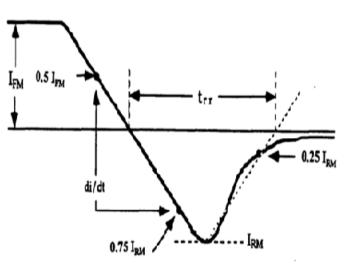


Fig12. Diode reverse recovery test circuit waveform

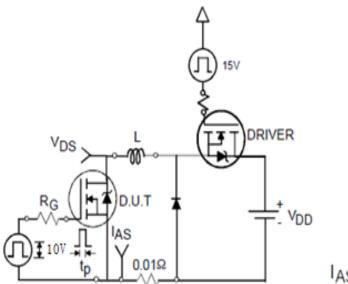
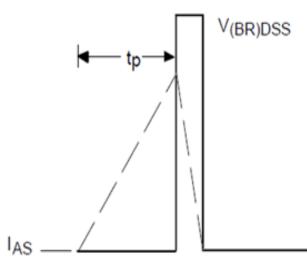


Fig13. Unclamped inductive test circuit waveform





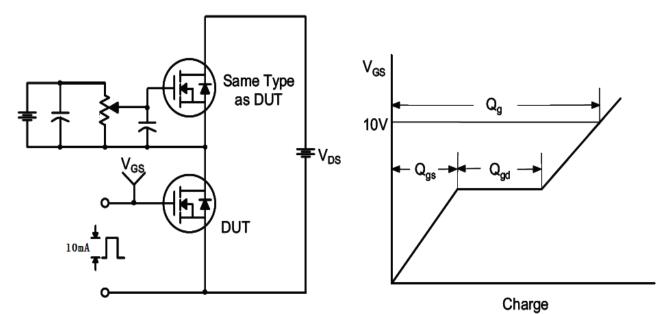


Fig14. Gate charge test circuit waveform

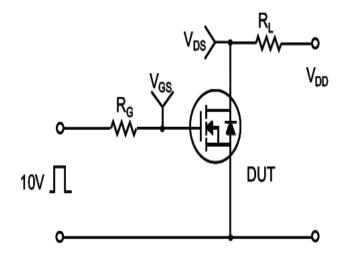
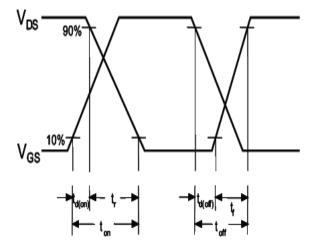
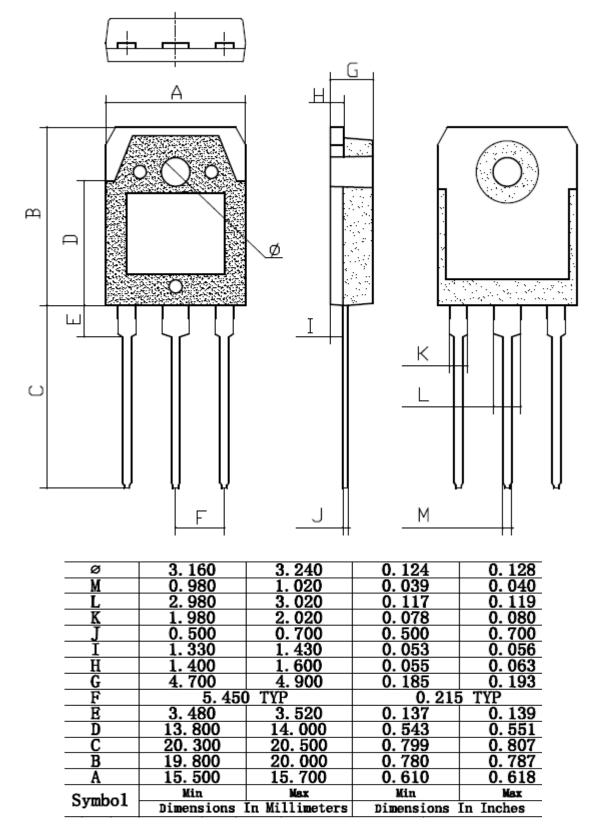


Fig15. Switching time waveform





TO3P PACKAGE OUTLINE





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