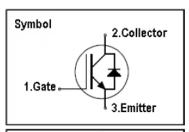


IGBT

Features

- 1200V,20A
- $V_{CE(sat)(typ.)}$ =2.7V@ V_{GE} =15V, I_{C} =20A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms





General Description

KEDA PT IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating), and other soft switching applications.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V _{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
1	Continuous Collector Current (T _C =25 °C)	40	А
I _C	Continuous Collector Current (T _C =100℃)	20	А
I _{CM}	Pulsed Collector Current (Note 1)	60	А
l _F	Diode Continuous Forward Current (T _C =100 °C)	20	А
I _{FM}	Diode Maximum Forward Current (Note 1)	60	А
Ъ	Maximum Power Dissipation (T_{C} =25 $^{\circ}{\text{C}}$)	160 W	
P_D	Maximum Power Dissipation (T _C =100℃)	60	W
TJ	Operating Junction Temperature Range	-55 to +150	$^{\circ}$
T _{STG}	Storage Temperature Range	-55 to +150	$^{\circ}$

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	0.78	°C/W
R _{th j-c}	Thermal Resistance, Junction to case for Diode	0.95	°C/W
R _{th j-a}	Thermal Resistance, Junction to Ambient	40	°C/W



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	1200	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 1200V, V _{GE} = 0V	-	-	250	uA
	Gate Leakage Current, Forward	V_{GE} =30V, V_{CE} = 0V	-	-	100	nA
GES	Gate Leakage Current, Reverse	V_{GE} = -30V, V_{CE} = 0V	-	-	-100	nA
$V_{\text{GE(th)}}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$, $I_{C} = 250uA$	4.0	-	6.0	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$V_{GE} = 15V, I_{C} = 20A$	-	2.7		V
Qg	Total Gate Charge	V _{CC} =600V	-	63		nC
Qge	Gate-Emitter Charge	V _{GE} =000V V _{GE} =15V I _C =20A	-	24		nC
Qgc	Gate-Collector Charge		-	19		nC
t _{d(on)}	Turn-on Delay Time	V _{CC} =600V V _{GE} =15V	-	36	-	ns
t _r	Turn-on Rise Time		-	78	-	ns
t _{d(off)}	Turn-off Delay Time		-	285	-	ns
t f	Turn-off Fall Time	I_{C} =20A R_{G} =28 Ω	-	331	-	ns
Eon	Turn-on Switching Loss	Inductive Load	-	1.9	-	mJ
Eoff	Turn-off Switching Loss	T _C =25 ℃	-	1.2	-	mJ
Ets	Total Switching Loss	1	-	3.1	-	mJ
C _{ies}	Input Capacitance	V _{CE} =25V	-	550	-	pF
C _{oes}	Output Capacitance	V _{CE} =23V V _{GE} =0V	-	180	-	pF
C _{res}	Reverse Transfer Capacitance	f = 100kHz	-	110	-	pF

Electrical Characteristics of Diode (T_C=25℃ unless otherwise noted)

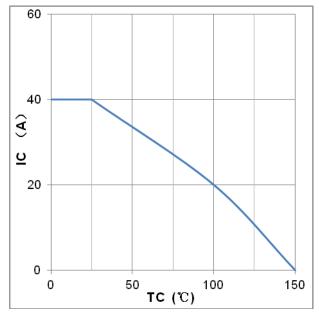
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =20A	-	2.0	2.8	V
trr	Diode Reverse Recovery Time	V _{CE} = 600V	1	172		ns
Irr	Diode peak Reverse Recovery Current	I _F = 20A	-	24		Α
Q _{r r}	Diode Reverse Recovery Charge	dl _F /dt = 500A/us	-	2415		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Performance Characteristics



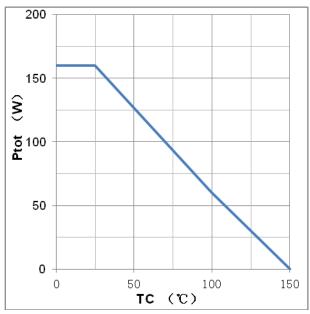
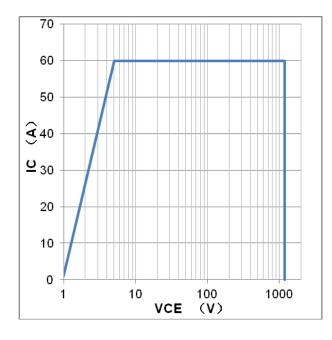


Figure1:maximum DC collector current VS. case temprature

Figure2:power dissipation VS. case temprature



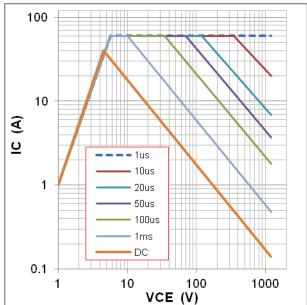
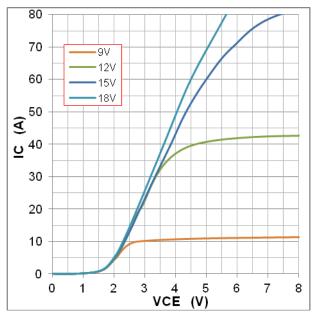


Figure3:reverse bias SOA,TJ=150°C,VGE=15V

Figure4:forward SOA,TC=25°C,TJ≤150°C



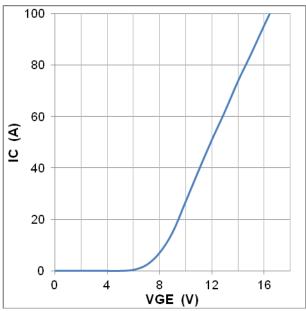
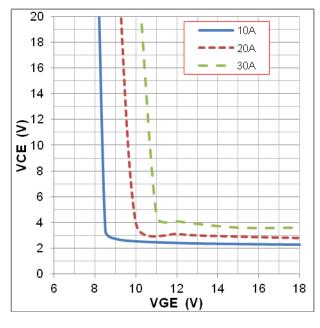


Figure 5: typical IGBT output characteristics, $TJ = 25\,^{\circ}\text{C}; tp = 300 \text{us}$

Figure6:typical trans characteristics, VCE=20V,tp=20us



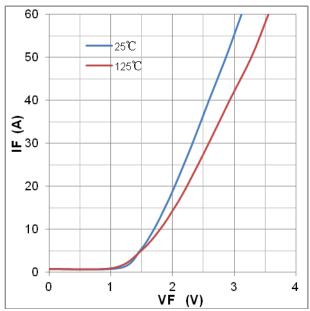
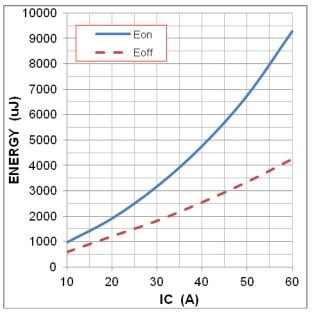


Figure7: typical VCE VS. VGE,TJ=25°C

Figure8:typical diode forward characteristic,tp=300us

<u>www.kedasemi.com</u> - 4 - Rev2 Cld20130615



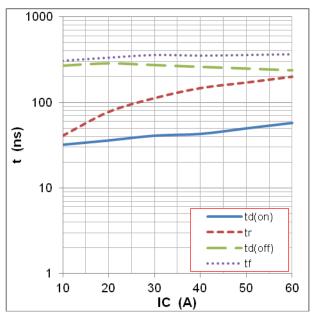
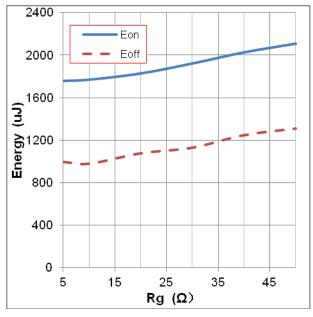


Figure9: typical energy loss VS. IC, TC=25°C,

L=500uH, VCE=600V,VGE=15V,Rg=28 Ω

Figure 10: typical switching time VS. IC, TC=25°C,

L=500uH, VCE=600V,VGE=15V,Rg= 28Ω



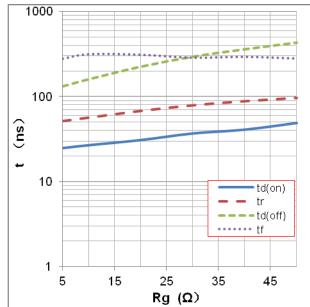
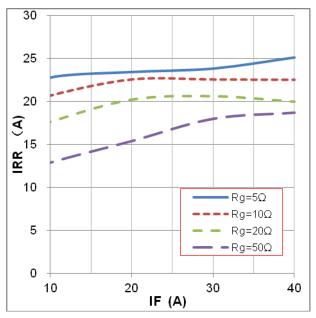


Figure11: typical energy loss VS. Rg,TC=25°C,

L=500uH, VCE=600V, VGE=15V,IC=20A

Figure12: typical switching time VS. Rg,TC=25°C,

L=500uH,VCE=600V,VGE=15V,IC=20A



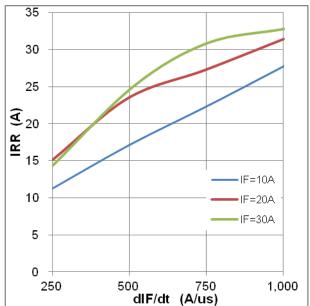
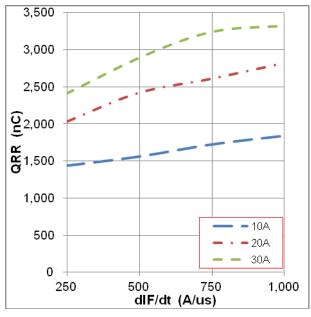


Figure 13: typical diode IRR VS. IF, TC=25°C $\,$

VCC=600V, VGE=15V

Figure14:typical diode IRR VS. dIF/dt

VCC=600V,VGE=15V



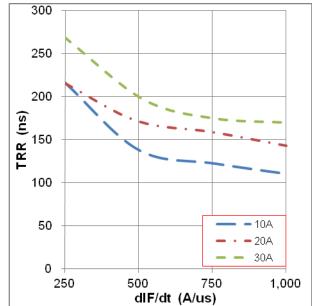


Figure15:typical diode QRR VS. dIF/dt

VCC=600V,VGE=15V

Figure16:typical diode TRR VS. dIF/dt,

VCC=600V,VGE=15V

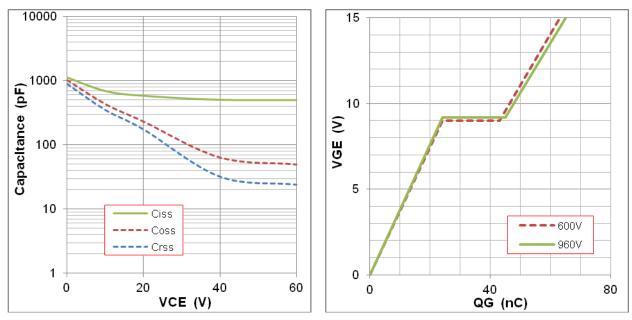


Figure17:typical capacitance VS. VCE, VGE=0V, f=100kHz Figure18:typical gate charge VS. VGE, IC=20A

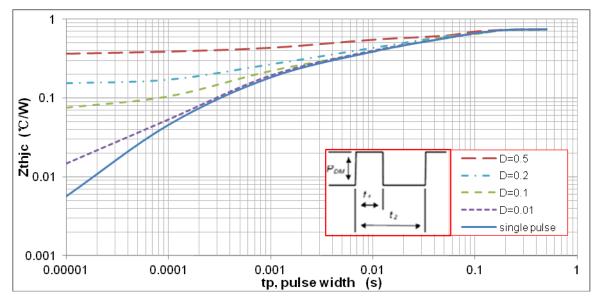
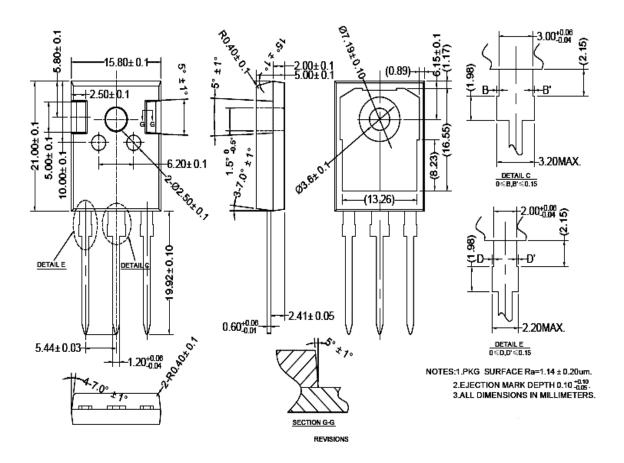


Figure 19: normalized transient thermal impedance, junction-to-case

Note1.Duty factor D=t1/t2; Note2:peak TJ=PDM×Zthjc+TC



TO247 PACKAGE OUTLINE



公差标注	会差值	表面粗糙度
0	±0.2	Ra3.2~6.3
0.0	±0.1	Ra1.6~3.2
0.00	±0.01	Ra0.8~1.6
0.000	±0.005	Ra0.4~0.8
0.0000	±0.002	Ra0.2~0.4

0≤D,D'≤0.15

NOTES:1.PKG SURFACE Ra=1.14 ± 0.20 um. 2.EJECTION MARK DEPTH 0.10 +0.10 3.ALL DIMENSIONS IN MILLIMETERS.



Disclaimers

KEDA Semiconductor Co., Ltd reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to KEDA's terms and conditions supplied at the time of order acknowledgement.

KEDA Semiconductor Co., Ltd warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent KEDA deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

KEDA Semiconductor Co., Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using KEDA's components. To minimize risk, customers must provide adequate design and operating safeguards.

KEDA Semiconductor Co., Ltd does not warrant or convey any license either expressed or implied under its parent rights, nor the rights of others. Reproduction of information in KEDA's datasheets or data books sis permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. KEDA Semiconductor Co., Ltd is not responsible or liable for such altered documentation.

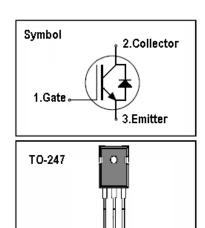
Resale of KEDA's products with statements different from or beyond the parameters stated by KEDA Semiconductor Co., Ltd for that product or service voids all express or implied warrantees for the associated KEDA's product or service and is unfair and deceptive business practice. KEDA Semiconductor Co., Ltd is not responsible or liable for any such statements.



IGBT

Features

- 1200V,20A
- $V_{CE(sat)(typ.)}$ =2.7V@ V_{GE} =15V, I_{C} =20A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms



GCE

General Description

KEDA PT IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating), and other soft switching applications.

Absolute Maximum Ratings

Symbol	Parameter	rameter Value Ur	
V _{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
	Continuous Collector Current (T _C =25 °C)	40	А
I _C	Continuous Collector Current (T _C =100°C)	20	А
I _{CM}	Pulsed Collector Current (Note 1)	60	А
I _F	Diode Continuous Forward Current (T_C =100 $^{\circ}$)	20	А
I _{FM}	Diode Maximum Forward Current (Note 1)	60	А
В	Maximum Power Dissipation (T _C =25 ℃)		W
P _D	P _D Maximum Power Dissipation (T _C =100℃)		W
T_J	Operating Junction Temperature Range	-55 to +150	${\mathbb C}$
T_{STG}	Storage Temperature Range	-55 to +150	${\mathbb C}$

Thermal Characteristics

Symbol	ymbol Parameter Max.		Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	0.74	°C/W
R _{th j-c}	Thermal Resistance, Junction to case for Diode	0.95	°C/W
R _{th j-a}	Thermal Resistance, Junction to Ambient	40	°C/W



$\underline{\textbf{Electrical Characteristics}} \text{ (T}_{\texttt{C}} = 25 \, ^{\circ}\!\! \text{C} \text{ unless otherwise noted)}$

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	1200	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 1200V, V _{GE} = 0V	-	-	250	uA
	Gate Leakage Current, Forward	V_{GE} =30V, V_{CE} = 0V	-	-	100	nA
GES	Gate Leakage Current, Reverse	V_{GE} = -30V, V_{CE} = 0V	-	-	-100	nA
$V_{\text{GE(th)}}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$, $I_{C} = 250uA$	4.0	-	6.0	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V_{GE} =15V, I_{C} = 20A	-	2.7		V
Qg	Total Gate Charge	V _{cc} =600V	-	63		nC
Q _{ge}	Gate-Emitter Charge	V _{GE} =000V V _{GE} =15V I _C =20A	-	24		nC
Qgc	Gate-Collector Charge		-	19		nC
t _{d(on)}	Turn-on Delay Time	V _{CC} =600V V _{GE} =15V	-	36	-	ns
t _r	Turn-on Rise Time		-	78	-	ns
t _{d(off)}	Turn-off Delay Time		-	285	-	ns
t f	Turn-off Fall Time	I _C =20A R _G =28Ω	-	331	-	ns
Eon	Turn-on Switching Loss	Inductive Load	-	1.9	-	mJ
Eoff	Turn-off Switching Loss	T _C =25 ℃	-	1.2	-	mJ
Ets	Total Switching Loss		-	3.1	-	mJ
C _{ies}	Input Capacitance	V _{CE} =25V V _{GE} =0V	-	550	-	pF
C _{oes}	Output Capacitance		-	180	-	pF
C _{res}	Reverse Transfer Capacitance	f = 100kHz	-	110	-	pF

Electrical Characteristics of Diode (T_C=25℃ unless otherwise noted)

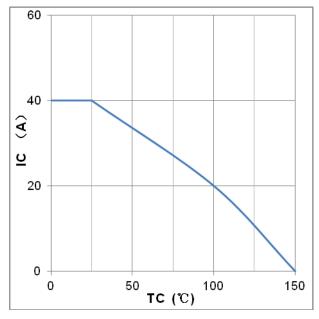
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =20A	-	2.0	2.8	V
trr	Diode Reverse Recovery Time	V _{CE} = 600V		172		ns
Irr	Diode peak Reverse Recovery Current	I _F = 20A		24		Α
Q _{r r}	Diode Reverse Recovery Charge	dl _F /dt = 500A/us	-	2415		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Performance Characteristics



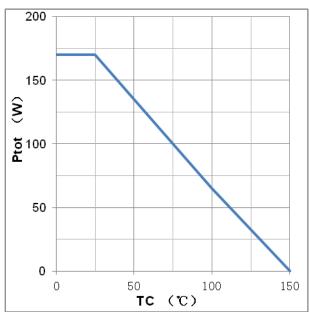
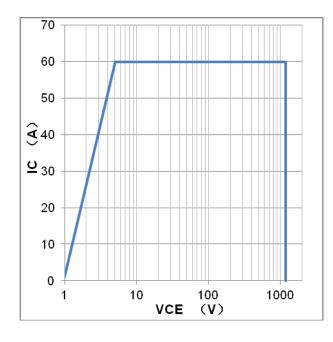


Figure1:maximum DC collector current VS. case temprature

Figure2:power dissipation VS. case temprature



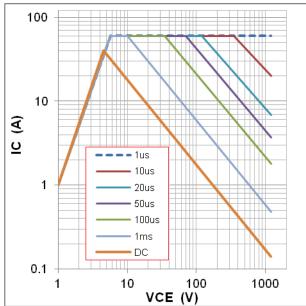
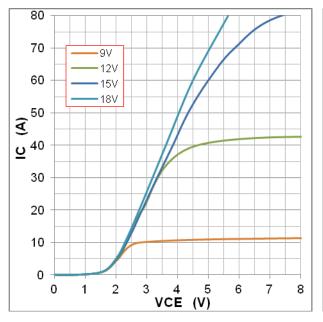


Figure3:reverse bias SOA,TJ=150°C,VGE=15V

Figure4:forward SOA,TC=25°C,TJ≤150°C



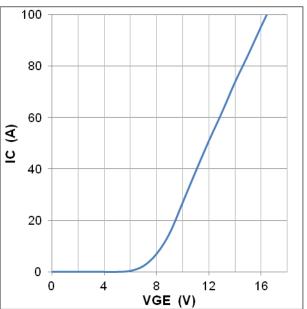
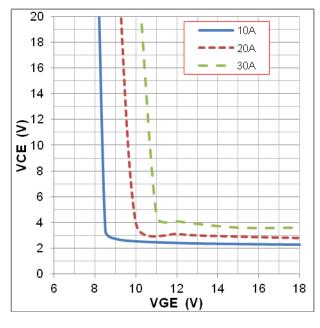


Figure 5: typical IGBT output characteristics, $TJ = 25\,^{\circ}\text{C}; tp = 300 \text{us}$

Figure6:typical trans characteristics, VCE=20V,tp=20us



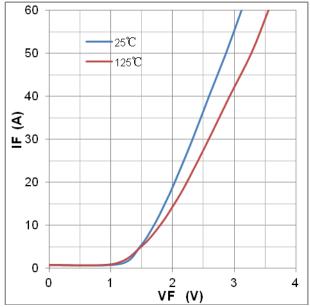
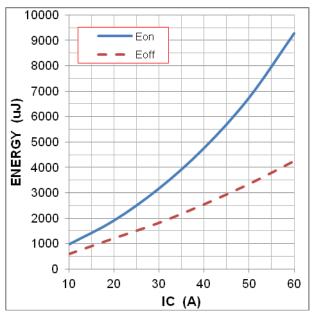


Figure7: typical VCE VS. VGE,TJ=25°C

Figure8:typical diode forward characteristic,tp=300us

<u>www.kedasemi.com</u> - 4 - Rev2 Cld20130615



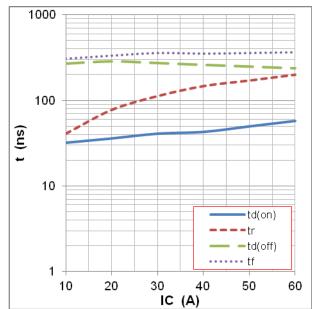
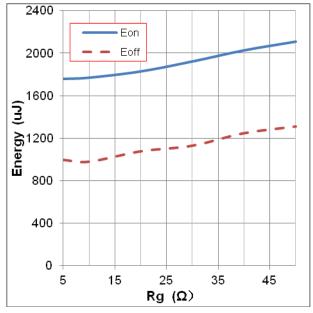


Figure9: typical energy loss VS. IC, TC=25°C,

L=500uH, VCE=600V,VGE=15V,Rg=28 Ω

Figure 10: typical switching time VS. IC, TC=25°C,

L=500uH, VCE=600V,VGE=15V,Rg= 28Ω



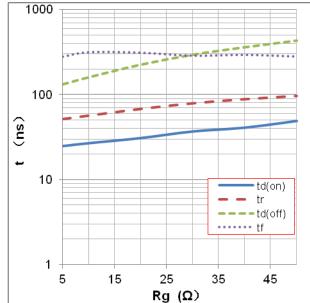
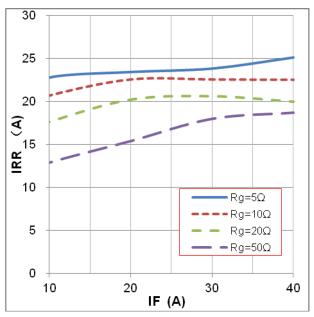


Figure11: typical energy loss VS. Rg,TC=25°C,

L=500uH, VCE=600V, VGE=15V,IC=20A

Figure12: typical switching time VS. Rg,TC=25°C,

L=500uH,VCE=600V,VGE=15V,IC=20A



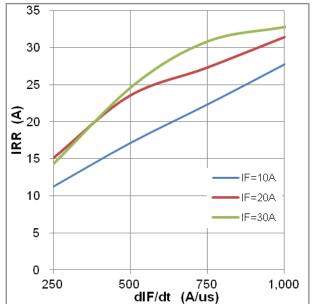
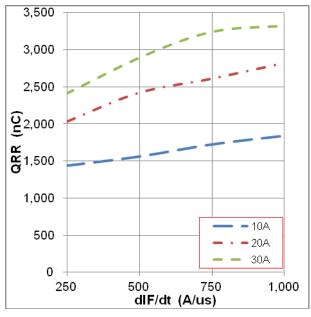


Figure13: typical diode IRR VS. IF, TC=25°C

VCC=600V, VGE=15V

Figure14:typical diode IRR VS. dIF/dt

VCC=600V,VGE=15V



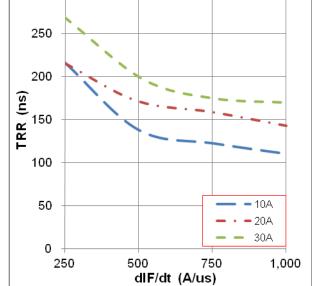


Figure15:typical diode QRR VS. dIF/dt

VCC=600V,VGE=15V

Figure16:typical diode TRR VS. dIF/dt,

VCC=600V,VGE=15V

300

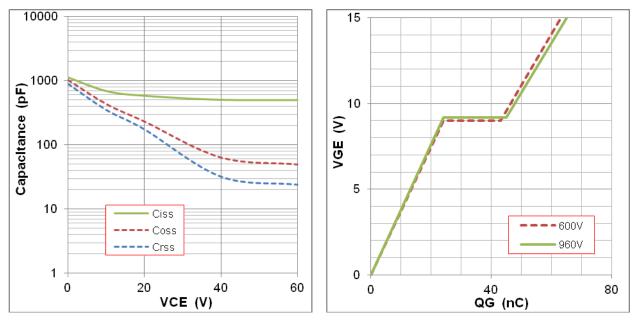


Figure17:typical capacitance VS. VCE, VGE=0V, f=100kHz Figure18:typical gate charge VS. VGE, IC=20A

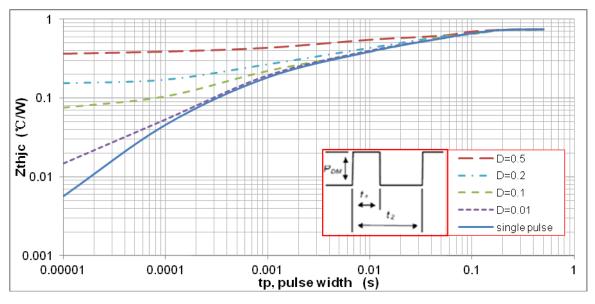
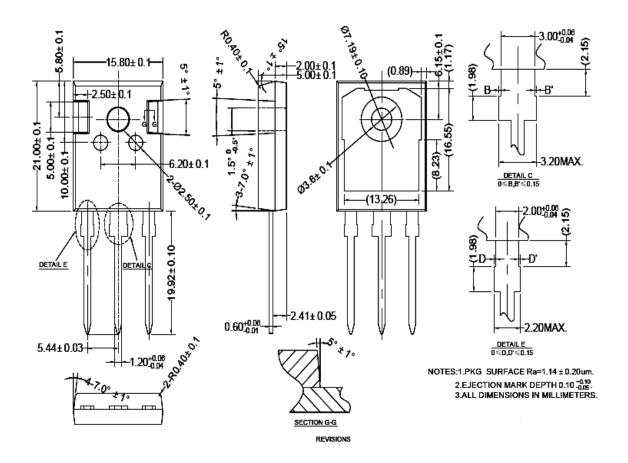


Figure 19: normalized transient thermal impedance, junction-to-case

Note1.Duty factor D=t1/t2; Note2:peak TJ=PDM×Zthjc+TC



TO247 PACKAGE OUTLINE



公差标注	公差值	表面粗糙度
0	±0.2	Ra3.2~6.3
0.0	±0.1	Ra1.6~3.2
0.00	±0.01	Ra0.8~1.6
0.000	±0.005	Ra0.4~0.8
0.0000	±0.002	Ra0.2~0.4

0≤D,D'≤0.15

NOTES:1.PKG SURFACE Ra=1.14 ± 0.20 um. 2.EJECTION MARK DEPTH 0.10 +0.10 3.ALL DIMENSIONS IN MILLIMETERS.



Disclaimers

KEDA Semiconductor Co., Ltd reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to KEDA's terms and conditions supplied at the time of order acknowledgement.

KEDA Semiconductor Co., Ltd warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent KEDA deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

KEDA Semiconductor Co., Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using KEDA's components. To minimize risk, customers must provide adequate design and operating safeguards.

KEDA Semiconductor Co., Ltd does not warrant or convey any license either expressed or implied under its parent rights, nor the rights of others. Reproduction of information in KEDA's datasheets or data books sis permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. KEDA Semiconductor Co., Ltd is not responsible or liable for such altered documentation.

Resale of KEDA's products with statements different from or beyond the parameters stated by KEDA Semiconductor Co., Ltd for that product or service voids all express or implied warrantees for the associated KEDA's product or service and is unfair and deceptive business practice. KEDA Semiconductor Co., Ltd is not responsible or liable for any such statements.