

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for LED Converter and switching mode power supplies.

FEATURES

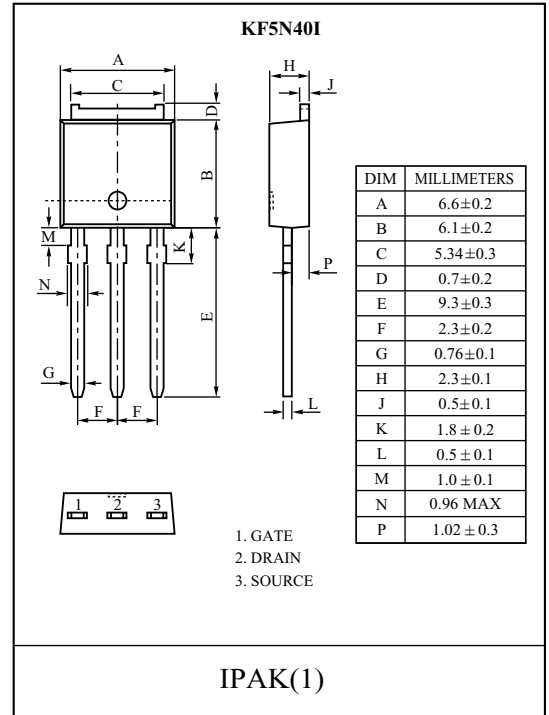
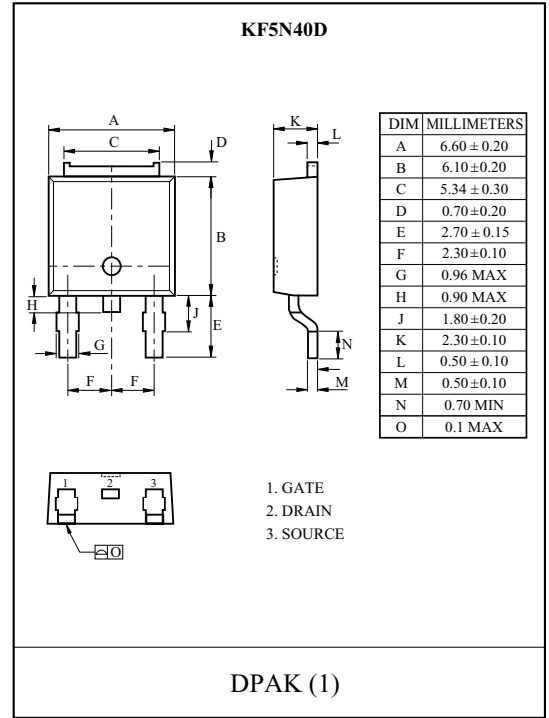
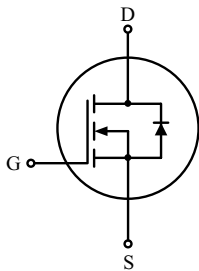
- $V_{DSS(Min.)} = 400V$, $I_D = 4.2A$
- Drain-Source ON Resistance : $R_{DS(ON)} = 1.6$ (max) @ $V_{GS} = 10V$
- $Q_g(typ.) = 5.5 nC$

MAXIMUM RATING (Tc=25)

| CHARACTERISTIC | SYMBOL | RATING | UNIT |
|---|-----------------|----------|------|
| Drain-Source Voltage | V_{DSS} | 400 | V |
| Gate-Source Voltage | V_{GSS} | ± 30 | V |
| Drain Current | @ $T_C=25$ | 4.2 | A |
| | @ $T_C=100$ | 2.8 | |
| | Pulsed (Note1) | 10* | |
| Single Pulsed Avalanche Energy (Note 2) | E_{AS} | 70 | mJ |
| Repetitive Avalanche Energy (Note 1) | E_{AR} | 2.7 | mJ |
| Peak Diode Recovery dv/dt (Note 3) | dv/dt | 4.5 | V/ns |
| Drain Power Dissipation | $T_C=25$ | 50 | W |
| | Derate above 25 | 0.4 | W/ |
| Maximum Junction Temperature | T_j | 150 | |
| Storage Temperature Range | T_{stg} | -55 150 | |
| Thermal Characteristics | | | |
| Thermal Resistance, Junction-to-Case | R_{thJC} | 2.5 | /W |
| Thermal Resistance, Junction-to-Ambient | R_{thJA} | 110 | /W |

* : Drain current limited by maximum junction temperature.

PIN CONNECTION



KF5N40D/I

ELECTRICAL CHARACTERISTICS (Tc=25)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---|----------------|---|------|------|-----------|---------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $I_D=250\mu A, V_{GS}=0V$ | 400 | - | - | V |
| Breakdown Voltage Temperature Coefficient | BV_{DSS}/T_j | $I_D=250\mu A$, Referenced to 25 | - | 0.4 | - | V/ |
| Drain Cut-off Current | I_{DSS} | $V_{DS}=400V, V_{GS}=0V$, | - | - | 10 | μA |
| Gate Threshold Voltage | V_{th} | $V_{DS}=V_{GS}, I_D=250\mu A$ | 2.5 | - | 4.5 | V |
| Gate Leakage Current | I_{GSS} | $V_{GS}=\pm 30V, V_{DS}=0V$ | - | - | ± 100 | nA |
| Drain-Source ON Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=2.1A$ | - | 1.3 | 1.6 | |
| Dynamic | | | | | | |
| Total Gate Charge | Q_g | $V_{DS}=320V, I_D=5A$ $V_{GS}=10V$ (Note4,5) | - | 5.4 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 1.5 | - | |
| Gate-Drain Charge | Q_{gd} | | - | 2.5 | - | |
| Turn-on Delay time | $t_{d(on)}$ | $V_{DD}=200V, I_D=5A$ $R_G=25$ (Note4,5) $V_{GS}=10V$ | - | 20 | - | ns |
| Turn-on Rise time | t_r | | - | 20 | - | |
| Turn-off Delay time | $t_{d(off)}$ | | - | 25 | - | |
| Turn-off Fall time | t_f | | - | 15 | - | |
| Input Capacitance | C_{iss} | $V_{DS}=25V, V_{GS}=0V, f=1.0MHz$ | - | 310 | - | pF |
| Output Capacitance | C_{oss} | | - | 50 | - | |
| Reverse Transfer Capacitance | C_{rss} | | - | 5 | - | |
| Source-Drain Diode Ratings | | | | | | |
| Continuous Source Current | I_S | $V_{GS}<V_{th}$ | - | - | 4.2 | A |
| Pulsed Source Current | I_{SP} | | - | - | 16.8 | |
| Diode Forward Voltage | V_{SD} | $I_S=4.2A, V_{GS}=0V$ | - | - | 1.4 | V |
| Reverse Recovery Time | t_{rr} | $I_S=5A, V_{GS}=0V$, | - | 260 | - | ns |
| Reverse Recovery Charge | Q_{rr} | $dI_S/dt=100A/\mu s$ | - | 1.1 | - | μC |

Note 1) Repetivity rating : Pulse width limited by junction temperature.

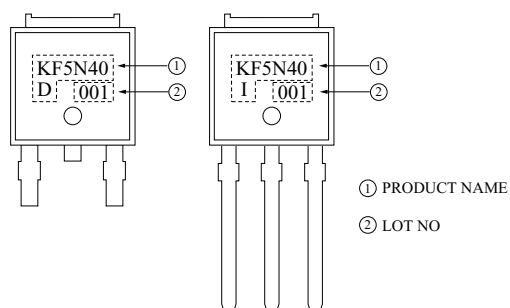
Note 2) $L = 4.9 mH, I_S=5A, V_{DD}=50V, R_G = 25$, Starting $T_j = 25$.

Note 3) $I_S = 5A, dI/dt = 100A/\mu s, V_{DD} = BV_{DSS}$, Starting $T_j = 25$.

Note 4) Pulse Test : Pulse width $300\mu s$, Duty Cycle 2% .

Note 5) Essentially independent of operating temperature.

Marking



Normalized Breakdown Voltage BV_{DSS}

$V_{GS} = 0V$
 $I_{DS} = 250 \mu A$

0.9
0.8
-10

$V_{GS}=10V$

$V_{GS}=7V$

$V_{GS}=5V$

$V_{DS}=30V$

$T_C=25^\circ C$

$T_C=100^\circ C$

$V_{GS}=7V$

$V_{GS}=10V$

Drain Current I_D (A)

$T_C=100^\circ C$

$25^\circ C$

KF5N40D/I

Fig 7. C - V_{DS}

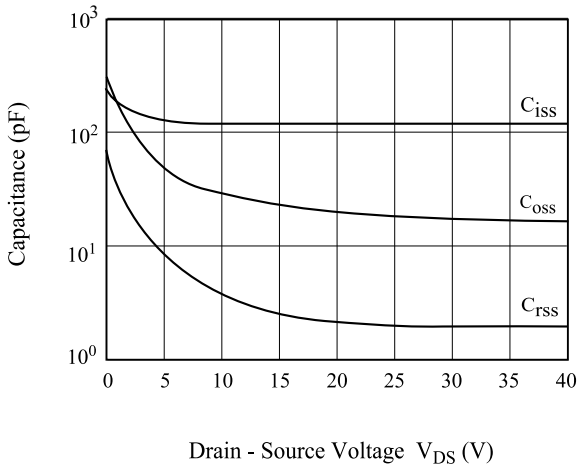


Fig8. Q_g- V_{GS}

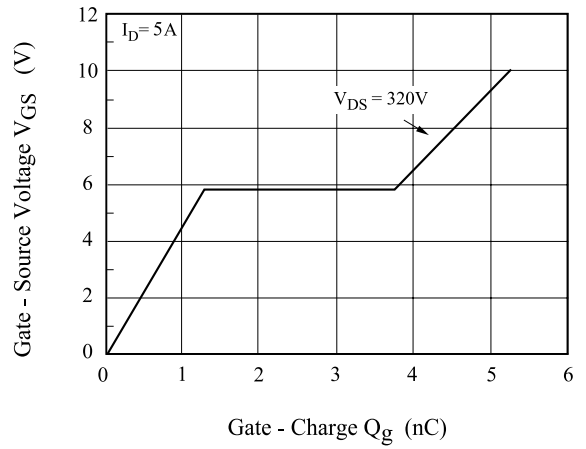


Fig9. Safe Operation Area

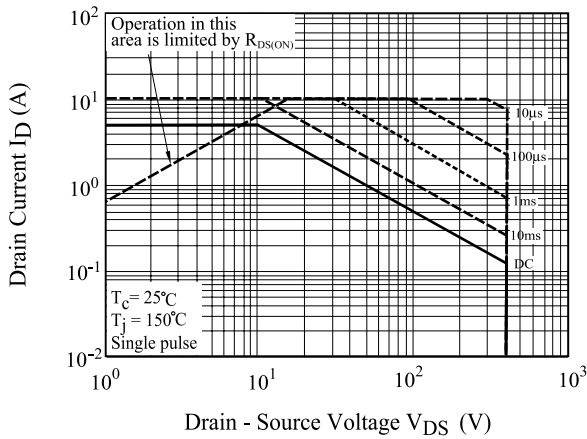


Fig10. I_D - T_j

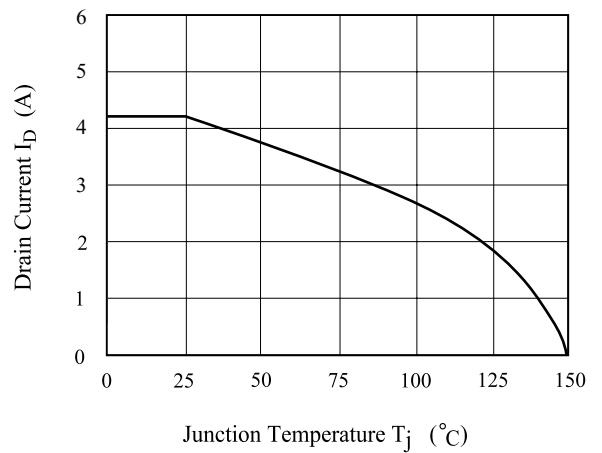


Fig11. Transient Thermal Response Curve

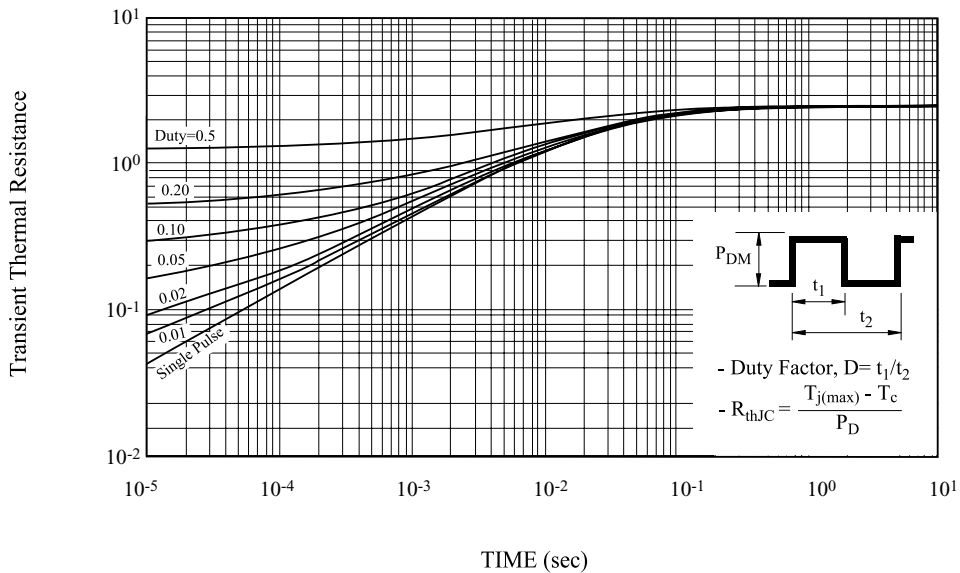


Fig12. Gate Charge

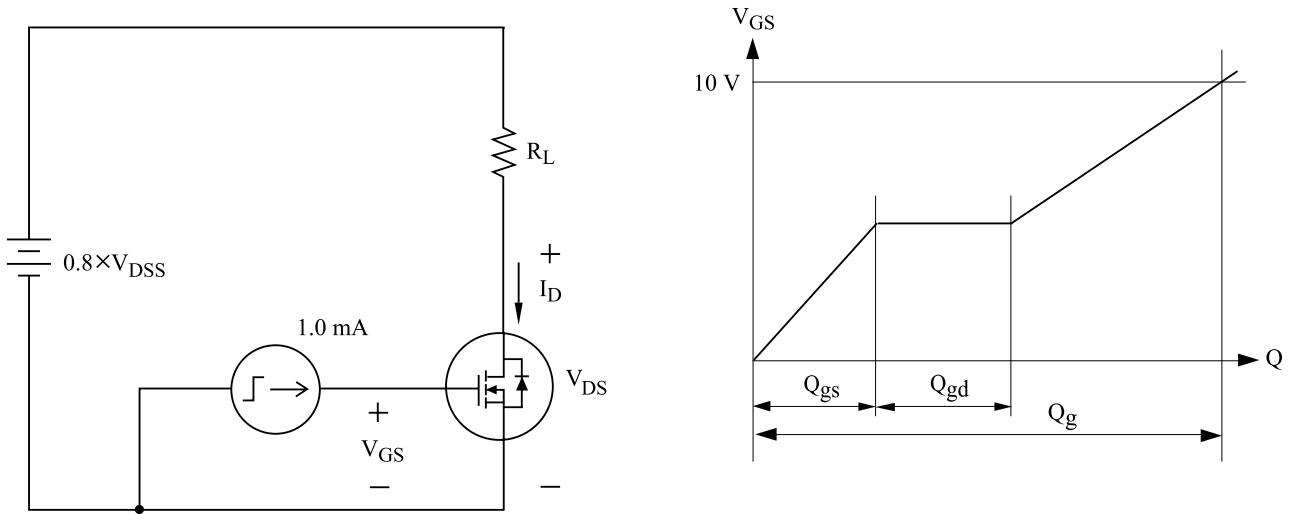


Fig13. Single Pulsed Avalanche Energy

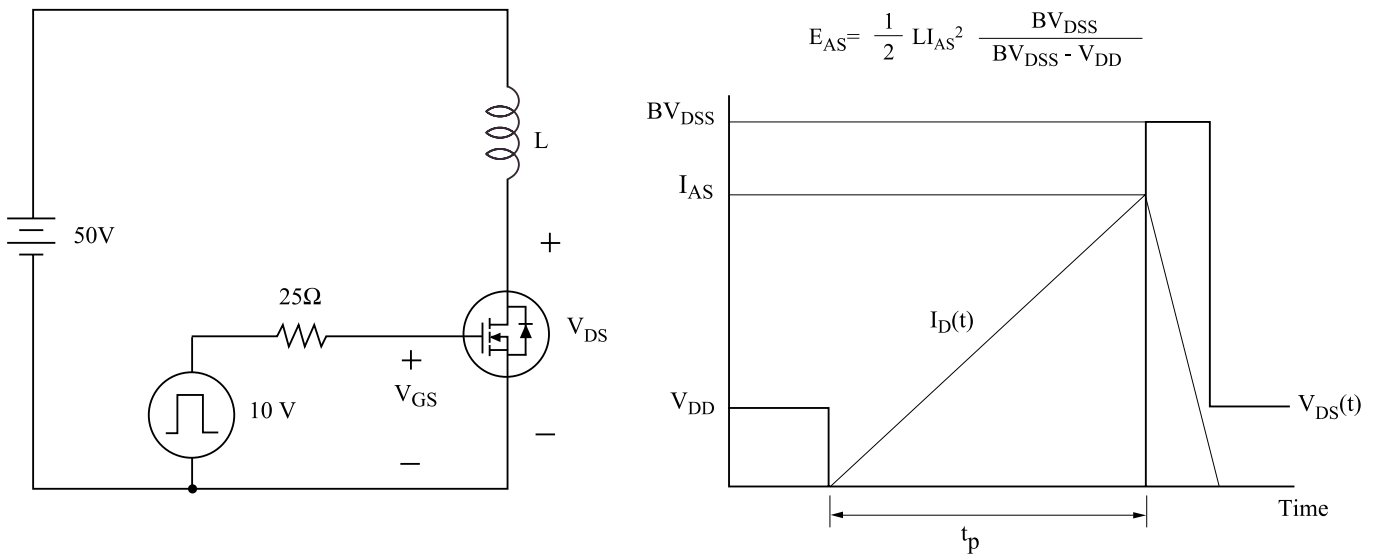


Fig14. Resistive Load Switching

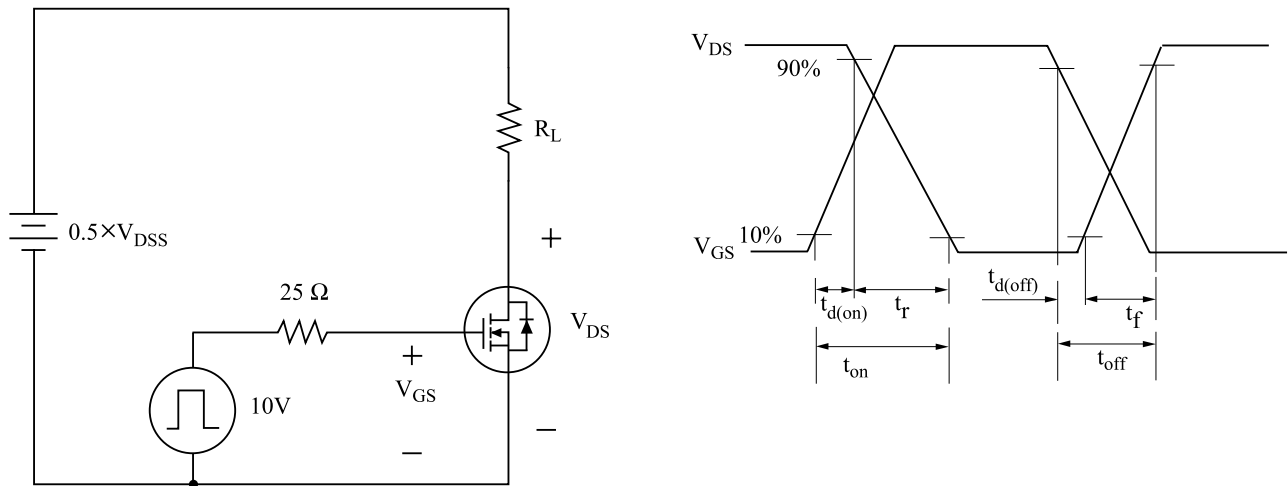


Fig15. Source - Drain Diode Reverse Recovery and dv/dt

