SPEC

Spec No.	TQ3C 8EAS0 E1DAM26 00
Date	April 19, 2007

TYPE : KG057QVLCD-G060

< 5.7 inch QVGA transflective monochrome STN
with LED backlight>

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Issued
Date : May 18, 2007
KYOCERA
Hayato LCD Division

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.	
	Prepared	Checked	Approved	Checked	Approved
April 19, 2007	O. Ajisaka	S. Oshita	Y. Matsumoto	J. Sakaguchi	

Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.

2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision record

Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev.No.	Date	Page	Descriptions

1. Application

This document defines the specification of KG057QVLCD-G060. (RoHS Compliant)

2. Construction and outline

LCD : Transflective monochrome dot matrix type STN
Duty ratio : 1/240 duty
Backlight system : LED
Polarizer : Glare treatment
Additional circuit : Bias voltage circuit, Randomizing circuit, DC/DC converter circuit, Temperature compensation circuit

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions	144 (W)× (104.8) (H) × 12.7 (D)	mm
Active area	115.18 (W) × 86.38 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Effective viewing area	117.2 (W) × 88.4 (H)	mm
Dot format	320 (W) × 240 (H)	dot
Dot size	0.34 (W) × 0.34 (H)	mm
Dot pitch	0.36 (W) × 0.36 (H)	mm
Base color *1	Normally Black	-
Mass	175	g

*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V_{DD}	0	6.0	V
Supply voltage for LCD driving	V_{CONT}	0	V_{DD}	V
Input signal voltage *1	V_{IN}	0	V_{DD}	V
FRM frequency	f_{FRM}	-	150	Hz
LED forward current *2	IF	-	27	mA
Reversed voltage *2	VR	-	5	V

*1 Input signal : FRM, LOAD, CP, DISP, D0～D3
*2 For each “AN1-CA1”, “AN2-CA2” and “AN3-CA3”

4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature *1	T_{OP}	-20	65	°C
Storage temperature *2	T_{STO}	-30	80	°C
Operating humidity *3	H_{OP}	10	*4	%RH
Storage humidity *3	H_{STO}	10	*4	%RH
Vibration	-	*5	*5	-
Shock	-	*6	*6	-

*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

*2 Temp. = -30°C＜48h , Temp. = 80°C＜168h

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to “Precautions for Use” for details.)

*3 Non-condensing

*4 Temp.≦40°C, 85%RH Max.

Temp.＞40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10～55 Hz	Acceleration value (0.3～9 m/s^2)
Vibration width	0.15mm	
Interval	10-55-10 Hz 1 minutes	

2 hours in each direction X, Y, Z (6 hours total)
EIAJ ED-2531

*6 Acceleration: 490 m/s^2, Pulse width: 11 ms
3 times in each direction: ±X, ±Y, ±Z
EIAJ ED-2531

5. Electrical characteristics

5-1. V_{DD} = 5.0V

V_{DD} = +5.0V±5%, Temp. = -20～65°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	V_{DD}	-	4.75	5.00	5.25	V
Supply voltage for LCD driving *1, *2	V_{CONT}= V_{OP}	-20°C～	0.80	-	-	V
		0～50°C *3	1.45	1.95	2. 45	V
		～65°C	-	-	2.80	V
Input signal voltage (FRM,LOAD,CP,DISP,D0～D3)	V_{IN}	"High" level	0.8V_{DD}	-	V_{DD}	V
		"Low" level	0	-	0.2V_{DD}	V
Input current	I_{IN}	Input signal	-100	-	100	μA
Rush current for logic	I_{RUSH}	When LCD turn on.	3.0A (Peak) × 1ms			
Clock frequency	f_{CP}	-	-	-	10.00	MHz
Frame frequency *4	f_{FRM}	-	70	75	80	Hz
Current consumption for logic	I_{DD}	*5	-	17	22	mA
Power consumption	P_{DISP}		-	85	110	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (V_{CONT} = V_{OP}) for driving the LCD.

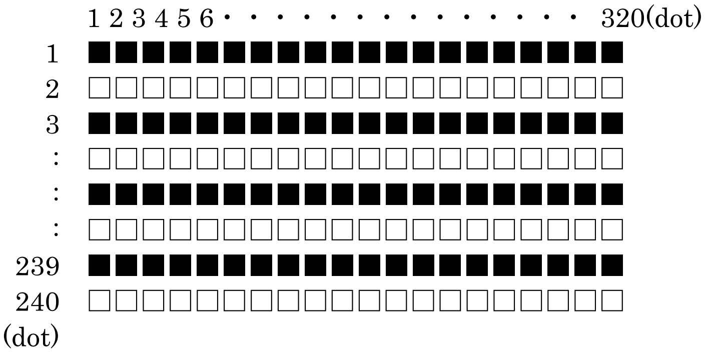
*2 Frame frequency : f_{FRM} = 75Hz

*3 The LCD module has a temperature compensation circuit.

*4 In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

*5 Display pattern:

V_{DD} = 5.0V, V_{CONT} = V_{OP}, f_{FRM} = 75Hz, f_{CP} = 1.44MHz, Temp. = 25°C



5-2. V_{DD} = 3.3V

V_{DD} =+3.3V±0.3V, Temp. = -20～65°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	V_{DD}	-	3.0	3.3	3.6	V
Supply voltage for LCD driving *1, *2	$V_{CONT}=V_{OP}$	-20°C～	0.80	-	-	V
		0～50°C *3	1.45	1.95	2. 45	V
		～65°C	-	-	2.80	V
Input signal voltage (FRM,LOAD,CP,DISP,D0～D3)	V_{IN}	"High" level	$0.8V_{DD}$	-	V_{DD}	V
		"Low" level	0	-	$0.2V_{DD}$	V
Input current	I_{IN}	Input signal	-100	-	100	μA
Rush current for logic	I_{RUSH}	When LCD turn on.	3.0A (Peak) × 1ms			
Clock frequency	f_{CP}	-	-	-	10.00	MHz
Frame frequency *4	f_{FRM}	-	70	75	80	Hz
Current consumption for logic	I_{DD}	*5	-	25	33	mA
Power consumption	P_{DISP}		-	83	109	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage ($V_{CONT}=V_{OP}$) for driving the LCD.

*2 Frame frequency : f_{FRM} = 75Hz

*3 The LCD module has a temperature compensation circuit.

*4 In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

*5 Display pattern:

V_{DD} = 3.3V, V_{CONT} = V_{OP}, f_{FRM} = 75Hz, f_{CP} = 1.44MHz, Temp. = 25°C

1 2 3 4 5 6 · · · · · · · · · · · · · · 320(dot)
1 ■■■■■■■■■■■■■■■■■■■■
2 □□□□□□□□□□□□□□□□□□□□
3 ■■■■■■■■■■■■■■■■■■■■
: □□□□□□□□□□□□□□□□□□□□
: ■■■■■■■■■■■■■■■■■■■■
: □□□□□□□□□□□□□□□□□□□□
239 ■■■■■■■■■■■■■■■■■■■■
240 □□□□□□□□□□□□□□□□□□□□
(dot)

6. Optical characteristics

6-1. Reflective mode

Measuring spot = φ6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_r	θ=φ=0°	-	380	480	ms
	Down	τ_d	θ=φ=0°	-	120	220	ms
Contrast ratio		CR	θ=φ=0°	(2.0)	(4.0)	-	-
Reflectance		ρ	Backlight off	(4.0)	(8.5)	-	%

Optimum contrast is obtained by adjusting the LCD driving voltage (V_{OP}) while at the viewing angle of θ=φ=0°.

6-2. Trasmissive mode

Measuring spot = φ6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_r	θ=φ=0°	-	380	480	ms
	Down	τ_d	θ=φ=0°	-	120	220	ms
Viewing angle range		θ UPPER	CR≧2	-	20	-	deg.
		θ LOWER		-	35	-	
		φ LEFT		-	35	-	deg.
		φ RIGHT		-	35	-	
Contrast ratio		CR	θ=φ=0°	(2.0)	(3.0)	-	-
Brightness		L	IF=25mA/Line	(100)	(150)	-	cd/m^2
Chromaticity coordinates	White	x	θ=φ=0°	TBD	TBD	TBD	-
		y		TBD	TBD	TBD	
	Black	x	θ=φ=0°	TBD	TBD	TBD	
		y		TBD	TBD	TBD	

Optimum contrast is obtained by adjusting the LCD driving voltage (V_{OP}) while at the viewing angle of θ=φ=0°.

6-3. Definition of reflectance

$$\rho\ (\text{Reflectance}) = \frac{\text{Measured reflection brightness}}{\text{Reflection brightness against standard white board}} \times 100[\%]$$

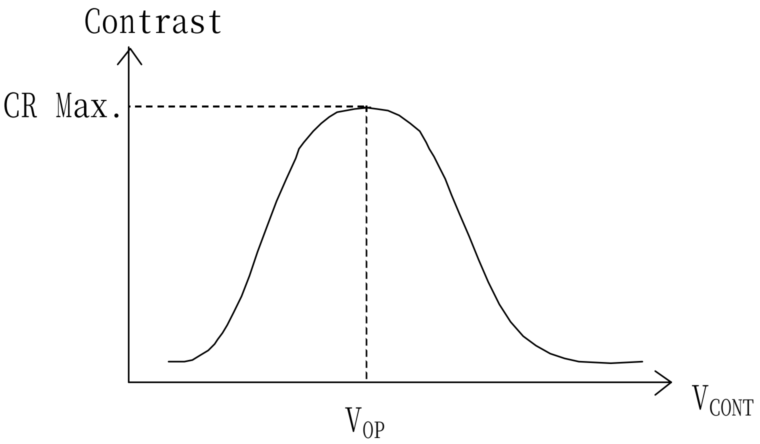
6-4. Definition of contrast ratio (Reflective mode)

$$\text{CR(Contrast ratio)} = \frac{\text{Reflectance at all pixel “white”}}{\text{Reflectance at all pixel “black”}} \times 100[\%]$$

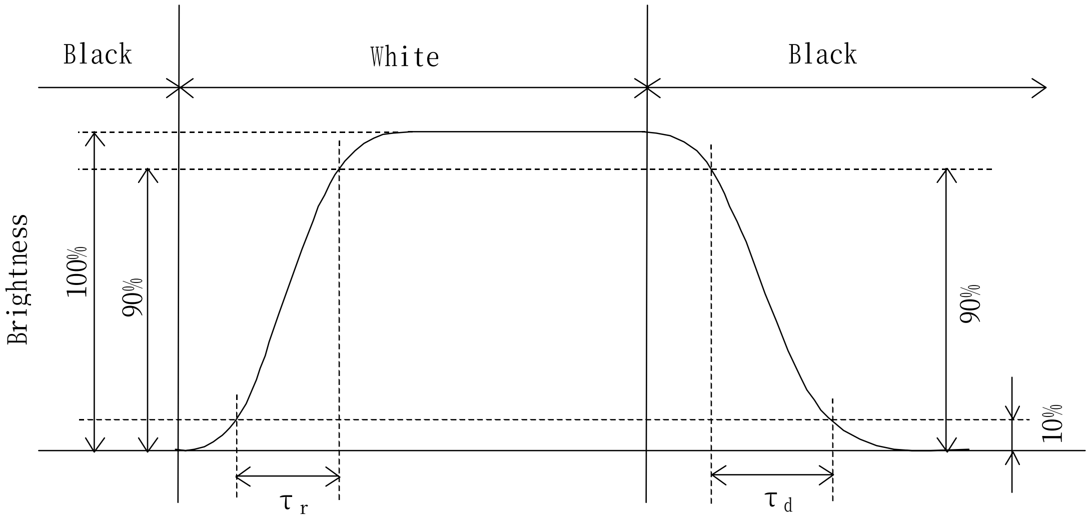
6-5. Definition of contrast ratio (Transmissive mode)

$$\text{CR(Contrast ratio)} = \frac{\text{Brightness at all pixel "white"}}{\text{Brightness at all pixel "black"}} \times 100[\%]$$

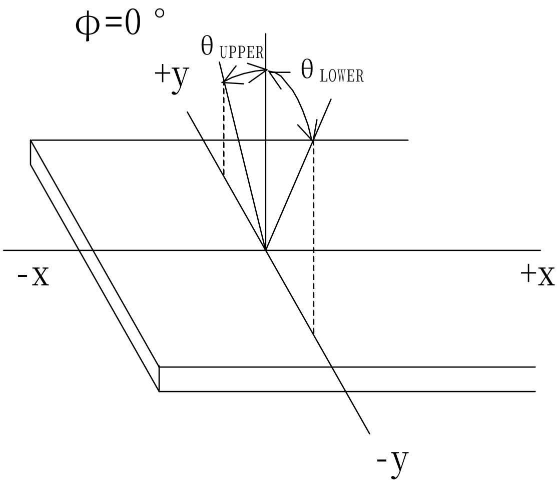
6-6. Definition of V_{OP}



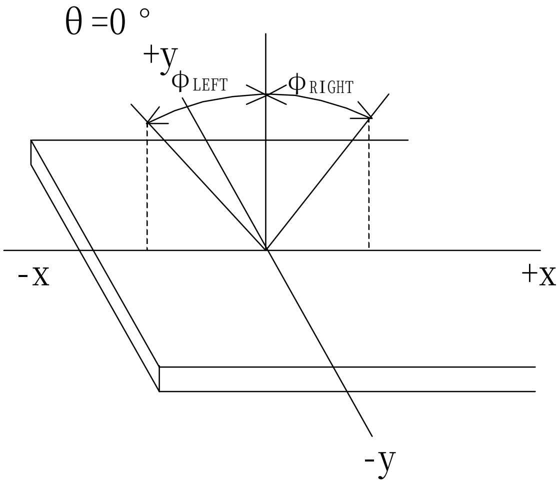
6-7. Definition of response time


Black
White
Black
Brightness
100%
90%
90%
10%
τ_r
τ_d

6-8. Definition of viewing angle

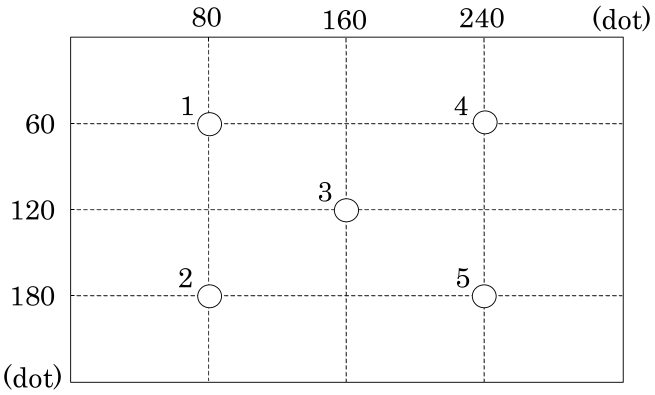

φ=0 °
θ UPPER
θ LOWER
+y
-x
+x
-y

θ direction


θ =0 °
+y
φ LEFT
φ RIGHT
-x
+x
-y

φ direction

6-9. Brightness measuring points


80
160
240
(dot)
60
120
180
(dot)
1
2
3
4
5

1) Rating is defined on the average in the viewing area.
2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)
3) Backlight : IF=25mA / 1 LED line

6-10. Measurement method of reflectance

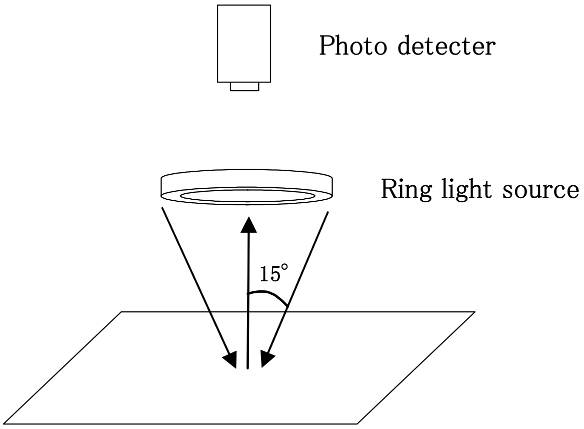
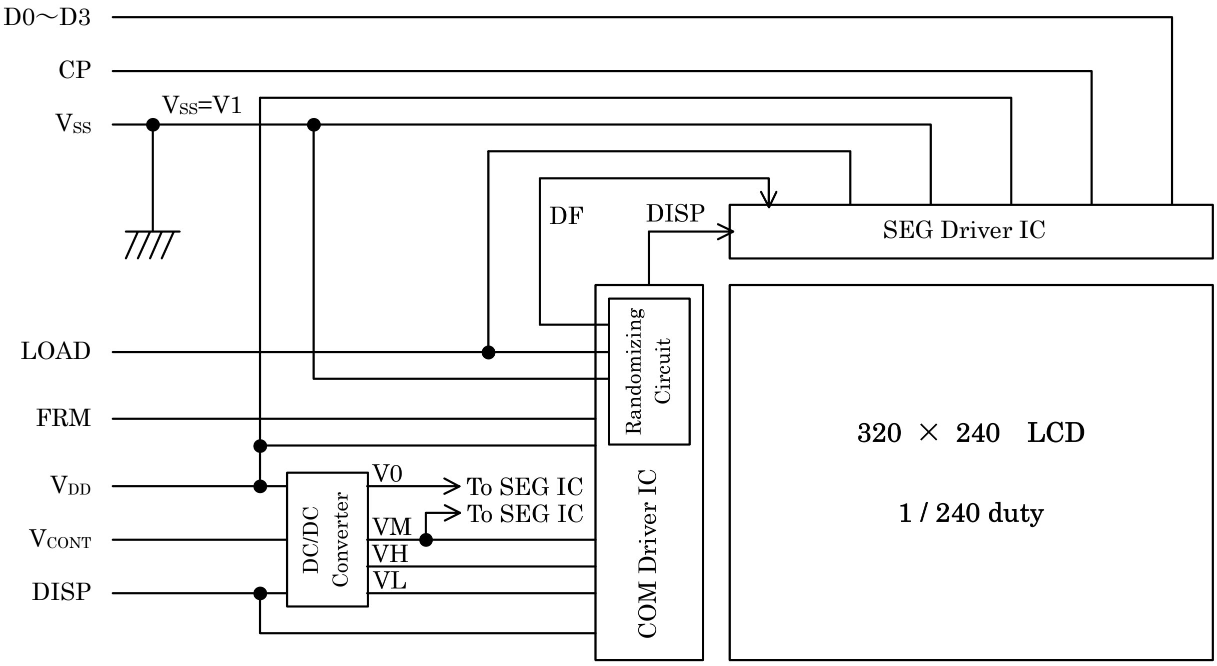


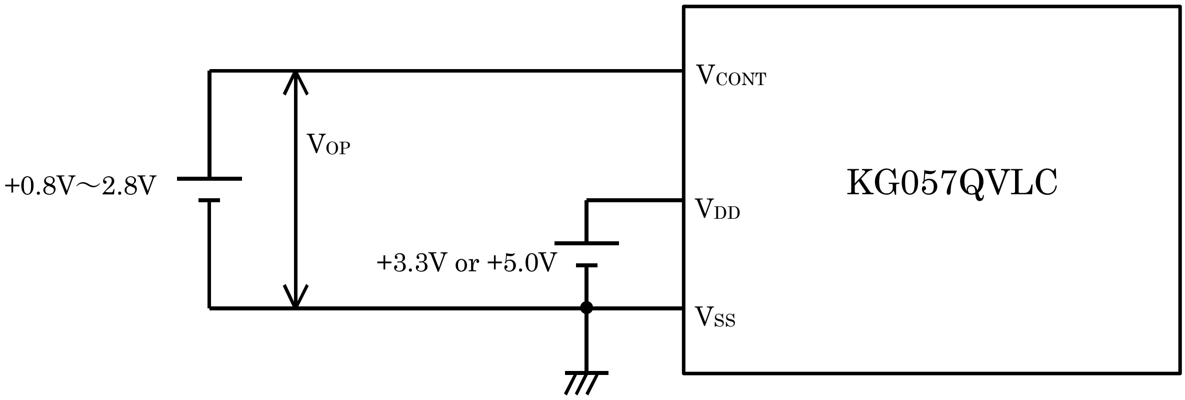
Photo detecter
Ring light source
15°

Measurement : 52002 (YOKOGAWA)

7. Circuit block diagram


D0～D3
CP
VSS
VSS=V1
DF
DISP
SEG Driver IC
LOAD
Randomizing Circuit
FRM
VDD
VCONT
DISP
DC/DC Converter
V0
VM
VH
VL
To SEG IC
To SEG IC
COM Driver IC
320 × 240 LCD
1 / 240 duty

7-1. Power supply


VCONT
VOP
+0.8V～2.8V
VDD
KG057QVLC
+3.3V or +5.0V
VSS

8. Interface signals

8-1. Pin assignment of LCD panel

No.	Symbol	Description	Level
1	FRM	Synchronous signal for driving scanning line	H
2	LOAD	Data signal latch clock	H → L
3	CP	Data signal shift clock	H → L
4	DISP	Display control signal	H(ON),L(OFF)
5	V_{DD}	Power supply for logic	-
6	V_{SS}	GND	-
7	V_{CONT}	LCD adjust voltage	-
8	N.C.	No connect	-
9	N.C.		
10	N.C.		
11	N.C.		
12	D3	Display data	H(ON),L(OFF)
13	D2		
14	D1		
15	D0		
16	V_{DD}	Power supply for logic	-
17	V_{DD}		
18	V_{SS}	GND	-
19	V_{SS}		
20	V_{SS}		

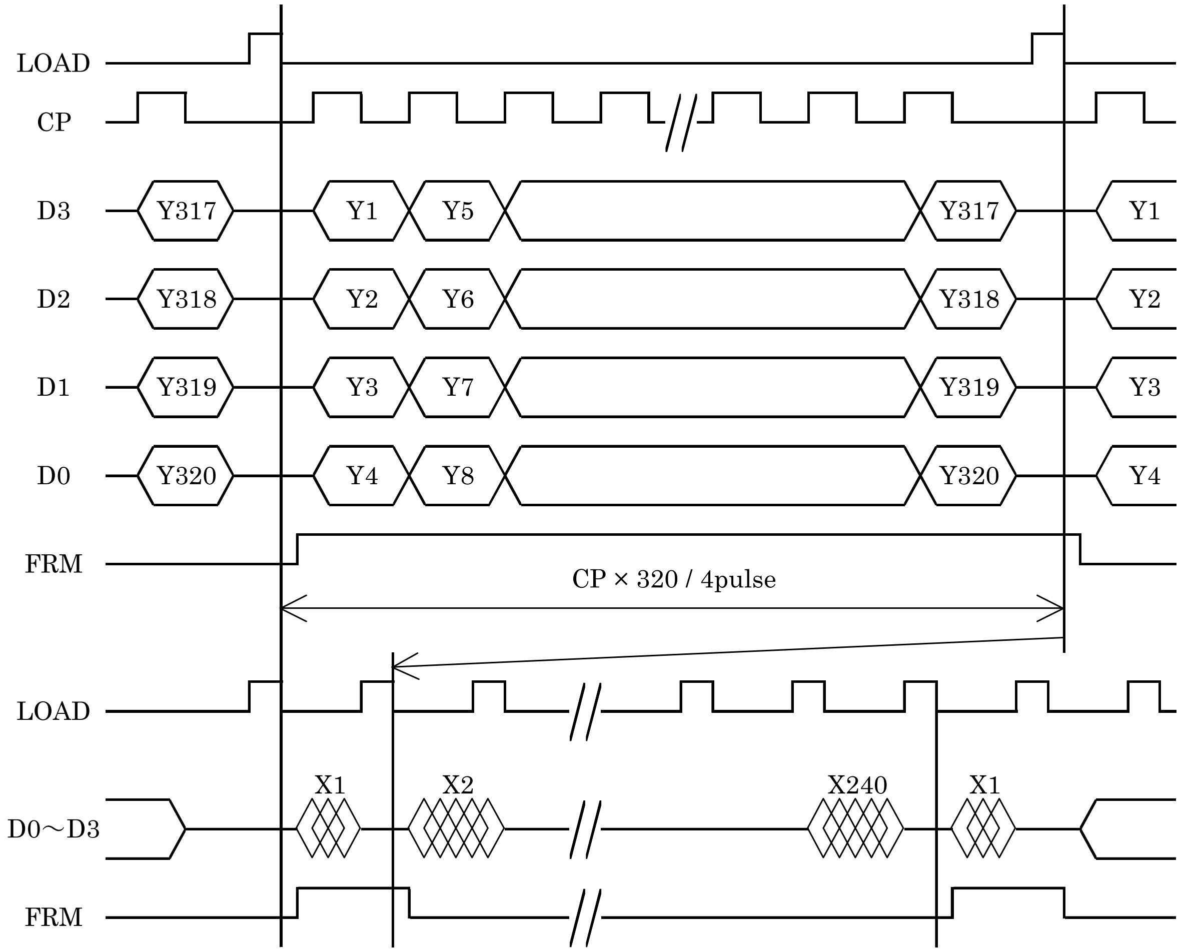
LCD connector : 08-6210-020-340-800+ (ELCO)
Recommended matching FFC or FPC : 0.5mm pitch

8-2. Pin assignment of LED

No.	Symbol	Description
1	AN1	Anode 1
2	AN2	Anode 2
3	AN3	Anode 3
4	CA1	Cathode 1
5	CA2	Cathode 2
6	CA3	Cathode 3

LCD side connector : SHLP-06V-S-B (JST)
Recommended matching connector
: SM06B-SHLS-TF (JST)
: SM06B-SHLS-TF(LF)(SN) (JST)・・・(RoHS Compliant)

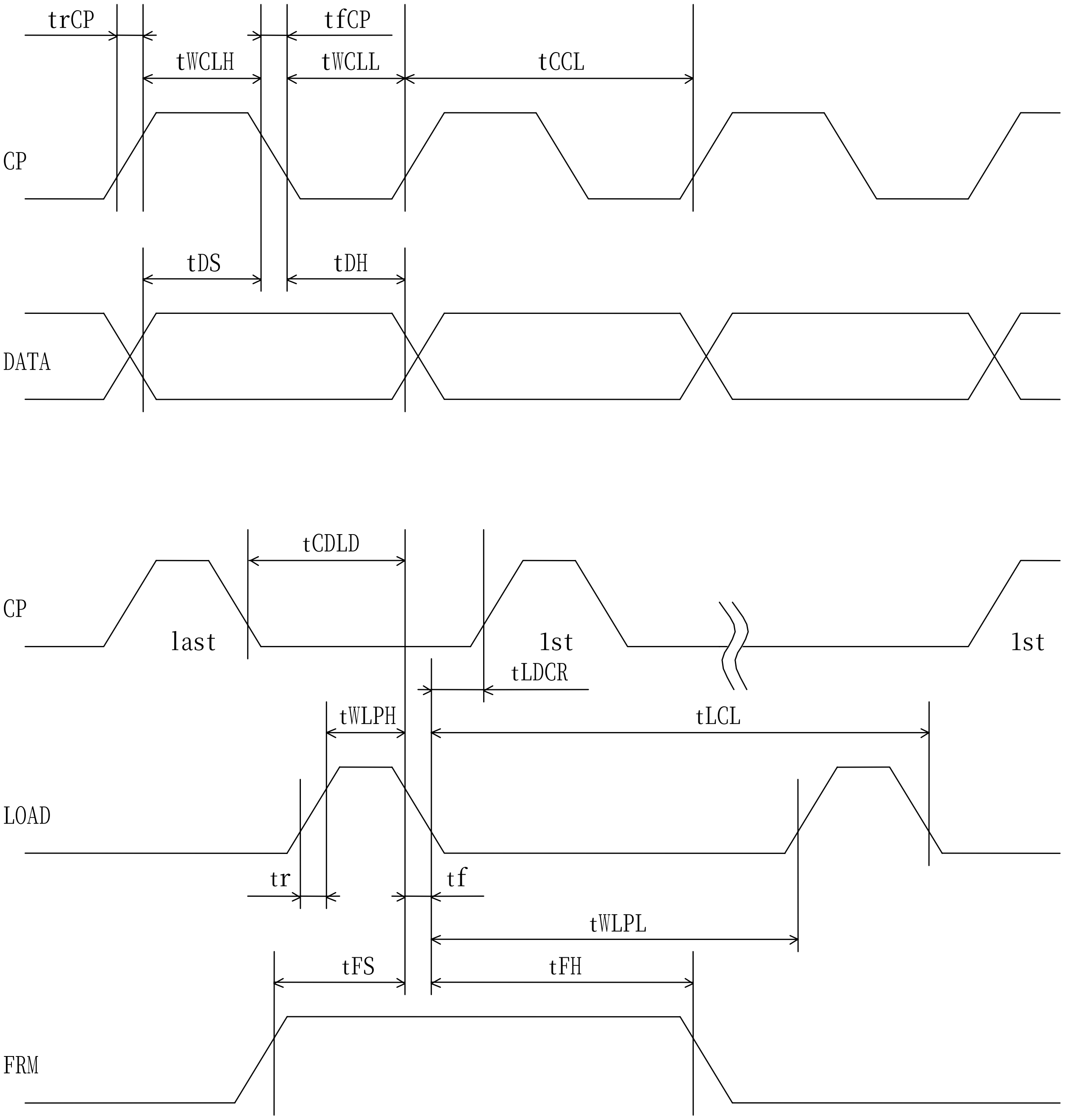
9. Interface timing chart


LOAD
CP
D3
Y317
Y1
Y5
Y317
Y1
D2
Y318
Y2
Y6
Y318
Y2
D1
Y319
Y3
Y7
Y319
Y3
D0
Y320
Y4
Y8
Y320
Y4
FRM
CP × 320 / 4pulse
LOAD
X1
X2
X240
X1
D0～D3
FRM

* The cycle of the LOAD signal should be stable and continuously applied without interruption.

* The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.

10. Input timing characteristics



trCP
tfCP
tWCLH
tWCLL
tCCL
CP
tDS
tDH
DATA
tCDLD
CP
last
1st
tLDCR
1st
tWLPH
tLCL
LOAD
tr
tf
tWLPL
tFS
tFH
FRM

10-1. Switching characteristics (V_{DD} = 5.0V)

Input characteristics : V_{DD} = +5.0V ± 5%, Temp. = 0～60°C

Item	Symbol	Min.	Max.	Unit
CP cycle *1	tCCL	100	-	ns
CP "H" pulse width	tWCLH	30	-	ns
CP "L" pulse width	tWCLL	30	-	ns
CP rise up time	trCP	-	15	ns
CP fall down time	tfCP	-	15	ns
Data set up time	tDS	25	-	ns
Data hold time	tDH	25	-	ns
LOAD "H" pulse width	tWLPH	40	-	ns
LOAD "L" pulse width	tWLPL	400	-	ns
LOAD cycle *2	tLCL	500	-	ns
CP → LOAD delay time	tCDLD	60	-	ns
LOAD → CP delay time	tLDCR	60	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

*1 CP cycle is adjusted so that FRM signal is 75Hz.

*2 LOAD cycle is constant.

10-2. Switching characteristics (V_{DD} = 3.3V)

Input characteristics : V_{DD} = +3.3V ± 0.3V, Temp. = 0～60°C

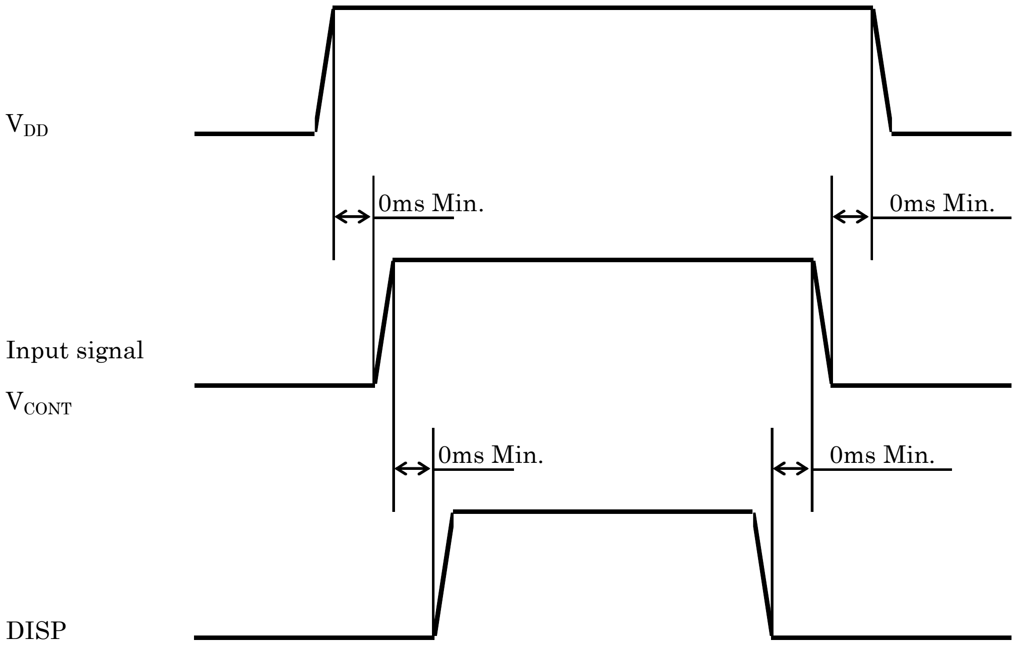
Item	Symbol	Min.	Max.	Unit
CP cycle *1	tCCL	100	-	ns
CP "H" pulse width	tWCLH	40	-	ns
CP "L" pulse width	tWCLL	40	-	ns
CP rise up time	trCP	-	20	ns
CP fall down time	tfCP	-	20	ns
Data set up time	tDS	35	-	ns
Data hold time	tDH	35	-	ns
LOAD "H" pulse width	tWLPH	50	-	ns
LOAD "L" pulse width	tWLPL	400	-	ns
LOAD cycle *2	tLCL	500	-	ns
CP → LOAD delay time	tCDLD	60	-	ns
LOAD → CP delay time	tLDCR	80	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

*1 CP cycle is adjusted so that FRM signal is 75Hz.

*2 LOAD cycle is constant.

11. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on V_{CONT} outside of the time when the V_{DD} logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



* Input signal:　FRM, LOAD, CP, D0～D3

* The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.

* Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.

12. Backlight characteristics

LED ratings

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward current *1	IF	-	25	-	mA	Ta=-20～65°C
Forward voltage *1	VF	-	(24.5)	(27.3)	V	IF=25mA, Ta=-20℃
		-	(23.1)	(25.9)	V	IF=25mA, Ta=25℃
		-	(22.1)	(24.9)	V	IF=25mA, Ta=65℃
Operating life time *2, *3	T	-	(50,000)	-	h	IF=25mA, Ta=25℃

*1 For each “AN1-CA1”, “AN2-CA2” and “AN3-CA3”

*2 When brightness decrease 50% of initial brightness.

*3 Life time is estimated data.

* An input current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

13. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

KG057QVLCD-G060 - □□ - □□ - □ MADE IN □□□□□

↓↓ ↓ ↓ ↓

1 2 3 4 5

No1. - No5. above indicate

1. Year code
2. Month code
3. Date
4. Version Number
5. Country of origin (Japan or China)

Year	2007	2008	2009	2010	2011	2012
Code	7	8	9	0	1	2

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

14. Warranty

14-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

14-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

15. Precautions for use

15-1. Installation of the LCD

1) The LCD module has a grounding hole. Please ground the module to prevent noise and to stabilize its performance as circumstances demand.
2) A transparent protection plate shall be added to protect the LCD and its polarizer.
3) The LCD shall be installed so that there is no pressure on the LSI chips.
4) The LCD shall be installed flat, without twisting or bending.
5) The display window size should be the same as the effective viewing area.
6) Please refer to the following our recommendable value of Clamp-down torque when installing.
   Clamp-down torque : 0.32±0.03N・m
   Please set up 'SPEED-LOW', 'SOFT START-SLOW' when using electric driver.
   Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 3.5±0.5mm
   Please be careful not to use high torque which may damage LCD module in installation.
7) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

15-2. Static electricity

1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
2) Workers should use body grounding. Operator should wear ground straps.

15-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2) Adjust the "Supply voltage for LCD driving (V_{CONT})" to obtain optimum viewing angle and contrast ratio.

15-4. Storage

1) The LCD shall be stored within the temperature and humidity limits specified.
   Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
2) Always store the LCD so that it is free from external pressure onto it.

15-5. Usage

1) DO NOT store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
6) Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
7) Do not disassemble LCD module because it will result in damage.
8) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
10) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

16. Reliability test data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	80°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Low temp. atmosphere	-30°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. operation	65°C	500h	Display function : No defect Display quality : No defect Current consumption : No defect

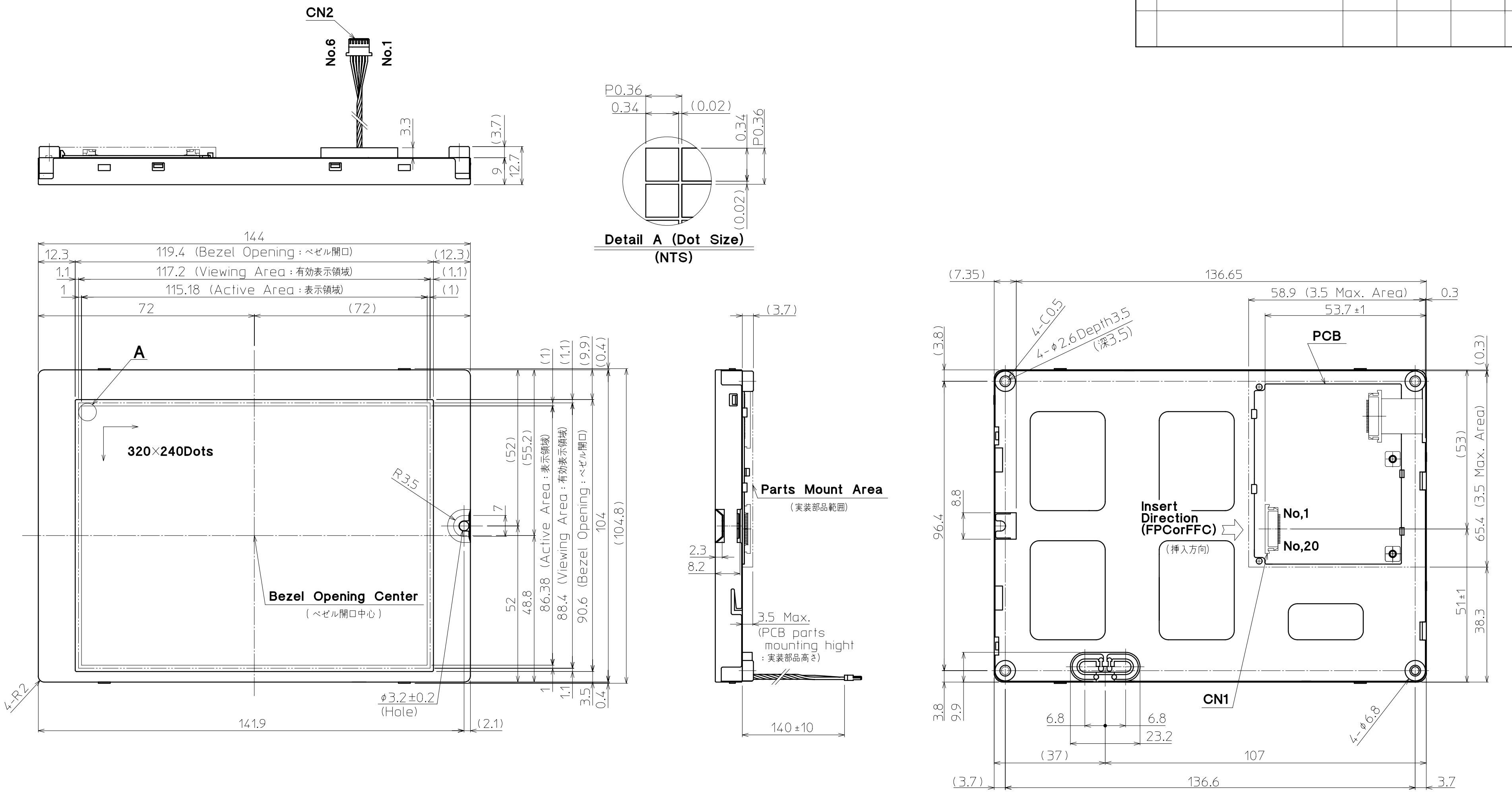
* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

* The LCD is tested in circumstances in which there is no condensation.

* The reliability test is not an out-going inspection.

* The result of the reliability test is for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.

No	Description	Drawn	Checked	Checked	Approved


CN2
No.6
No.1
3.3
(3.7)
9
12.7
P0.36
0.34
(0.02)
0.34
P0.36
(0.02)
Detail A (Dot Size)
(NTS)
144
12.3
119.4 (Bezel Opening：ベゼル開口)
(12.3)
1.1
117.2 (Viewing Area：有効表示領域)
(1.1)
1
115.18 (Active Area：表示領域)
(1)
72
(72)
A
320×240Dots
R3.5
7
Bezel Opening Center
(ベゼル開口中心)
(52)
(55.2)
52
48.8
86.38 (Active Area：表示領域)
88.4 (Viewing Area：有効表示領域)
90.6 (Bezel Opening：ベゼル開口)
104
(104.8)
(1)
(1.1)
(9.9)
(0.4)
1
1.1
3.5
0.4
4-R2
φ3.2±0.2
(Hole)
141.9
(2.1)
(3.7)
Parts Mount Area
(実装部品範囲)
2.3
8.2
3.5 Max.
(PCB parts mounting hight
：実装部品高さ)
140±10
(7.35)
136.65
58.9 (3.5 Max. Area)
0.3
53.7±1
4-C0.5
4-φ2.6 Depth3.5
(深3.5)
PCB
(3.8)
(0.3)
96.4
8.8
Insert Direction (FPCorFFC)
(挿入方向)
No,1
No,20
(53)
65.4 (3.5 Max. Area)
51±1
38.3
104
3.8
9.9
6.8
6.8
23.2
CN1
4-φ6.8
(37)
107
(3.7)
136.6
3.7

CN2 Pin Assign

	Description	Cable Color
No.1	Anode 1	Black(黒)
No.2	Anode 2	Red(赤)
No.3	Anode 3	White(白)
No.4	Cathode 1	Black(黒)
No.5	Cathode 2	Red(赤)
No.6	Cathode 3	White(白)

Note

1. Connector CN1：08-6210-020-340-800+ (ELCO)
   (コネクター) CN2：SHLP-06V-S-B (JST)
2. Matching Connector CN2：SM06B-SHLS-TF (JST)
   (適合コネクター) SM06B-SHLS-TF(LF)(SN) (JST)
3. The Information of LCD is displayed starting at the upper left hand corner, moving right then down to the lower right hand corner.
   (LCDにおいて、画像データの表示は左上コーナーから始まり、右へ進み下へ送られ右下コーナーへ向かう。)
4. Tolerance without indication：±0.5
   (指示無き公差)

Material 材 質	Treatment 処 理	Approved '06.02.14	Checked '06.02.14	Checked '06.02.14	Drawn	Scale 1:1(NTS)	Title KG057QVLCD	KYOCERA	Year-Month-Day '06.02.10
Quantity 製作数	Description; 備 考 RoHS 06006AAA	朝倉	阿部	今村	茶圓		Outline Dimensions	Drawing No.	121A5047500

Size 2

Spec No.	TQ3C 8EAS0 E2DAM25 00
Date	April 19, 2007

KYOCERA INSPECTION STANDARD

TYPE : KG057QVLCD-G060

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original Issue Date	Designed by：Engineering dept.			Confirmed by：QA dept.	
	Prepared	Checked	Approved	Checked	Approved
April 19, 2007	O. Ajisaka	S. Oshita	Y. Matsumoto	J. Sakaguchi	

Revision record

Date	Designed by：Engineering dept.			Confirmed by：QA dept.	
	Prepared	Checked	Approved	Checked	Approved

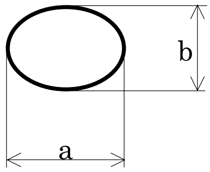
Rev.No.	Date	Page	Descriptions

Visuals specification

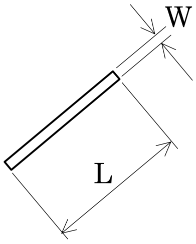
1) Note

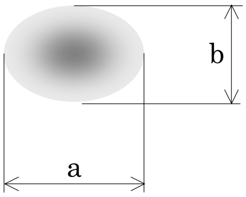
Item	Note	
General	1.When defects specified In this Inspection Standards are inspected, operating voltage (V_{OP}) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-level INSPECTION) 2.This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area. 3.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera. 4.Inspection conditions Luminance : 500 Lux minimum. Inspection distance : 300 mm(from the sample) Temperature : 25±5°C Direction : right above	
Definition of inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.
	Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.

2)Standard

<table>
<tr><th>Inspection item</th><th>Judgement standard</th></tr>
<tr><td>Pinhole, Bright spot,
Black spot, Foreign particle</td><td>


$d = (a + b) / 2$

<table>
<tr><th>Category</th><th>Size(mm)</th><th>Acceptable number</th></tr>
<tr><td>A</td><td>$d \leqq 0.2$</td><td>Neglected</td></tr>
<tr><td>B</td><td>$0.2 < d \leqq 0.3$</td><td>5</td></tr>
<tr><td>C</td><td>$0.3 < d \leqq 0.5$</td><td>3</td></tr>
<tr><td>D</td><td>$0.5 < d$</td><td>0</td></tr>
</table>
</td></tr>
<tr><td>Scratch, Foreign particle</td><td>


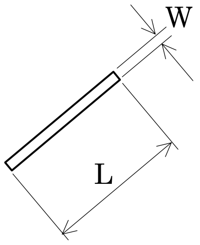
<table>
<tr><th></th><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable number</th></tr>
<tr><td>A</td><td>$W \leqq 0.03$</td><td>-</td><td>Neglected</td></tr>
<tr><td>B</td><td>$0.03 < W \leqq 0.10$</td><td>$L \leqq 2.0$</td><td>Neglected</td></tr>
<tr><td>C</td><td></td><td>$2.0 < L \leqq 4.0$</td><td>3</td></tr>
<tr><td>D</td><td></td><td>$4.0 < L$</td><td>0</td></tr>
<tr><td>E</td><td>$0.10 < W$</td><td>-</td><td>According to ‘Circular’</td></tr>
</table>
</td></tr>
<tr><td>Contrast variation</td><td>


$d = (a + b) / 2$

<table>
<tr><th>Category</th><th>Size (mm)</th><th>Acceptable number</th></tr>
<tr><td>A</td><td>$d \leqq 0.5$</td><td>Neglected</td></tr>
<tr><td>B</td><td>$0.5 < d \leqq 0.7$</td><td>3</td></tr>
<tr><td>C</td><td>$0.7 < d$</td><td>0</td></tr>
</table>
</td></tr>
</table>

<table>
<tr><th>Inspection item</th><th>Judgement standard</th></tr>
<tr><td>Polarizer
(Scratch, Bubble, Dent)</td><td>

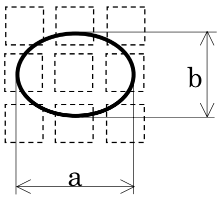
(1) Scratch



<table>
<tr><th></th><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable No.</th></tr>
<tr><td>A</td><td>$W \leqq 0.1$</td><td>-</td><td>Neglected</td></tr>
<tr><td>B</td><td rowspan="2">$0.1 < W \leqq 0.3$</td><td>$L \leqq 5.0$</td><td>Neglected</td></tr>
<tr><td>C</td><td>$5.0 < L$</td><td>0</td></tr>
<tr><td>D</td><td>$0.3 < W$</td><td>-</td><td>0</td></tr>
</table>

</td></tr>
<tr><td></td><td>

(2) Bubble (dent)



$d = (a + b) / 2$

<table>
<tr><th>Category</th><th>Size (mm)</th><th>Acceptable number</th></tr>
<tr><td>A</td><td>$d \leqq 0.2$</td><td>Neglected</td></tr>
<tr><td>B</td><td>$0.2 < d \leqq 0.3$</td><td>5</td></tr>
<tr><td>C</td><td>$0.3 < d \leqq 0.5$</td><td>3</td></tr>
<tr><td>D</td><td>$0.5 < d$</td><td>0</td></tr>
</table>

</td></tr>
</table>