

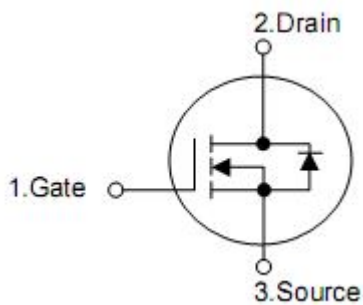
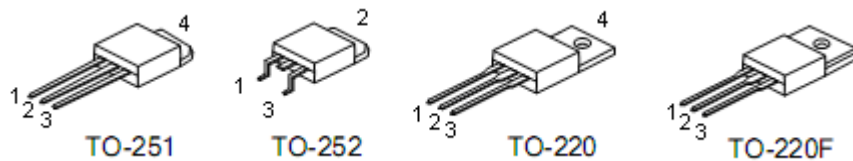
1. Description

The KIA2N60H N-Channel enhancement mode silicon gate power MOSFET is designed for high voltage, high speed power switching applications such as switching regulators, switching converters, solenoid, motor drivers, relay drivers.

2. Features

- $R_{DS(ON)}=4.1\Omega@V_{GS}=10V$.
- Low gate charge (typical 9nC)
- High ruggedness
- Fast switching capability
- Avalanche energy specified
- Improved dv/dt capability

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

4. Absolute maximum ratings

($T_C = 25^\circ\text{C}$, unless otherwise noted)

Parameter		Symbol	Rating			Units
			252/251	220	220F	
Drain-source voltage		V_{DSS}	600			V
Gate-source voltage		V_{GSS}	± 30			V
Drain current continuous	$T_C = 25^\circ\text{C}$	I_D	2.0*	2.0	2.0*	A
	$T_C = 100^\circ\text{C}$		1.35*	1.35	1.35*	A
Drain current pulsed (note1)		I_{DP}	8*	8	8*	A
Avalanche Enlsted	Repetitive (note1)	E_{AR}	4.4			mJ
	Single pulse (note2)	E_{AS}	120			mJ
Peak diode recovery dv/dt (note3)		dv/dt	4.5			V/ns
Total power dissipation	$T_C = 25^\circ\text{C}$	P_D	44	55.5	23.6	W
	Derate above 25°C		0.35	0.44	0.19	W/°C
Junction temperature		T_J	+150			°C
Storage temperature		T_{STG}	-50~+150			°C

*Drain current limited by maximum junction temperature.

5. Thermal characteristics

Parameter	Symbol	Rating			Unit
		252/251	220	220F	
Thermal resistance,Junction-ambient	R_{thJA}	62.5	62.5	62.5	°C/W
Thermal resistance,case-to-sink typ.	R_{thCS}	--	0.5	--	°C/W
Thermal resistance,Junction-case	R_{thJC}	2.87	2.25	5.3	°C/W

6. Electrical characteristics

(T_C = 25 °C, unless otherwise noted)

Parameter		Symbol	Test conditions	Min	Typ	Max	Unit
Off characteristics							
Drain-source breakdown voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	600	-	-	V
Zero gate voltage drain current		I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	1	μA
			V _{DS} =480V, T _C =125°C	-	-	10	μA
Gate-body leakage current	Forward	I _{GSS}	V _{GS} =30V, V _{DS} =0V	-	-	100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V	-	-	-100	nA
Breakdown voltage temperature coefficient		ΔBV _{DSS} /ΔT _J	I _D =250μA	-	0.7	-	V/°C
On characteristics							
Gate threshold voltage		V _{GS(TH)}	V _{DS} = V _{GS} I _D =250μA	2.0	-	4.0	V
Static drain-source on- resistance		R _{DS(ON)}	V _{DS} =10V, I _D =1.0A	-	4.1	5.0	Ω
Dynamic characteristics							
Input capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	200	-	pF
Output capacitance		C _{OSS}		-	20	-	pF
Reverse transfer capacitance		C _{RSS}		-	4	-	pF
Switching characteristics							
Turn-on delay time		t _{D(ON)}	V _{DD} =300V, I _D =2.0A, R _G =25Ω (note4,5)	-	10	-	ns
Rise time		t _R		-	25	-	ns
Turn-off delay time		t _{D(OFF)}		-	25	-	ns
Fall time		t _F		-	30	-	ns
Total gate charge		Q _G	V _{DS} =480V, I _D =2.0A V _{GS} =10V (note4,5)	-	9	-	nC
Gate-source charge		Q _{GS}		-	1.5	-	nC
Gate-drain charge		Q _{GD}		-	4.0	-	nC
Drain-source diode characteristics							
drain-source diode forward voltage		V _{SD}	V _{GS} =0V, I _{SD} =2.0A	-	-	1.4	V
Continuous drain-source current		I _{SD*}		-	-	2.0	A
Pulsed drain-source current		I _{SM*}				8.0	A
Reverse recovery time		t _{RR}	I _{SD} =2.0A		230	-	ns
Reverse recovery charge		Q _{RR}	dI _{SD} /dt=100A/μs (note4)		1.0	-	μC

Notes: 1. repetitive rating: pulse width limited by maximum junction temperature

2. L=60mH, I_{AS}=2.0A, V_{DD}=50V, R_G=25Ω, starting T_J=25°C

3. I_{SD}≤2.0A, di/dt≤200A/μs, V_{DD}≤BV_{DSS}, starting T_J=25°C

4. Pulse test: pulse width≤300μs, duty cycle≤2%

5. Essentially independent of operating temperature

7. Typical characteristics

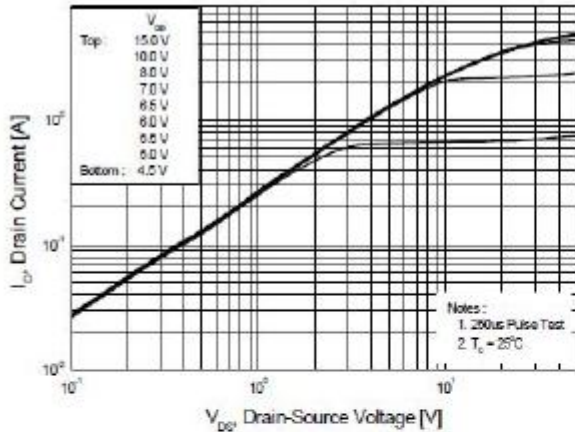


Figure 1. On-Region Characteristics

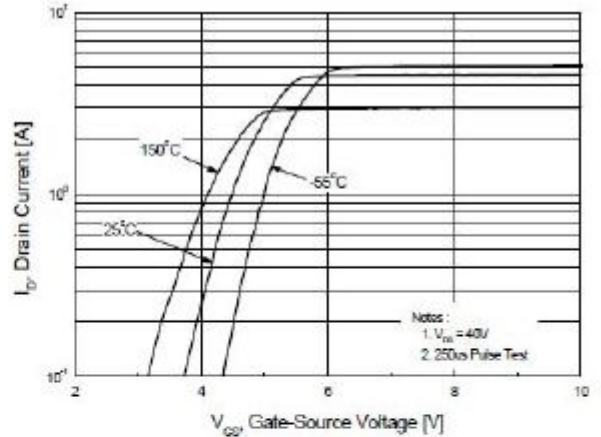


Figure 2. Transfer Characteristics

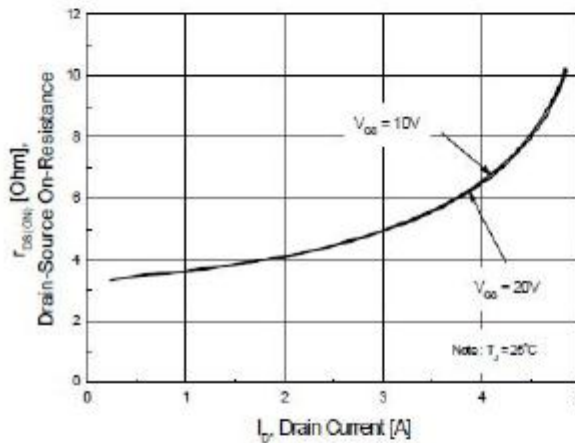


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

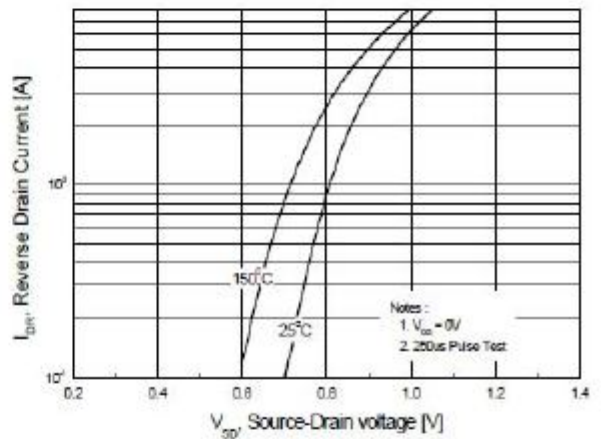


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

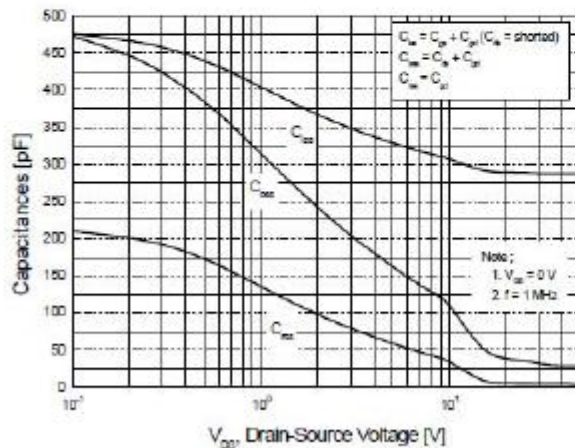


Figure 5. Capacitance Characteristics

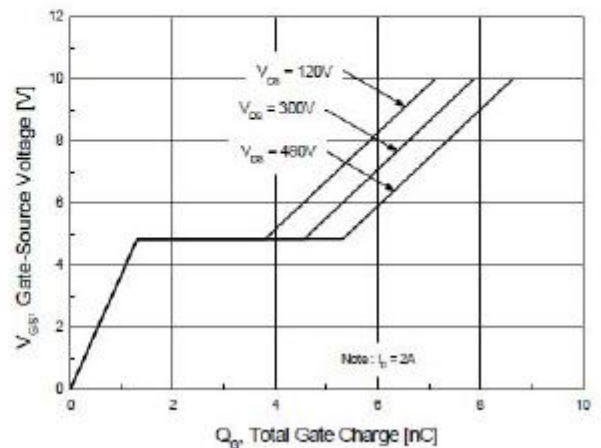


Figure 6. Gate Charge Characteristics

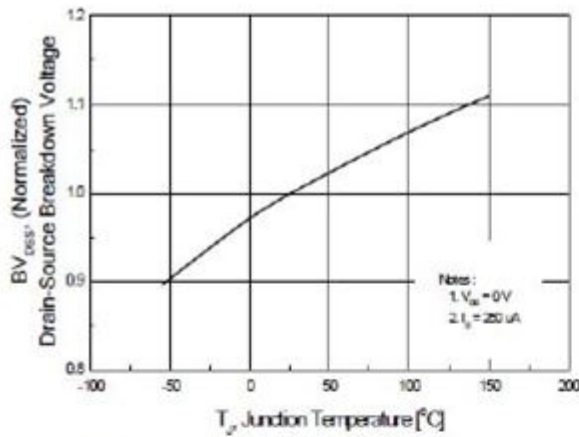


Figure 7. Breakdown Voltage Variation vs Temperature

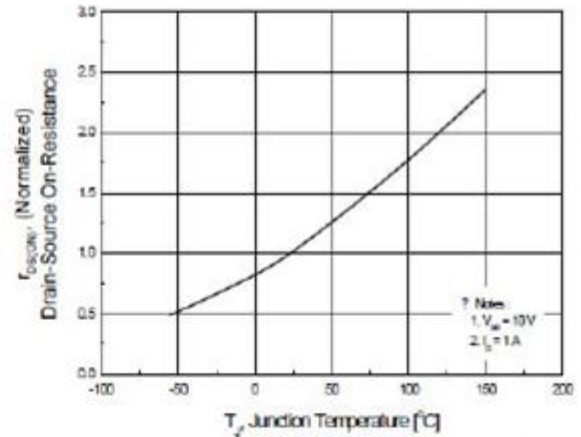


Figure 8. On-Resistance Variation vs Temperature

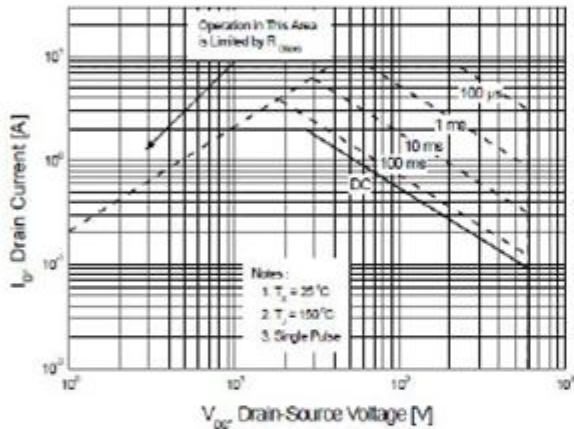


Figure 9. Maximum Safe Operating Area

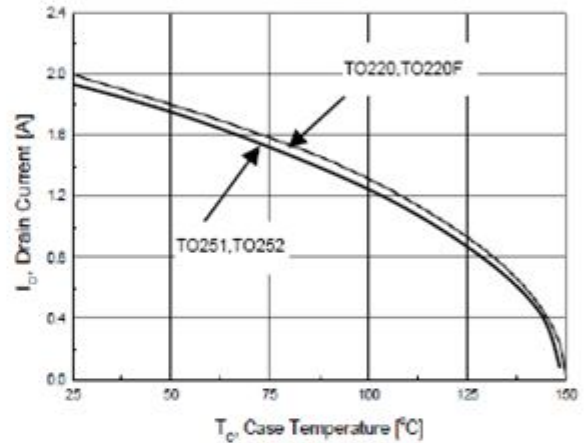


Figure 10. Maximum Drain Current vs Case Temperature

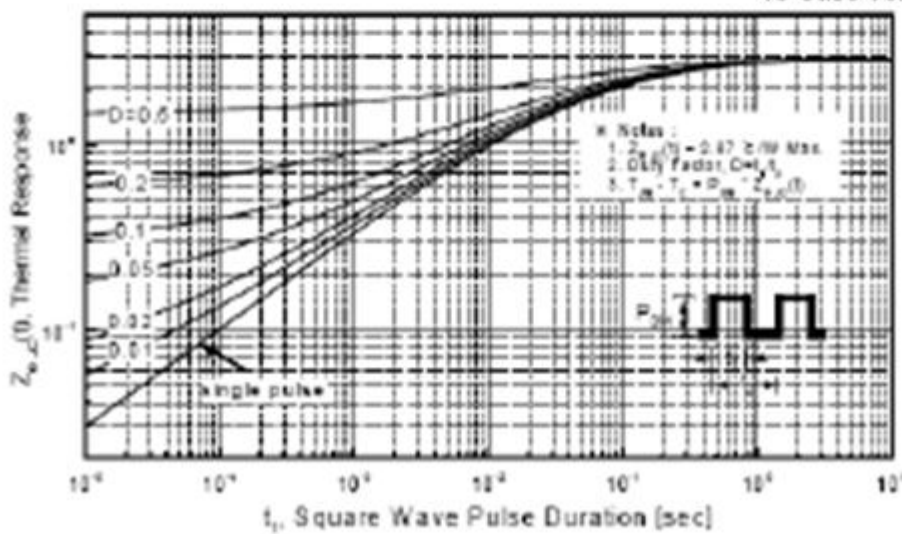
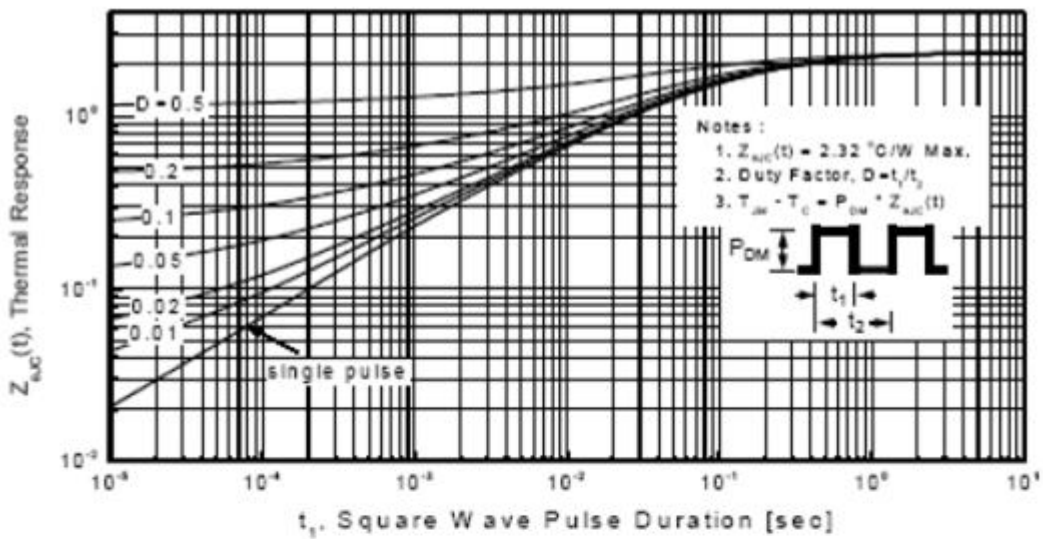
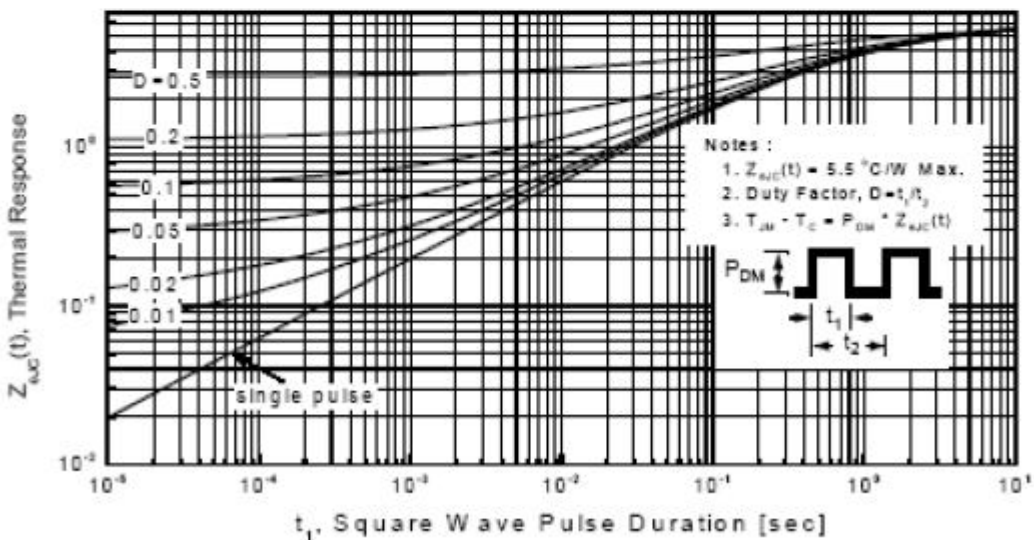


Figure 11. Transient Thermal Response Curve for TO-251/252



**Figure 11-1. Transient Thermal Response Curve
for TO-220**



**Figure 11-2. Transient Thermal Response Curve
for TO-220F**