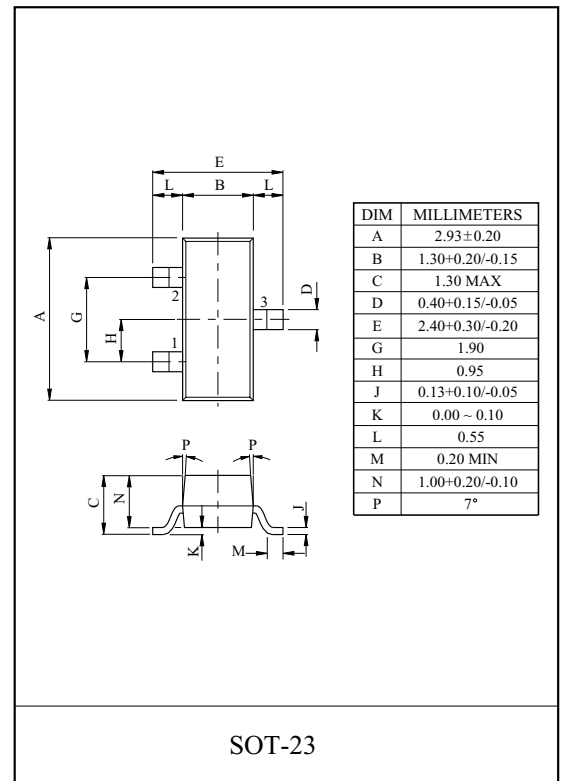


General Description

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for portable equipment and SMPS.

FEATURES

- $V_{DSS}=30V$, $I_D=4A$
- Drain-Source ON Resistance
- $R_{DS(ON)}=47m\ \Omega$ (Max.) @ $V_{GS}=10V$
- $R_{DS(ON)}=65m\ \Omega$ (Max.) @ $V_{GS}=4.5V$
- Super Hige Dense Cell Design

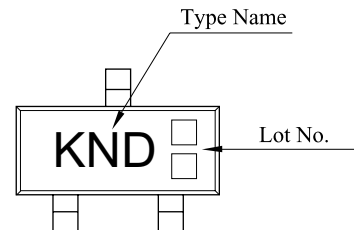


MAXIMUM RATING (Ta=25 °C)

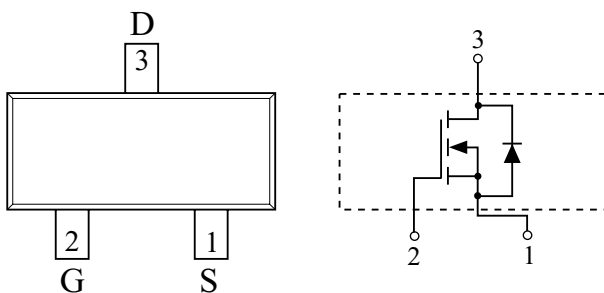
CHARACTERISTIC		SYMBOL	N-Ch	UNIT
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	DC@ $T_A=25\ ^\circ C$	I_D	4.0	A
	DC@ $T_A=70\ ^\circ C$		3.5	
	Pulsed	I_{DP}	20	
Drain-Source-Diode Forward Current		I_S	1.04	A
Drain Power Dissipation	$T_A=25\ ^\circ C$	P_D	1.25	W
	$T_A=70\ ^\circ C$		0.8	
Maximum Junction Temperature		T_j	150	°C
Storage Temperature Range		T_{stg}	-55 ~ 150	°C
Thermal Resistance, Junction to Ambient		R_{thJA}	130	°C/W

Note : Surface Mounted on FR4 Board, $t \leq 10sec.$

Marking



PIN CONNECTION (TOP VIEW)



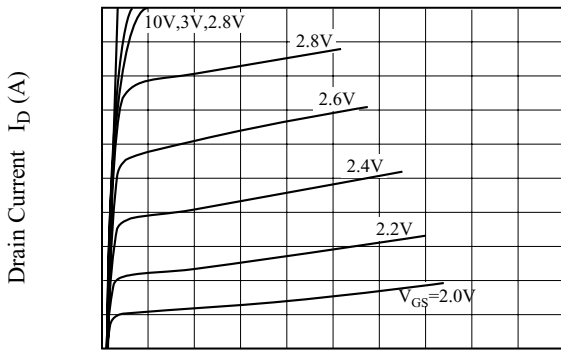
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ELECTRICAL CHARACTERISTICS (Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_{DS}=250\mu A, V_{GS}=0V,$	30	-	-	V
Drain Cut-off Current	I_{DSS}	$V_{GS}=0V, V_{DS}=30V$	-	-	0.5	μA
		$V_{GS}=0V, V_{DS}=30V, T_j=55^\circ C$	-	-	10	
Gate Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	V_{th}	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	-	3.0	V
Drain-Source ON Resistance	$R_{DS(ON)}^*$	$V_{GS}=10V, I_D=3.5A$	-	38	47	m Ω
		$V_{GS}=4.5V, I_D=2.8A$	-	52	65	
On-State Drain Current	$I_{D(ON)}^*$	$V_{GS}=5V, V_{DS}=4.5V$	6	-	-	A
Forward Transconductance	g_{fs}^*	$V_{DS}=5V, I_D=2.5A$	-	7	-	S
Dynamic (Note 3)						
Input Capacitance	C_{iss}	$V_{DS}=15V, f=1MHz, V_{GS}=0V$	-	305	-	pF
Output Capacitance	C_{oss}		-	65	-	
Reverse Transfer Capacitance	C_{rss}		-	29	-	
Total Gate Charge	Q_g^*	$V_{DS}=15V, V_{GS}=10V, I_D=2.5A$	-	6	9	nC
Gate-Source Charge	Q_{gs}^*		-	1.6	-	
Gate-Drain Charge	Q_{gd}^*		-	1.0	-	
Turn-On Delay Time	$t_{d(on)}^*$	$V_{DD}=15V, V_{GS}=10V$ $I_D=1A, R_G=6\Omega$ (NOTE 1)	-	7	11	ns
Turn-On Rise Time	t_r^*		-	12	18	
Turn-On Delay Time	$t_{d(off)}^*$		-	14	25	
Turn-On Fall Time	t_f^*		-	6	10	
Source-Drain Diode Ratings						
Source-Drain Forward Voltage	V_{SDF}^*	$V_{GS}=0V, I_{DR}=1.25A$	-	0.8	1.2	V
NOTE 1) * : Pulse Test : Pulse width <300 μs , Duty cycle < 2%						

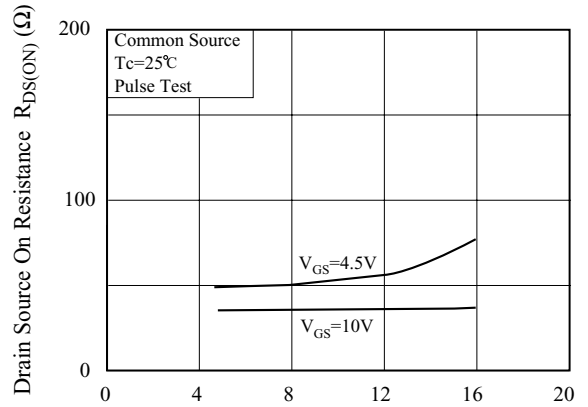
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Fig1. $V_{DS} - I_D$



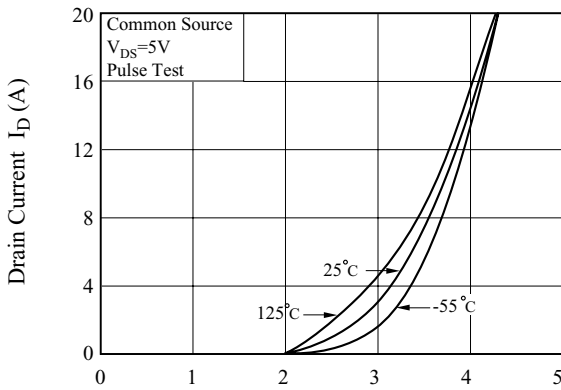
Drain - Source Voltage V_{DS} (V)

Fig2. $R_{DS(ON)} - I_D$



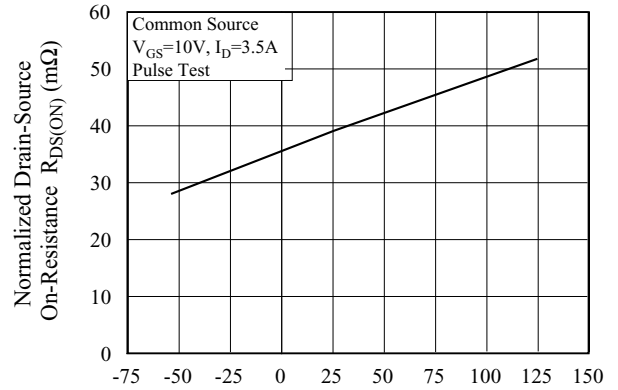
Drain - Current I_D (A)

Fig3. $V_{GS} - I_D$



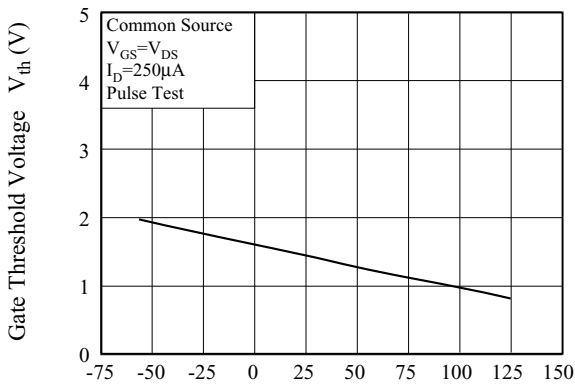
Gate Source Voltage V_{GS} (V)

Fig4. $R_{DS(ON)} - T_j$



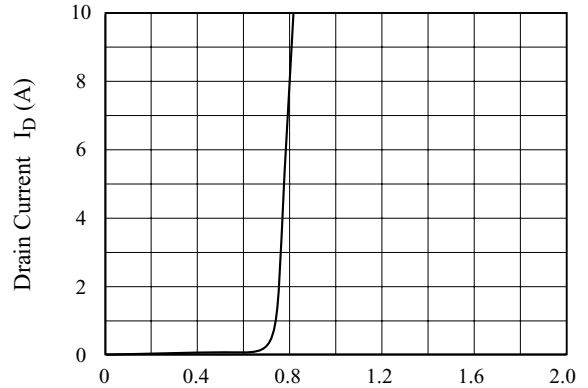
Junction Temperature T_j ($^\circ C$)

Fig5. $V_{th} - T_j$



Junction Temperature T_j ($^\circ C$)

Fig6. $I_S - V_{SDF}$



Source - Drain Forward Voltage V_{SDF} (V)

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Fig7. Transient Thermal Response Curve

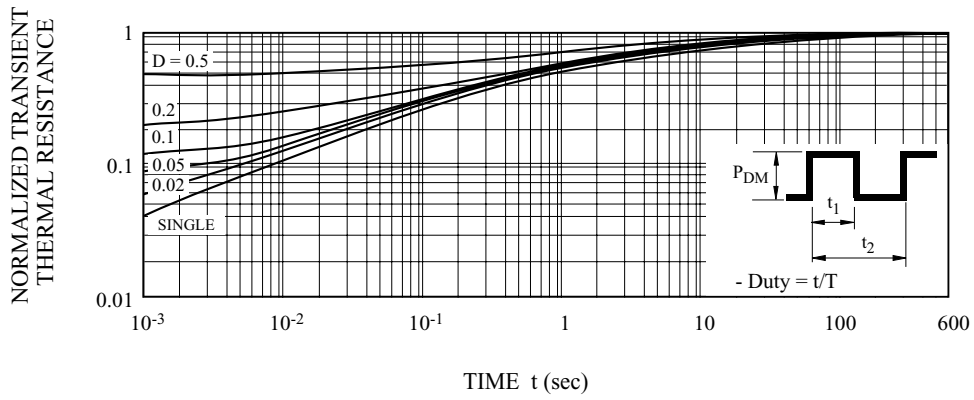
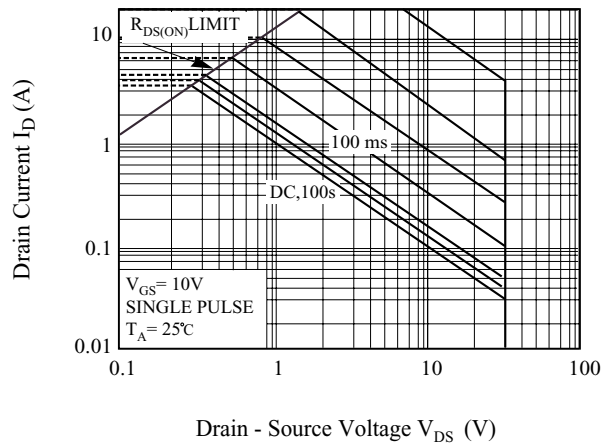


Fig8. Safe Operation Area



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Fig9. Gate Charge Circuit and Wave Form

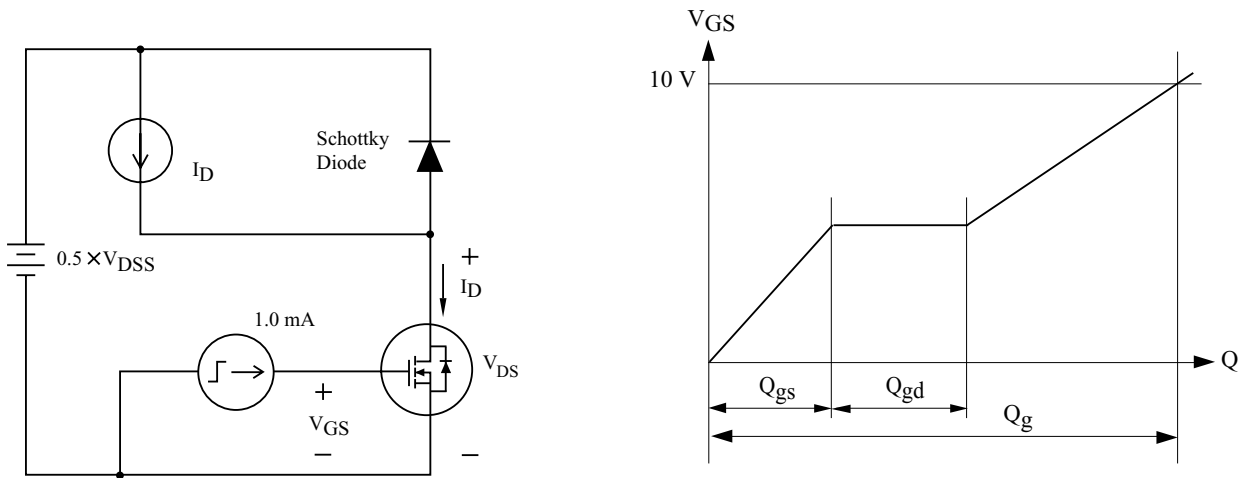


Fig10. Resistive Load Switching

