

GENERAL DESCRIPTION

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for portable equipment and DC-DC Converter Applications.

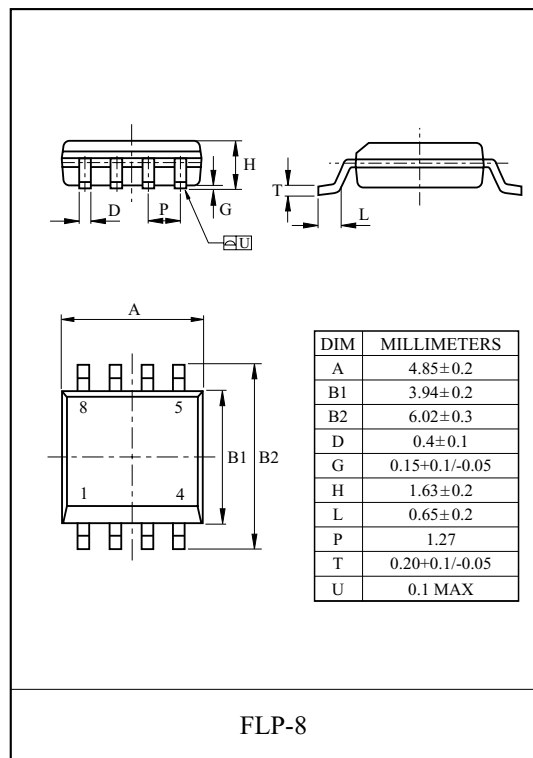
FEATURES

- $V_{DS}=30V$, $I_D=6A$.
- Drain-Source ON Resistance.
 $R_{DS(ON)}=28m\Omega$ (Max.) @ $V_{GS}=10V$
 $R_{DS(ON)}=42m\Omega$ (Max.) @ $V_{GS}=4.5V$
- Super High Dense Cell Design
- High Power and Current Handling Capability

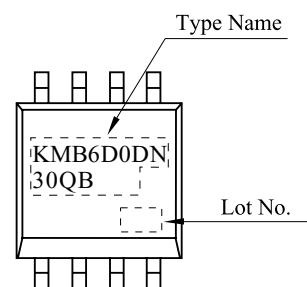
MAXIMUM RATING (Ta=25 Unless otherwise noted)

CHARACTERISTIC		SYMBOL	PATING	UNIT
Drain Source Voltage		V_{DSS}	30	V
Gate Source Voltage		V_{GSS}	± 20	V
Drain Current	DC	I_D^*	6	A
	Pulsed	I_{DP}	30	A
Drain Source Diode Forward Current		I_S	1.7	A
Drain Power Dissipation	25	P_D^*	2	W
Maximum Junction Temperature		T_j	150	
Storage Temperature Range		T_{stg}	-50~150	
Thermal Resistance, Junction to Ambient		R_{thJA}^*	62.5	/W

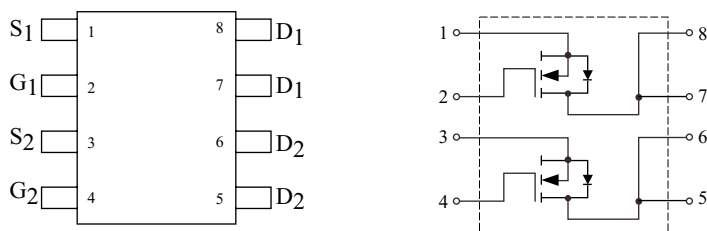
Note> *Surface Mounted on FR4 Board, $t \leq 10sec$.



Marking



PIN CONNECTION (TOP VIEW)



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ELECTRICAL CHARACTERISTICS (Ta=25) UNLESS OTHERWISE NOTED

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250 μA, V _{GS} =0V	30	-	-	V
Drain Cut-off Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ± 20V, V _{DS} =0V	-	-	± 100	nA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250 μA	1.0	1.7	2.5	V
Drain-Source ON Resistance	R _{DS(ON)} *	V _{GS} =10V, I _D =6A	-	24	28	mΩ
		V _{GS} =4.5V, I _D =5A	-	35	42	
On-State Drain Current	I _{D(ON)} *	V _{DS} =5V, V _{GS} =10V	20	-	-	A
Forward Transconductance	g _{fs} *	V _{DS} =5V, I _D =6A	-	20	-	S
Dynamic						
Input Capacitance	C _{iss}	V _{DS} =15V, f=1MHz, V _{GS} =0V	-	742	-	pF
Ouput Capacitance	C _{oss}		-	126	-	
Reverse Transfer Capacitance	C _{rss}		-	76	-	
Total Gate Charge	Q _g *	V _{DS} =15V, V _{GS} =10V, I _D =6A	-	16.5	-	nC
Gate-Source Charge	Q _{gs} *		-	4.0	-	
Gate-Drain Charge	Q _{gd} *		-	2.6	-	
Turn-On Delay Time	t _{d(on)} *	V _{DD} =15V, V _{GS} =10V I _D =6A, R _G =3 Ω	-	7.4	-	ns
Turn-On Rise Time	t _r *		-	27.7	-	
Turn-Off Delay Time	t _{d(off)} *		-	12.2	-	
Turn-Off Fall Time	t _f *		-	7.6	-	
Source-Drain Diode Ratings						
Source-Drain Forward Voltage	V _{SDF} *	I _{DR} =1.7A, V _{GS} =0V	-	0.75	1.2	V
Note> * Pulse Test : Pulse width ≤ 300μs , Duty cycle ≤ 2%						

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Fig1. $I_D - V_{DS}$

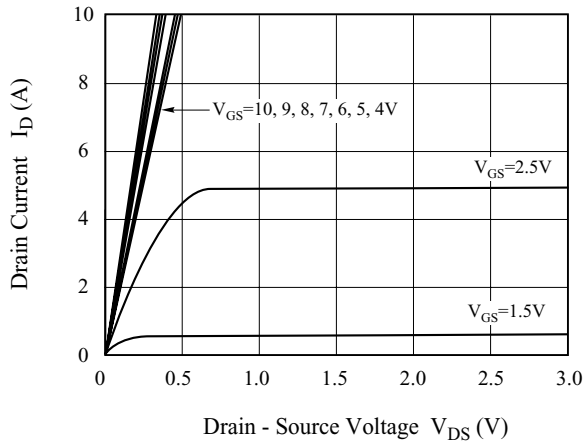


Fig2. $R_{DS(on)} - I_D$

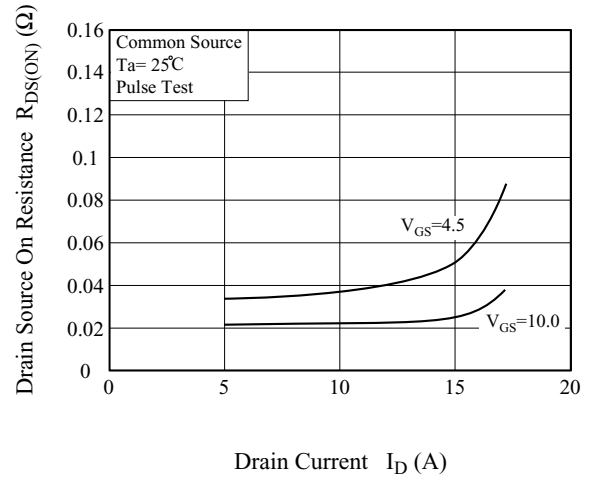


Fig3. $I_D - V_{GS}$

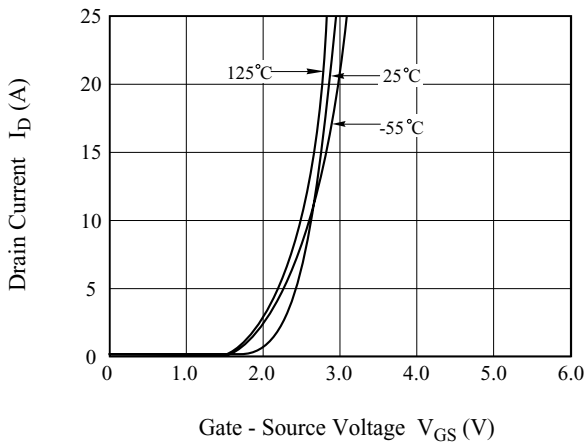


Fig4. $R_{DS(on)} - T_j$

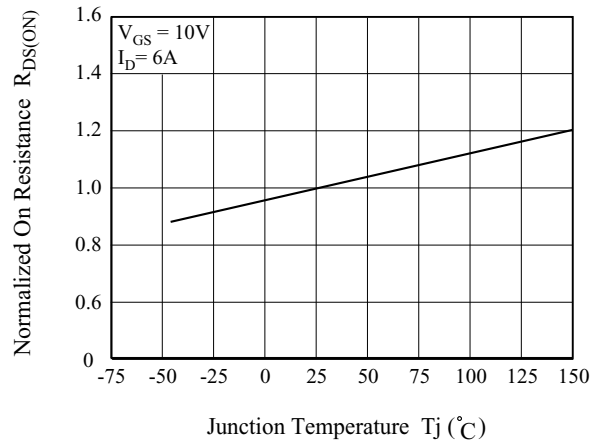


Fig5. $V_{th} - T_j$

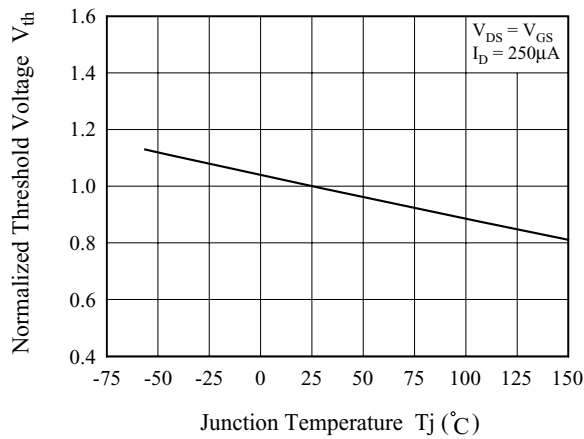
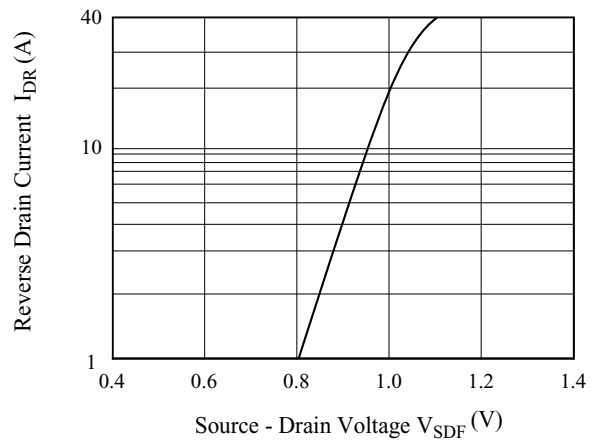


Fig6. $I_S - V_{SDF}$



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Fig7. C - V_{DS}

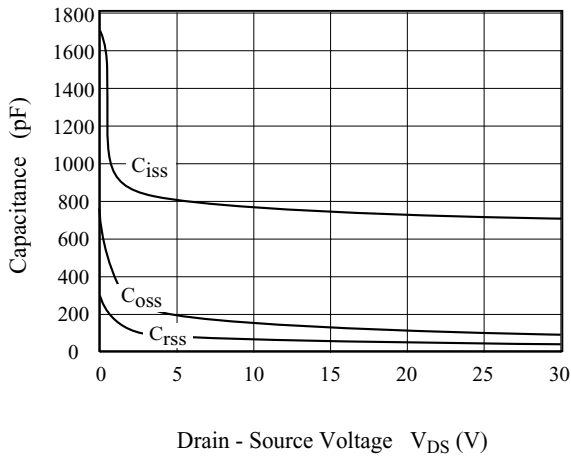


Fig8. Q_g - V_{GS}

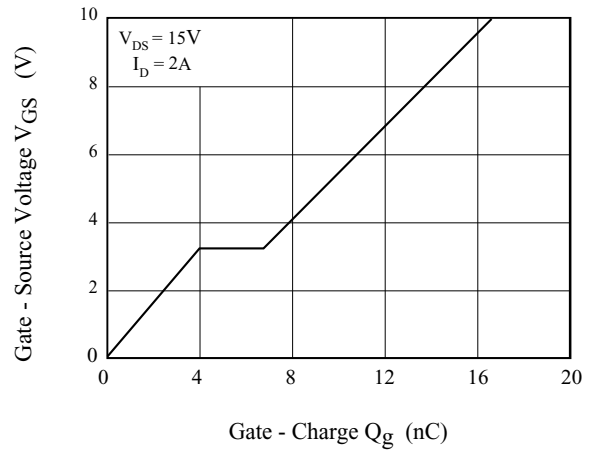


Fig9. Safe Operation Area

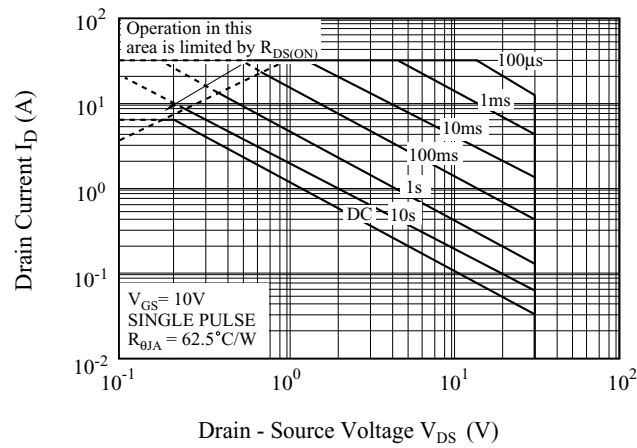


Fig10. Transient Thermal Response Curve

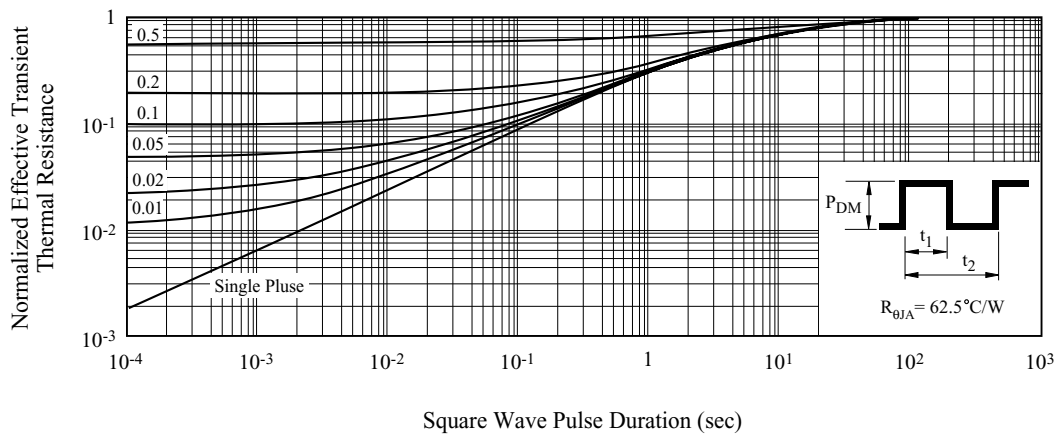


Fig11. Gate Charge

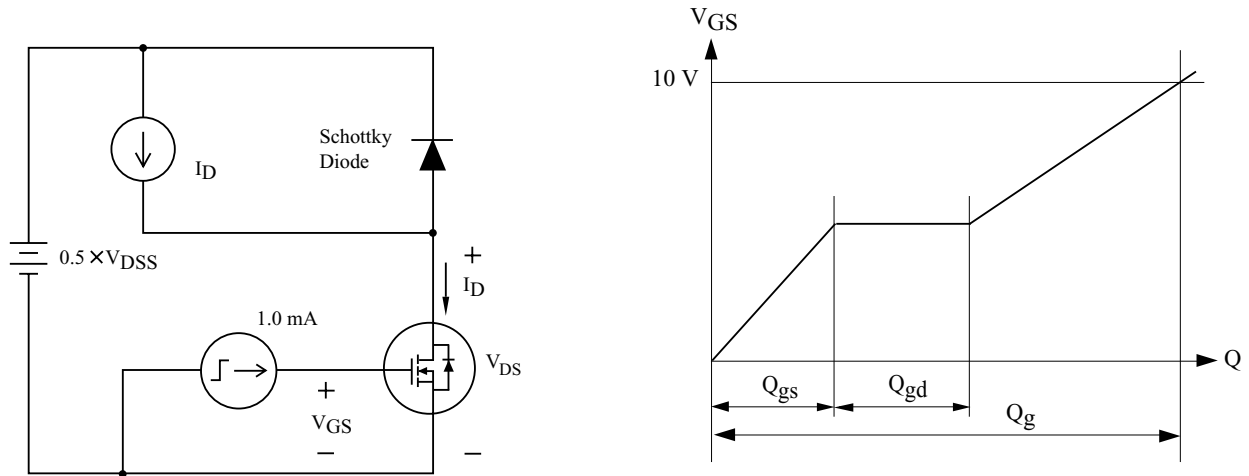


Fig12. Resistive Load Switching

