

KN400/1300V Fast Switching Reverse-conducting Thyristor

1300 V_{DRM}; 630 A rms

RCT FOR INVERTER AND CHOPPER APPLICATIONS

Features:

- . All Diffused Structure
- . Interdigitated Amplifying Gate Configuration
- . Blocking capability up to 1300 volts
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

ELECTRICAL CHARACTERISTICS AND RATINGS

Blocking - Off State

Device Type	V _{DRM} (1)	V _{D_{SM}} (1)
KN400/1300	1300	1300

V_{DRM} = Repetitive peak off state voltage

Repetitive peak off state leakage	I _{DRM}	10 mA 35mA (3)
Critical rate of voltage rise	dV/dt (4)	500 V/μsec

Notes:

All ratings are specified for T_j=25 °C unless otherwise stated.

- (1) All voltage ratings are specified for an applied 50Hz/60Hz sinusoidal waveform over the temperature range -40 to +115 °C.
- (2) 10 msec. max. pulse width
- (3) Maximum value for T_j = 115 °C.
- (4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM}. Gate open. T_j = 115 °C.
- (5) Non-repetitive value.

Conducting - on state

Parameter	Symbol	Max.	Typ.	Units	Conditions
RMS value of on-state current	I _{TRMS}	630		A	Nominal value
Average on-state current	I _{T(AV)}	400		A	Continuous single-phase, half sine wave, 180° conduction
Peak one cycle surge (non repetitive) current	I _{TSM}	7000		A	8.3 msec (60Hz), sinusoidal wave-shape, 180° conduction, T _j = 115 °C
I square t	I ² t	2.0x10 ⁵		A ² s	8.3 msec and 10.0 msec
RMS reverse current	I _{R(RMS)}	235		A	
Average reverse current	I _{R(AV)}	150		A	Continuous single-phase, half sine wave, 180° conduction
Peak on-state voltage	V _{TM}	3.0		V	I _{TM} = 1200A; T _j = 25 °C
Peak reverse voltage	V _{RM}	2.5		V	I _{RM} = 500A, T _j = 25 °C
Critical rate of rise of on-state current	di/dt	100		A/μs	V _D = 1/2V _{DRM} , I _{TM} = 800A f = 60Hz I _{GM} = 1.5A, di _G /dt = 1.0A/us, T _j = 115 °C
Critical rate of decrease of reverse commutating current	(di/dt) _C	200		A/μs	I _{TM} = 2000A, tw = 60us, I _{RM} = 1000A, V _{DM} = 1/2V _{DRM} , T _j = 115 °C

ELECTRICAL CHARACTERISTICS AND RATINGS (cont.)**Gating**

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	P_{GM}		16		W	$t_p = 40 \mu s$
Average gate power dissipation	$P_{G(AV)}$		8		W	
Peak gate current	I_{GM}		10		A	
Gate current required to trigger all units	I_{GT}		350		mA	$V_D = 6 V; R_L = 2 \text{ ohms}; T_j = +25^\circ C$
Gate voltage required to trigger all units	V_{GT}		4		V	$V_D = 6 V; R_L = 2 \text{ ohms}; T_j = 25^\circ C$
Peak non- trigger voltage	V_{GD}		0.2		V	$T_j = 125^\circ C; V_D = 1/2 V_{DRM}$

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Turn-off time	t_q		40		μs	$I_{TM} = 400 A;$ $I_{RM} = 500 A; dV/dt(C) = 200 V/\mu s$ $V_D = 650 V$ $T_j = 115^\circ C; tw = 60 \mu s$

* For guaranteed max. value, contact factory.

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

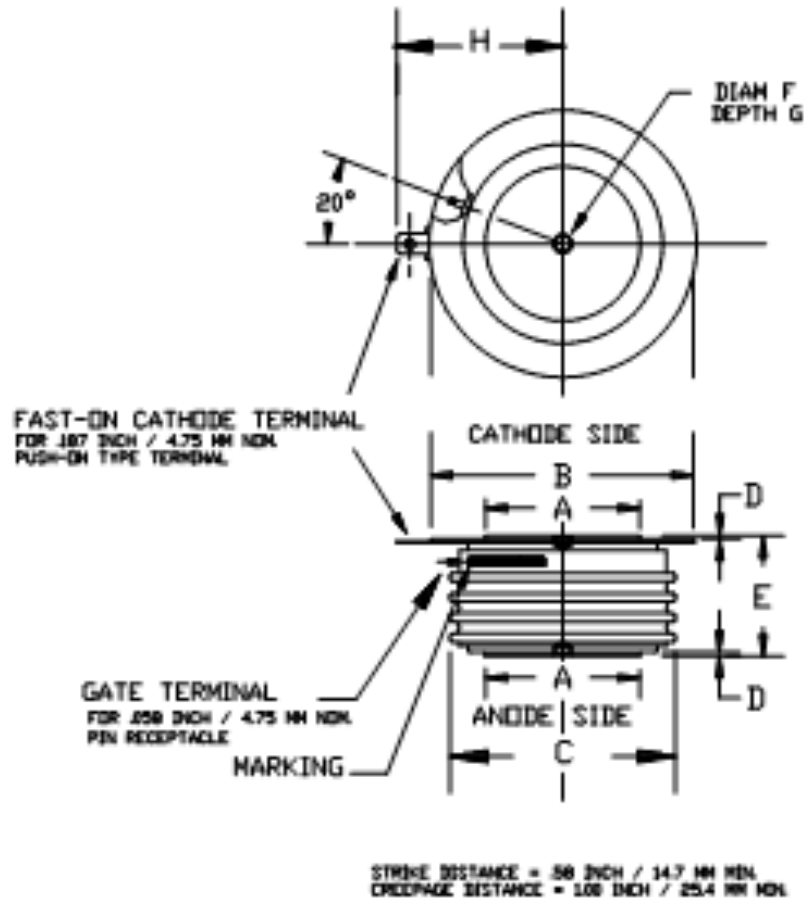
Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T_j	-40	+115		$^\circ C$	
Storage temperature	T_{stg}	-40	+150		$^\circ C$	
Thyristor part thermal resistance - junction to fin	$R_{\theta I (j-f)}$		0.04		$^\circ C/W$	Double sided cooled
Diode part thermal resistance – junction to fin	$R_{\theta D (j-f)}$		0.10		$^\circ C/W$	Double sided cooled
Mounting force	P	14.5	16.7		kN	
Weight	W			360	g	

* Mounting surfaces smooth, flat and greased

Note : for case outline and dimensions, see case outline drawing in page 4 of this Technical Data

CASE OUTLINE AND DIMENSIONS.

Reverse-conducting Thyristor



DIMENSIONS	Min mm	Max mm	Min in	Max in
DIAM A	35.02	40.29	1.38	1.58
DIAM B	62.88	65.50	2.47	2.57
DIAM C	-----	56.61	-----	2.23
D	0.76	-----	0.03	----
E	25.40 (13.72)	27.08 (15.24)	1.00 (0.54)	1.07 (0.60)
F	3.3	3.81	0.13	0.15
G	1.78	2.03	0.07	0.08