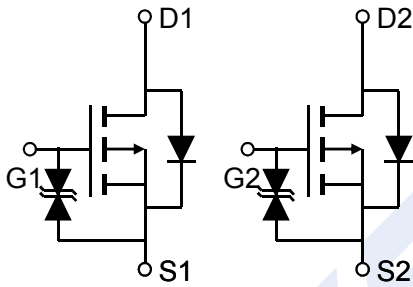
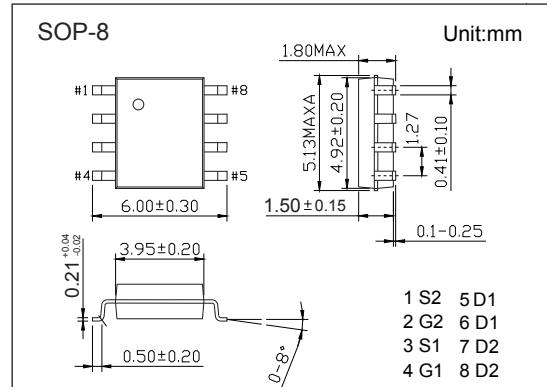


Dual P-Channel MOSFET AO4817-HF (KO4817-HF)

■ Features

- $V_{DS} (V) = -30V$
- $I_D = -8 A (V_{GS} = -20V)$
- $R_{DS(ON)} < 18m\Omega (V_{GS} = -20V)$
- $R_{DS(ON)} < 21m\Omega (V_{GS} = -10V)$
- ESD Rating: 1.5KV HBM
- Pb-Free Package May be Available. The G-Suffix Denotes a Pb-Free Lead Finish



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	-30	V	
Gate-Source Voltage	V_{GS}	± 25		
Continuous Drain Current	I_D	$T_A=25^\circ C$	-8	A
		$T_A=70^\circ C$	-6.9	
Pulsed Drain Current	I_{DM}	-40		
Power Dissipation	P_D	$T_A=25^\circ C$	2	W
		$T_A=70^\circ C$	1.44	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	$t \leq 10s$	62.5	$^\circ C/W$
		Steady-State	110	
Thermal Resistance.Junction- to-Lead	R_{thJL}	40		
Junction Temperature	T_J	150	$^\circ C$	
Storage Temperature Range	T_{stg}	-55 to 150		

Dual P-Channel MOSFET

AO4817-HF (K04817-HF)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =-250 μA, V _{GS} =0V	-30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	μA
		V _{DS} =-24V, V _{GS} =0V, T _J =55°C			-5	
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±25V			±1	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1		-3	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-20V, I _D =-8A			18	mΩ
		V _{GS} =-20V, I _D =-8A, T _J =125°C			25	
		V _{GS} =-10V, I _D =-8A			21	
		V _{GS} =-4.5V, I _D =-4A		44		
On State Drain Current	I _{D(ON)}	V _{GS} =-10V, V _{DS} =-5V	-40			A
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-8A		15		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =-15V, f=1MHz		1760	2200	pF
Output Capacitance	C _{oss}			360		
Reverse Transfer Capacitance	C _{rss}			255		
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		6.4	8	Ω
Total Gate Charge	Q _g	V _{GS} =-10V, V _{DS} =-15V, I _D =-8A		30	38	nC
Gate Source Charge	Q _{gs}			7		
Gate Drain Charge	Q _{gd}			8		
Turn-On DelayTime	t _{d(on)}	V _{GS} =-10V, V _{DS} =-15V, R _L =1.8Ω, R _{GEN} =3Ω		12.5		ns
Turn-On Rise Time	t _r			10.5		
Turn-Off DelayTime	t _{d(off)}			40		
Turn-Off Fall Time	t _f			23		
Body Diode Reverse Recovery Time	t _{rr}	I _F =-8A, di/dt=100A/μs		24	30	nC
Body Diode Reverse Recovery Charge	Q _{rr}			16		
Maximum Body-Diode Continuous Current	I _S				-2.6	A
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V			-1	V

Note. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

■ Marking

Marking	4817 KA**** _F
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Dual P-Channel MOSFET AO4817-HF (KO4817-HF)

■ Typical Characteristics

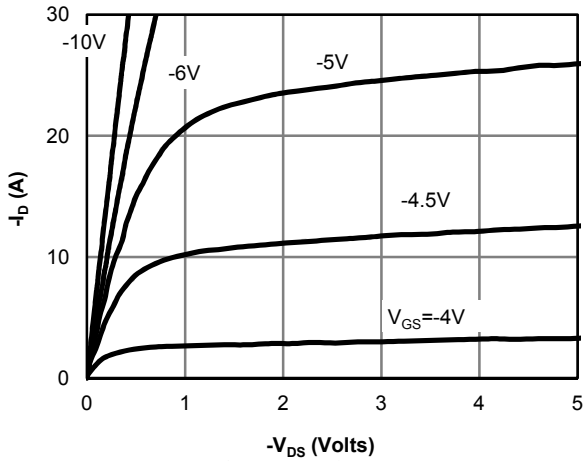


Fig 1: On-Region Characteristics

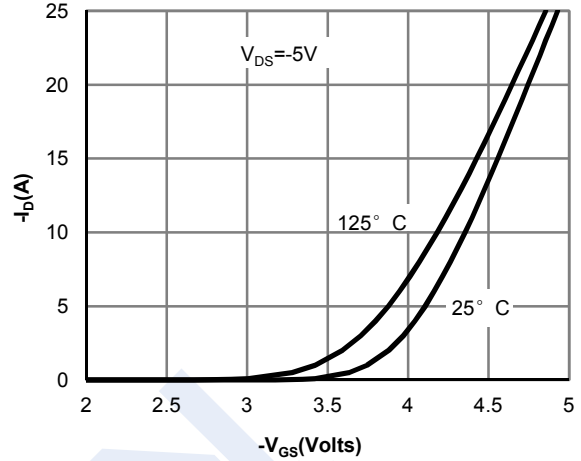


Figure 2: Transfer Characteristics

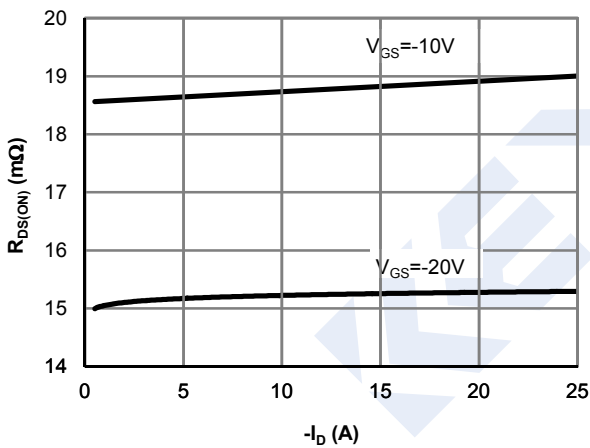


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

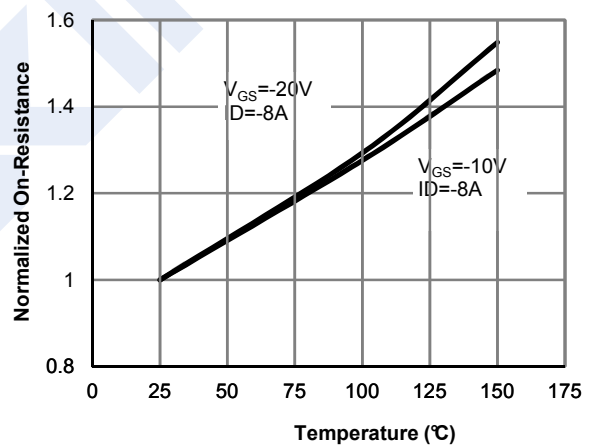


Figure 4: On-Resistance vs. Junction Temperature

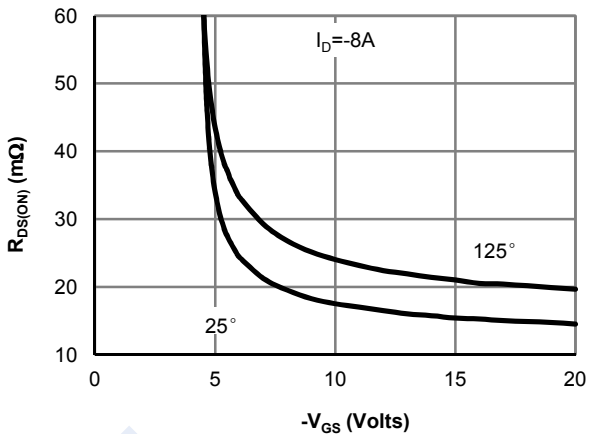


Figure 5: On-Resistance vs. Gate-Source Voltage

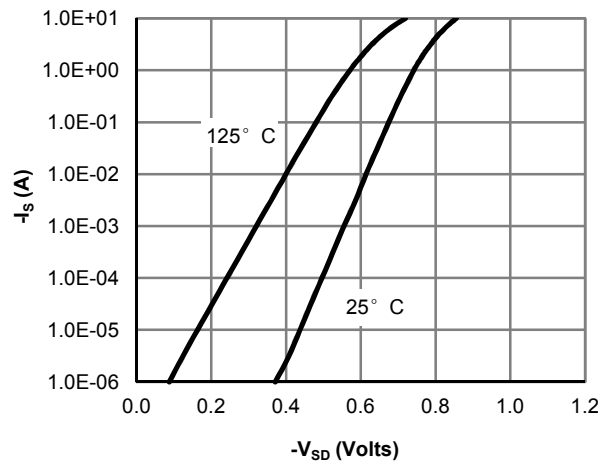


Figure 6: Body-Diode Characteristics

Dual P-Channel MOSFET AO4817-HF (KO4817-HF)

■ Typical Characteristics

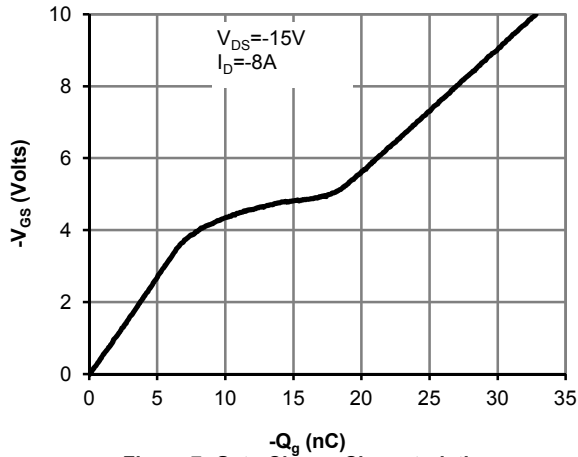


Figure 7: Gate-Charge Characteristics

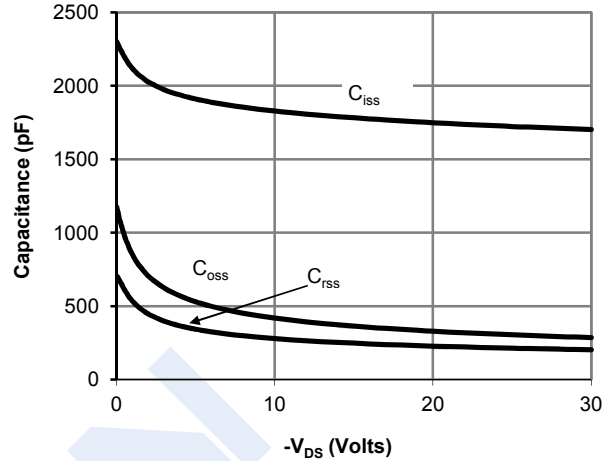


Figure 8: Capacitance Characteristics

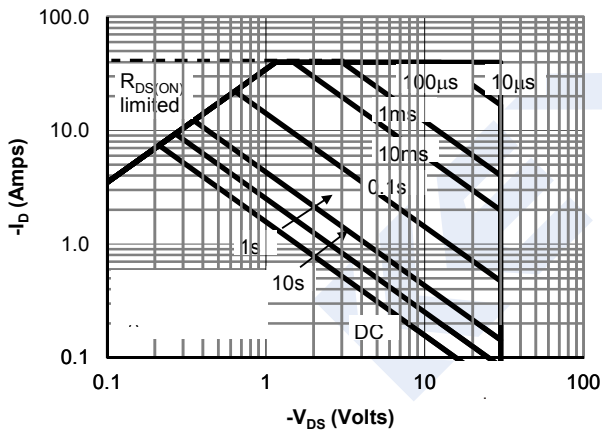


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

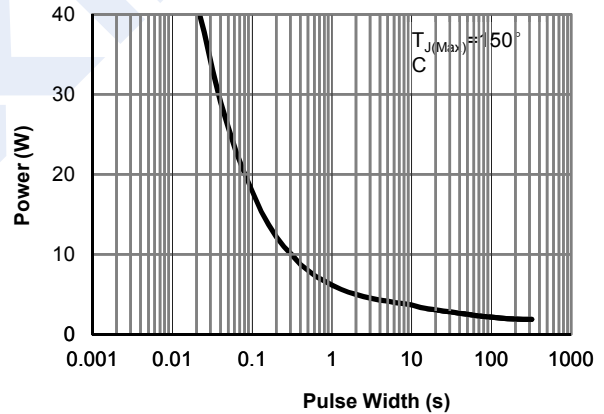


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

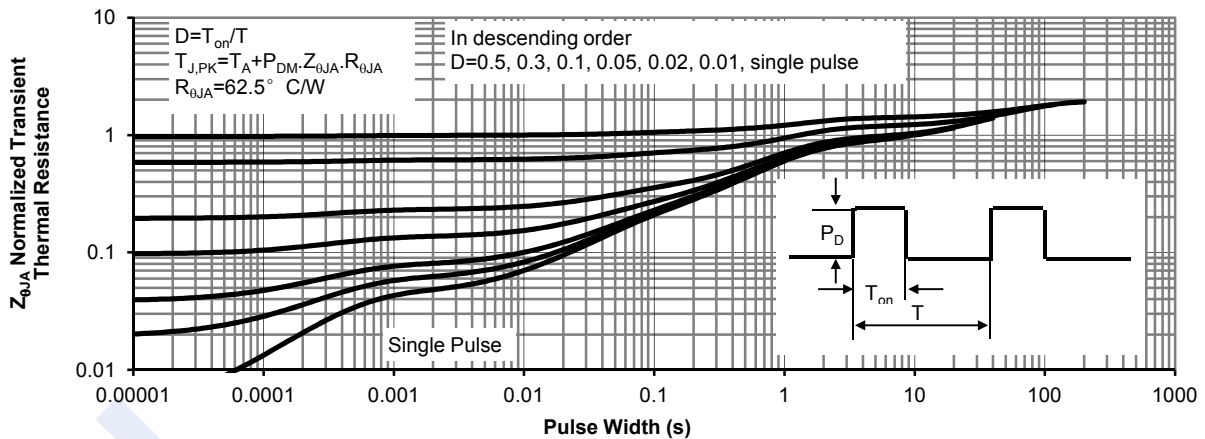


Figure 11: Normalized Maximum Transient Thermal Impedance