KP1000A/2000V

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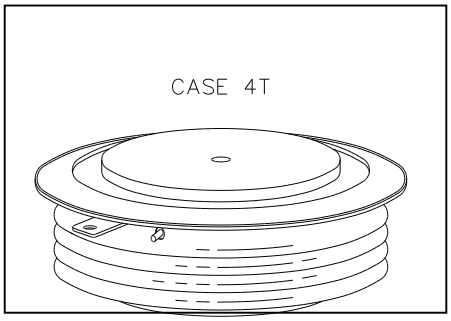
HIGH POWER THYRISTOR FOR PHASE CONTROL APPLICATIONS

Features:

- **All Diffused Structure**
- **Center Amplifying Gate Configuration**
- **Guaranteed Maximum Turn-Off Time**
- **High dV/dt Capability**
- **Pressure Assembled Device**



ELECTRICAL CHARACTERISTICS AND RATINGS

Blocking - Off State

Device Type	V_{RRM} (1)	V_{DRM} (1)	V_{RSM} (1)
KP1000A	2000	2000	2100

V_{RRM} = Repetitive peak reverse voltage
V_{DRM} = Repetitive peak off state voltage
V_{RSM} = Non repetitive peak reverse voltage (2)

Notes:
All ratings are specified for Tj=25 ºC unless otherwise stated.
(1) All voltage ratings are specified for an applied 50Hz/60zHz sinusoidal waveform over the temperature range -40 to +125 ºC.
(2) 10 msec. max. pulse width
(3) Maximum value for Tj = 125 ºC.
(4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM}. Gate open. Tj = 125 ºC.
(5) Non-repetitive value.

Repetitive peak reverse leakage and off state leakage	I_{RRM} / I_{DRM}	10 mA 65 mA (3)
Critical rate of voltage rise	dV/dt (4)	500 V/μsec

Conducting - on state

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Average value of on-state current	$I_{T(AV)}$		1300		A	T_c=93ºC
RMS value of on-state current	I_{TRMS}		2000		A	Nominal value
Peak one cPSTCle surge (non repetitive) current	I_{TSM}		20000 18000		A A	8.3 msec (60Hz), sinusoidal wave-shape, 180º conduction, T_j = 125 ºC 10.0 msec (50Hz), sinusoidal wave-shape, 180º conduction, T_j = 125 ºC
I square t	I^2t		$1.7x10^6$		A^2s	8.3 msec and 10.0 msec
Latching current	I_L		800		mA	V_D = 24 V; R_L= 12 ohms
Holding current	I_H		400		mA	V_D = 24 V; I = 2.5 A
Peak on-state voltage	V_{TM}		1.65		V	I_{TM} = 2900 A; Duty cPSTCle ≤ 0.01%
Critical rate of rise of on-state current (5)	di/dt		200		A/μs	Switching from V_{DRM} ≤ 1000 V, non-repetitive

V

Gating

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	P_{GM}		200		W	t_p = 40 us
Average gate power dissipation	$P_{G(AV)}$		5		W	
Peak gate current	I_{GM}		10		A	
Gate current required to trigger all units	I_{GT}		300 150 125		mA mA mA	V_D = 6 V;R_L = 3 ohms;T_j = -40 °C V_D = 6 V;R_L = 3 ohms;T_j = +25 °C V_D = 6 V;R_L = 3 ohms;T_j = +125°C
Gate voltage required to trigger all units	V_{GT}	 0.30	5 3 		V V V	V_D = 6 V;R_L = 3 ohms;T_j = -40 °C V_D = 6 V;R_L = 3 ohms;T_j = 0-125°C V_D = Rated V_{DRM}; R_L = 1000 ohms; T_j = + 125 °C
Peak negative voltage	V_{GRM}		5		V	

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Delay time	t_d		1.5	0.7	µs	I_{TM} = 50 A; V_D = Rated V_{DRM} Gate pulse: V_G = 20 V; R_G = 20 ohms; t_r = 0.1 µs; t_p = 20 µs
Turn-off time (with V_R = -50 V)	t_q		250	150	µs	I_{TM} = 1000 A; di/dt = 25 A/µs; V_R ≥ -50 V; Re-applied dV/dt = 20 V/µs linear to 80% V_{DRM}; V_G = 0; T_j = 125 °C; Duty cPSTCle ≥ 0.01%
Reverse recovery charge	Q_{rr}		*		µC	I_{TM} = 1000 A; di/dt = 25 A/µs; V_R ≥ -50 V

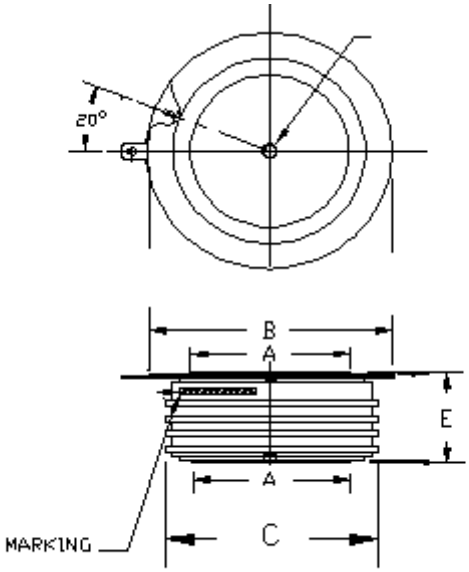
* For guaranteed max. value, contact factory.

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T_j	-40	+125		°C	
Storage temperature	T_{stg}	-40	+150		°C	
Thermal resistance - junction to case	$R_{\theta (j-c)}$		0.025 0.050		°C/W	Double sided cooled Single sided cooled
Thermal resistamce - case to sink	$R_{\theta (c-s)}$		0.010 0.020		°C/W	Double sided cooled * Single sided cooled *
Mounting force	P	20	24		kN	

* Mounting surfaces smooth, flat and greased

Note : for case outline and dimensions, see case outline drawing in page 4 of this Technical Data



A: 47 mm
B: 74 mm
C: 66 mm
E: 26 mm