KP1000A/4200V

HIGH POWER THYRISTOR FOR PHASE CONTROL APPLICATIONS

Features:

- . All Diffused Structure
- . Center Amplifying Gate Configuration
- . Blocking capabilty up to 4200 volts
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

ELECTRICAL CHARACTERISTICS AND RATINGS

Blocking - Off State

| Device Type | V _{RRM} (1) | V _{DRM} (1) | V _{RSM} (1) |
|-------------|----------------------|----------------------|----------------------|
| KP1000 | 4200 | 4200 | 4300 |

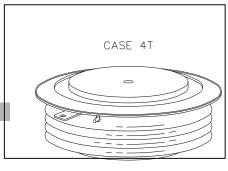
 V_{RRM} = Repetitive peak reverse voltage

 V_{DRM} = Repetitive peak off state voltage

 V_{RSM} = Non repetitive peak reverse voltage (2)

| Repetitive peak reverse leakage and off state leakage | $I_{RRM/}I_{DRM}$ | 15 mA 200mA (3) |
|---|-------------------|--------------------|
| Critical rate of voltage rise | dV/dt (4) | 300 V/μsec |

Conducting - on state



Notes

All ratings are specified for Tj=25 $^{\circ}$ C unless otherwise stated.

- (1) All voltage ratings are specified for an applied 50Hz/60zHz sinusoidal waveform over the temperature range -40 to +125 °C.
- (2) 10 msec. max. pulse width
- (3) Maximum value for T_j = 125 °C.
- (4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM}. Gate open. Tj = 125 °C.
- (5) Non-repetitive value.
- (6) The value of di/dt is established in accordance with EIA/NIMA Standard RS-397, Section 5-2-2-6. The value defined would be in addition to that obtained from a snubber circuit, comprising a 0.2 μF capacitor and 20 ohms resistance in parallel with the thristor under test.

| Parameter | Symbol | Min. | Max. | Тур. | Units | Conditions |
|--|-------------------|------|----------------|------|--------|--|
| Average value of on-state current | $I_{T(AV)}$ | | 1150 | | A | Sinewave,180° conduction,T _c =74°C |
| RMS value of on-state current | I _{TRMS} | | 1800 | | A | Nominal value |
| Peak one cpstcle surge (non repetitive) current | I _{TSM} | | 15000 14000 | | A A | 8.3 msec (60Hz), sinusoidal wave- shape, 180° conduction, $T_j = 125 ^{\circ}$ C 10.0 msec (50Hz), sinusoidal wave- shape, 180° conduction, $T_i = 125 ^{\circ}$ C |
| I square t | I ² t | | 937000 | | A^2s | 8.3 msec and 10.0 msec |
| Latching current | I_L | | 800 | | mA | $V_D = 24 \text{ V}; R_L = 12 \text{ ohms}$ |
| Holding current | I_{H} | | 400 | | mA | $V_{D} = 24 \text{ V}; I = 2.5 \text{ A}$ |
| Peak on-state voltage | V_{TM} | | 2.30 | | V | I _{TM} = 2000 A; Tvj=125°C |
| Critical rate of rise of on-state current (5, 6) | di/dt | | 300 | | A/µs | Switching from $V_{DRM} \le 1500 \text{ V}$, non-repetitive |
| Critical rate of rise of on-state current (6) | di/dt | | 100 | | A/µs | Switching from V _{DRM} ≤ 1500 V |

Gating

| Parameter | Symbol | Min. | Max. | Тур. | Units | Conditions |
|--------------------------------------|-------------|------|------|------|-------|--|
| Peak gate power dissipation | P_{GM} | | 200 | | W | $t_p = 40 \text{ us}$ |
| Average gate power dissipation | $P_{G(AV)}$ | | 5 | | W | |
| Peak gate current | I_{GM} | | 10 | | A | |
| Gate current required to trigger all | I_{GT} | | 300 | | mA | $V_D = 6 \text{ V;} R_L = 3 \text{ ohms;} T_j = -40 ^{\circ}\text{C}$ |
| units | | | 150 | | mA | $V_D = 6 \text{ V}; R_L = 3 \text{ ohms}; T_j = +25 ^{\circ}\text{C}$ |
| | | | 125 | | mA | $V_D = 6 \text{ V}; R_L = 3 \text{ ohms}; T_j = +125^{\circ} \text{C}$ |
| Gate voltage required to trigger all | V_{GT} | | 5 | | V | $V_D = 6 \text{ V}; R_L = 3 \text{ ohms}; T_j = -40 \text{ °C}$ |
| units | | | 3 | | V | $V_D = 6 \text{ V}; R_L = 3 \text{ ohms}; T_j = 0-125^{\circ}\text{C}$ |
| | | 0.30 | | | V | $V_D = Rated V_{DRM}$; $R_L = 1000 ohms$; |
| | | | | | | $T_j = + 125 {}^{\circ}\text{C}$ |
| Peak negative voltage | V_{GRM} | | 5 | | V | |

Dynamic

| Parameter | Symbol | Min. | Max. | Тур. | Units | Conditions |
|---|---------|------|------|------|-------|--|
| Delay time | t_d | | | 0.7 | μs | $I_{TM} = 50 \text{ A}; V_D = \text{Rated } V_{DRM}$ |
| | | | 1.5 | | | Gate pulse: $V_G = 20 \text{ V}$; $R_G = 20 \text{ ohms}$; |
| | | | | | | $t_r = 0.1 \ \mu s; \ t_p = 20 \ \mu s$ |
| Turn-off time (with $V_R = -50 \text{ V}$) | t_{q} | | | 125 | μs | $I_{TM} = 1000 \text{ A}$; di/dt = 25 A/ μ s; |
| | | | 250 | | | $V_R \ge -50 \text{ V}$; Re-applied dV/dt = 20 |
| | | | | | | $V/\mu s$ linear to 50% V_{DRM} ; $V_G = 0$; |
| | | | | | | $T_i = 125$ °C; Duty cpstcle $\ge 0.01\%$ |
| Reverse recovery charge | Qrr | | | | μС | $I_{TM} = 1000 \text{ A}$; di/dt = 25 A/ μ s; |
| | | | * | | | $V_R \ge -50 \text{ V}$ |

^{*} For guaranteed max. value, contact factory.

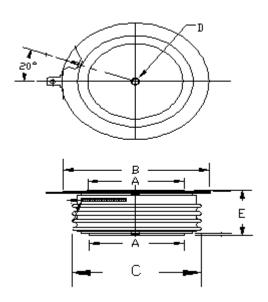
THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

| Parameter | Symbol | Min. | Max. | Тур. | Units | Conditions |
|-----------------------------------|----------------------|------|-------|------|-------|-----------------------|
| Operating temperature | T _j | -40 | +125 | | °C | |
| Storage temperature | T_{stg} | -40 | +150 | | °C | |
| Thermal resistance - junction to | R _{e (j-c)} | | 0.025 | | °C/W | Double sided cooled |
| case | 0 -7 | | 0.050 | | | Single sided cooled |
| Thermal resistance - case to sink | R _{e (c-s)} | | 0.010 | | °C/W | Double sided cooled * |
| | () | | 0.020 | | | Single sided cooled * |
| Mounting force | P | 5500 | 6000 | | lb. | |
| | | 24.5 | 26.7 | | kN | |
| Weight | W | | | 16 | OZ. | |
| | | | | 460 | g | |

^{*} Mounting surfaces smooth, flat and

greased

Note: for case outline and dimensions, see case outline drawing in page 4 of this Technical Data



A: 47 mm
B: 74 mm
C: 66 mm
D: \$\psi 5 \times 3\$ mm
E: 26 mm