

Features:

- . All Diffused Structure
- . Interdigitated Amplifying Gate Configuration
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

ELECTRICAL CHARACTERISTICS AND RATINGS**Blocking - Off State**

V_{RRM} (1)	V_{DRM} (1)	V_{RSM} (1)
1800	1800	1900

V_{RRM} = Repetitive peak reverse voltage

V_{DRM} = Repetitive peak off state voltage

V_{RSM} = Non repetitive peak reverse voltage (2)

Repetitive peak reverse leakage and off state leakage	I_{RRM} / I_{DRM}	10 mA 70 mA (3)
Critical rate of voltage rise	dV/dt (4)	500 V/ μ sec

Notes:

All ratings are specified for $T_j=25^\circ\text{C}$ unless otherwise stated.

(1) All voltage ratings are specified for an applied 50Hz/60Hz sinusoidal waveform over the temperature range -40 to $+125^\circ\text{C}$.

(2) 10 msec. max. pulse width

(3) Maximum value for $T_j = 125^\circ\text{C}$.

(4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM} . Gate open. $T_j = 125^\circ\text{C}$.

(5) Non-repetitive value.

(6) The value of di/dt is established in accordance with EIA/NIMA Standard RS-397, Section 5-2-2-6. The value defined would be in addition to that obtained from a ubber circuit,comprising a $0.2 \mu\text{F}$ capacitor and 20 ohms resistance in parallel with the thristor under test.

Conducting - on state

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Max. average value of on-state current	$I_{T(AV)M}$		1965		A	Sinewave, 180° conduction, $T_c=55^\circ\text{C}$
RMS value of on-state current	$I_{T(RMS)M}$		-		A	Nominal value, $T_c=70^\circ\text{C}$
Peak one cPSTClc surge (non repetitive) current	I_{TSM}		-		kA	8.3 msec (60Hz), sinusoidal wave-shape, 180° conduction, $T_j = 125^\circ\text{C}$
			36		kA	10.0 msec (50Hz), sinusoidal wave-shape, 180° conduction, $T_j = 125^\circ\text{C}$
I^2t	I^2t		6.48×10^6		A^2s	8.3 msec
Latching current	I_L		700		mA	$V_D = 24 \text{ V}$; $R_L = 12 \text{ ohms}$
Holding current	I_H		300		mA	$V_D = 24 \text{ V}$; $I = 2.5 \text{ A}$
Peak on-state voltage	V_{TM}		1.4		V	$I_{TM} = 2900 \text{ A}$; $T_j = 25^\circ\text{C}$
Critical rate of rise of on-state current (5, 6)	di/dt		-		A/ μ s	Switching from $V_{DRM} \leq 1000 \text{ V}$, non-repetitive
Critical rate of rise of on-state current (6)	di/dt		200		A/ μ s	Switching from $V_{DRM} \leq 1000 \text{ V}$



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Gating

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	P _{GM}		150		W	
Average gate power dissipation	P _{G(AV)}		2		W	
Peak gate current	I _{GM}		-		A	
Gate current required to trigger all units	I _{GT}		300		mA	V _D = 10 V; I _T =3A; T _j = +25 °C
Gate voltage required to trigger all units	V _{GT}		3.5		V	V _D = 10 V; I _T =3A; T _j = +25 °C
Peak negative voltage	V _{RGM}		5		V	

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Delay time	t _{gd}		-	3	μs	V _D =67% V _{DRM} , I _T =2000A, di/dt=60A/us, I _{FG} =2A, t _r =0.5us, T _j =25C
Turn-on time	t _{gt}		-	-	-	
Turn-off time (with V _R = -5 V)	t _q	-	-	250	μs	I _{TM} =1000A, t _p =1000us, di/dt=60A/us, V _F =50V, V _{dr} =80%V _{DRM} , dV _{dr} /dt=20V/us
Reverse recovery current	I _{rm}	-	-	-	A	I _{TM} =4000A, t _p =2000us, di/dt=60A/us

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

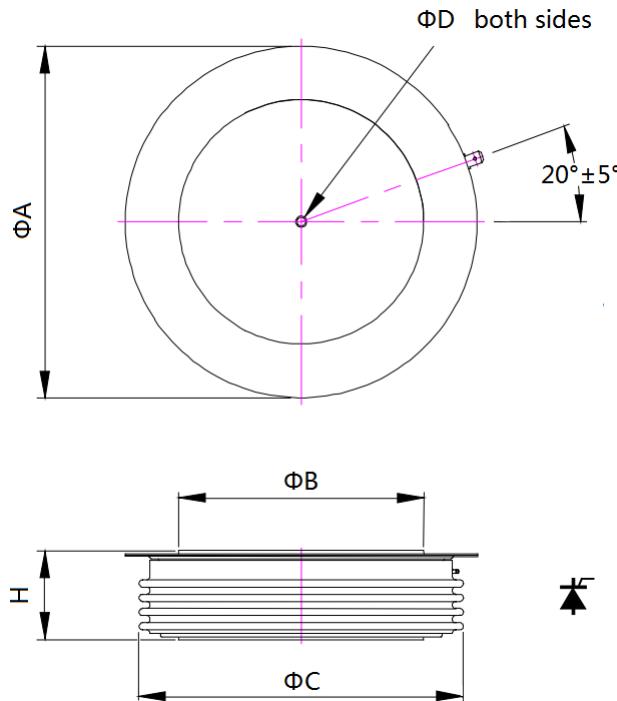
Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T _j	-30	+125		°C	
Storage temperature	T _{stg}	-30	+125		°C	
Thermal resistance - junction to case	R _{Θ (j-c)}		-	-	°C/kW	Double sided cooled Single sided cooled
Thermal resistamce - case to sink	R _{Θ (c-s)}		6	-	°C/kW	Double sided cooled * Single sided cooled *
Thermal resistance - junction to sink	R _{Θ (j-s)}		21	-	°C/kW	Double sided cooled Single sided cooled
Mounting force	F	22	24.5	-	kN	
Weight	W			-	Kg	about

* Mounting surfaces smooth, flat and greased

Note : for case outline and dimensions, see case outline drawing in page 3 of this Technical Data



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Sym	A	B	C	D	H
mm	75	47	66	3.5×3	26±1