KP760A/6500V

HIGH POWER THYRISTOR FOR PHASE CONTROL APPLICATIONS

Features:

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- . All Diffused Structure
- . Center Amplifying Gate Configuration
- . Blocking capabilty up to 6500 volts
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

ELECTRICAL CHARACTERISTICS AND RATINGS

Blocking - Off State

Device Type	V _{RRM} (1)	V _{DRM} (1)	V _{RSM} (1)
KP760	5800	5800	6000
KP760	6200	6200	6400
KP760	6500	6500	6700

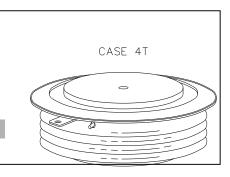
 V_{RRM} = Repetitive peak reverse voltage

 V_{DRM} = Repetitive peak off state voltage

 V_{RSM} = Non repetitive peak reverse voltage (2)

Repetitive peak reverse	$I_{RRM/}I_{DRM}$	15 mA
leakage and off state		200mA (3)
Critical rate of voltage rise	dV/dt (4)	2000 V/µsec

Conducting - on state



Notes:

All ratings are specified for Tj=25 °C unless otherwise stated.

- (1) All voltage ratings are specified for an applied 50Hz/60zHz sinusoidal waveform over the temperature range -40 to +125 °C.
- (2) 10 msec. max. pulse width
- (3) Maximum value for Tj = 125 °C.
- (4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM} . Gate open. Tj = 125 °C.
- (5) Non-repetitive value.
- (6) The value of di/dt is established in accordance with EIA/NIMA Standard RS-397, Section 5-2-2-6. The value defined would be in addition to that obtained from a snubber circuit, comprising a 0.2 μ F capacitor and 20 ohms resistance in parallel with the thristor under test.

Parameter	Symbo l	Min.	Max.	Тур.	Units	Conditions
Average value of on-state current	I _{T(AV)}		760		A	Sinewave,180° conduction,T _c =74°C
RMS value of on-state current	I _{TRMS}		1200		А	Nominal value
Peak one cpstcle surge (non repetitive) current	I _{TSM}		11600		A	8.3 msec (60Hz), sinusoidal wave- shape, 180° conduction, $T_j = 125$ °C
			12300		A	10.0 msec (50Hz), sinusoidal wave- shape, 180° conduction, $T_1 = 125 {}^{\circ}C$
I square t	$I^2 t$		620		KA^2s	8.3 msec and 10.0 msec
Latching current	IL		300		mA	$V_D = 24 \text{ V}; \text{ R}_L = 12 \text{ ohms}$
Holding current	I _H		90		mA	$V_{D=} 24 V; I = 2.5 A$
Peak on-state voltage	V _{TM}		2.25		V	I _{TM} = 1000 A; Tvj=125℃
Critical rate of rise of on-state current (5, 6)	di/dt		200		A/µs	Switching from $V_{DRM} \le 1500 \text{ V}$, non-repetitive
Critical rate of rise of on-state current (6)	di/dt		100		A/µs	Switching from $V_{DRM} \le 1500 \text{ V}$

ELECTRICAL CHARACTERISTICS AND RATINGS (cont'd)

KP760A/6500V

Gating

Parameter	Symbo l	Min.	Max.	Тур.	Units	Conditions
Peak gate power dissipation	\mathbf{P}_{GM}		200		W	$t_p = 40 \text{ us}$
Average gate power dissipation	P _{G(AV)}		5		W	
Peak gate current	I _{GM}		10		А	
Gate current required to trigger all units	I _{GT}		300 150 125		mA mA mA	$V_D = 6 V; R_L = 3 \text{ ohms}; T_j = -40 \ ^{\circ}\text{C}$ $V_D = 6 V; R_L = 3 \text{ ohms}; T_j = +25 \ ^{\circ}\text{C}$ $V_D = 6 V; R_L = 3 \text{ ohms}; T_j = +125 \ ^{\circ}\text{C}$
Gate voltage required to trigger all units	V _{GT}	0.30	53		V V V	$V_D = 6 V; R_L = 3 \text{ ohms}; T_j = -40 \text{ °C}$ $V_D = 6 V; R_L = 3 \text{ ohms}; T_j = 0.125 \text{ °C}$ $V_D = \text{Rated } V_{DRM}; R_L = 1000 \text{ ohms};$ $T_j = + 125 \text{ °C}$
Peak negative voltage	V _{GRM}		5		V	

Dynamic

Parameter	Symbo 1	Min.	Max.	Тур.	Units	Conditions
Delay time	t _d		3.0		μs	$I_{TM} = 50 \text{ A}; V_D = \text{Rated } V_{DRM}$ Gate pulse: $V_G = 20 \text{ V}; R_G = 20$ ohms; $t_r = 0.1 \mu\text{s}; t_p = 20 \mu\text{s}$
Turn-off time (with $V_R = -50 \text{ V}$)	t _q		800		μs	$I_{TM} = 1000 \text{ A}; \text{ di/dt} = 25 \text{ A/}\mu\text{s};$ $V_R \ge -50 \text{ V}; \text{ Re-applied } \text{dV/dt} = 20$ $V/\mu\text{s linear to 50\% } V_{DRM}; V_G = 0;$ $T_j = 125 ^{\circ}\text{C}; \text{ Duty cpstcle} \ge 0.01\%$
Reverse recovery charge	Qrr		*		μC	$I_{TM} = 760 \text{ A}; \text{ di/dt} = 5 \text{ A/}\mu\text{s};$ $V_R \ge 100 \text{V}$

* For guaranteed max. value, contact factory.

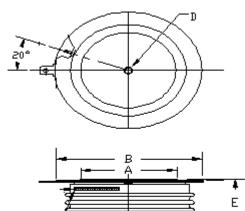
THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

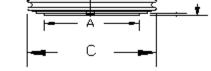
Parameter	Symbo l	Min.	Max.	Тур.	Units	Conditions
Operating temperature	Tj	-40	+125		°C	
Storage temperature	T _{stg}	-40	+150		°C	
Thermal resistance - junction to case	R _e (j-c)		0.025 0.050		°C/W	Double sided cooled Single sided cooled
Thermal resistance - case to sink	$R_{e (c-s)}$		0.010 0.020		°C/W	Double sided cooled * Single sided cooled *
Mounting force	F		24		kN	

* Mounting surfaces smooth, flat and greased

Note : for case outline and dimensions, see case outline drawing in page 4 of this Technical Data

KP760A/6500V





A:	47	mm
B:	74	mm
C:	66	mm
D:	¢5×3	mm
E:	36	mm