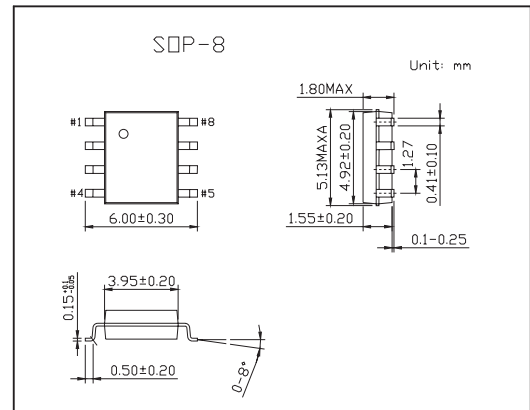
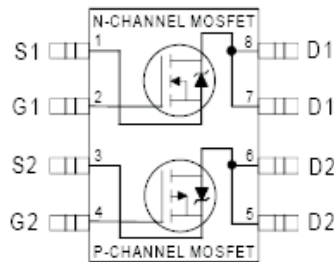


# HEXFET<sup>®</sup> Power MOSFET

## KRF7317

### ■ Features

- Generation V Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- Fully Avalanche Rated



### ■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	20	-20	V
Continuous Drain Current $T_a = 25^\circ\text{C}$	$I_D$	6.6	-5.3	A
Continuous Drain Current $T_a = 70^\circ\text{C}$	$I_D$	5.3	-4.3	
Pulsed Drain Current	$I_{DM}$	26	-21	
Continuous Source Current (Diode Conduction)	$I_S$	2.5	-2.5	
Power Dissipation @ $T_a = 25^\circ\text{C}$ *2	$P_D$	2.0		W
Power Dissipation @ $T_a = 70^\circ\text{C}$ *2		1.3		
Single Pulse Avalanche Energy	$E_{AS}$	100	150	mJ
Avalanche Current	$I_{AR}$	4.1	-2.9	A
Repetitive Avalanche Energy	$E_{AR}$	0.20		mJ
Peak Diode Recovery $dv/dt$ *1	$dv/dt$	5.0	-5	V/ ns
Gate-to-Source Voltage	$V_{GS}$	$\pm 12$		V
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150		$^\circ\text{C}$
Maximum Junction-to-Ambient *2	$R_{\theta JA}$	62.5		$^\circ\text{C/W}$

\*1 N-Channel  $I_{SD} \leq 4.1\text{A}$ ,  $di/dt \leq 92\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ\text{C}$

P-Channel  $I_{SD} \leq -2.9\text{A}$ ,  $di/dt \leq -77\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ\text{C}$

\*2 Surface mounted on FR-4 board,  $t \leq 10\text{sec}$ .

## KRF7317

## ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250 \mu A$	N-Ch	20		V	
		$V_{GS} = 0V, I_D = -250 \mu A$	P-Ch	-20			
Breakdown Voltage Temp. Coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_J}$	$I_D = 1mA, \text{Reference to } 25^\circ C$	N-Ch		0.027	V/°C	
		$I_D = -1mA, \text{Reference to } 25^\circ C$	P-Ch		0.031		
Static Drain-to-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 4.5V, I_D = 6.0A*1$	N-Ch		0.023	0.029	Ω
		$V_{GS} = 2.7V, I_D = 5.2A*1$			0.030	0.046	
		$V_{GS} = -4.5V, I_D = -2.9A*1$	P-Ch		0.049	0.058	
		$V_{GS} = -2.7V, I_D = -1.5A*1$			0.082	0.098	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	N-Ch	0.7		V	
		$V_{DS} = V_{GS}, I_D = -250 \mu A$	P-Ch	-0.7			
Forward Transconductance	$g_{fs}$	$V_{DS} = 10V, I_D = 6.0A*1$	N-Ch		20	S	
		$V_{DS} = -10V, I_D = -1.5A*1$	P-Ch		5.9		
Drain-to-Source Leakage Current	$I_{DSS}$	$V_{DS} = 16V, V_{GS} = 0V$	N-Ch			1.0	μA
		$V_{DS} = -16V, V_{GS} = 0V$	P-Ch			-1.0	
		$V_{DS} = 16V, V_{GS} = 0V, T_J = 55^\circ C$	N-Ch			5.0	
		$V_{DS} = -16V, V_{GS} = 0V, T_J = 55^\circ C$	P-Ch			-25	
Gate-to-Source Forward Leakage	$I_{GSS}$	$V_{GS} = \pm 12V$	N-Ch			±100	nA
			P-Ch			±100	
Total Gate Charge	$Q_g$	N-Channel $I_D = 6.0A, V_{DS} = 10V, V_{GS} = 4.5V$	N-Ch		18	27	nC
Gate-to-Source Charge	$Q_{gs}$	P-Channel	N-Ch		2.2	3.3	
			P-Ch		4.0	6.1	
Gate-to-Drain ("Miller") Charge	$Q_{gd}$	$I_D = -2.9A, V_{DS} = -16V, V_{GS} = -4.5V$	N-Ch		6.2	9.3	
			P-Ch		7.7	12	
Turn-On Delay Time	$t_{d(on)}$	N-Channel $V_{DD} = 10V, I_D = 1.0A, R_G = 6.0 \Omega$	N-Ch		8.1	12	ns
Rise Time	$t_r$	P-Channel $R_D = 10 \Omega$	N-Ch		17	25	
			P-Ch		40	60	
Turn-Off Delay Time	$t_{d(off)}$	$V_{DD} = -10V, I_D = -2.9A, R_G = 6.0 \Omega$ $R_D = 3.4 \Omega$	N-Ch		38	57	
			P-Ch		42	63	
Fall Time	$t_f$		N-Ch		31	47	
			P-Ch		49	73	
Input Capacitance	$C_{iss}$	N-Channel $V_{GS} = 0V, V_{DS} = 15V, f = 1.0MHz$	N-Ch		900		
			P-Ch		780		
Output Capacitance	$C_{oss}$	P-Channel	N-Ch		430		
			P-Ch		470		
Reverse Transfer Capacitance	$C_{rss}$	$V_{GS} = 0V, V_{DS} = -15V, f = 1.0MHz$	N-Ch		200		
			P-Ch		240		

## KRF7317

## ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit	
Continuous Source Current (Body Diode)	Is		N-Ch			2.5	A
			P-Ch			-2.5	
Pulsed Source Current (Body Diode) *2	ISM		N-Ch			26	A
			P-Ch			-21	
Diode Forward Voltage	VSD	TJ = 25°C, Is = 1.7A, VGS = 0V*3	N-Ch		0.72	1.0	V
		TJ = 25°C, Is = -2.9A, VGS = 0V*3	P-Ch		-0.78	-1.0	
Reverse Recovery Time	trr	N-Channel	N-Ch		52	77	ns
		TJ = 25°C, IF = 1.7A, di/dt = 100A/μs*	P-Ch		47	71	
Reverse RecoveryCharge	Qrr	P-Channel	N-Ch		58	86	nC
		TJ=25°C,IF=-2.9A,di/dt=-100A/μs*1	P-Ch		49	73	

\*1 Pulse width  $\leq 300 \mu s$ ; duty cycle  $\leq 2\%$ .

\*2 Repetitive rating; pulse width limited by max. junction temperature.

\*3 N-Channel Starting TJ = 25°C, L = 12mH RG = 25Ω, IAS = 4.1A.

P-Channel Starting TJ = 25°C, L = 35mH RG = 25Ω, IAS = -2.9A.