

PNP Epitaxial Silicon Transistor

KSA1298

Low Frequency Power Amplifier

Complement to KSC3265

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted.)

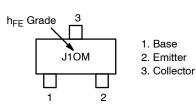
Symbol	Parameter	Ratings	Unit	
V _{CBO}	Collector-Base Voltage	-30	V	
V _{CEO}	Collector-Emitter Voltage	-25	V	
V _{EBO}	Emitter-Base Voltage	- 5	V	
I _C	Collector Current	-800	mA	
Ι _Β	Base Current	-160	mA	
Pc	Collector Power Dissipation	200	mW	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	−55 ~ 150	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



SOT-23 CASE 318

MARKING DIAGRAM



J1O = Specific Device Code
M = Date Code

ORDERING INFORMATION

Device	Package	Shipping
KSA1298YMTF	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-25	_	-	V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -1 \text{ mA}, I_C = 0$	-5	_	-	V
I _{CBO}	Collector Cut-off Current	$V_{CB} = -30 \text{ V}, I_E = 0$	-	_	-100	nA
I _{EBO}	Emitter Cut-off Current	$V_{BE} = -5 \text{ V}, I_{C} = 0$	-	_	-100	nA
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = -1 \text{ V, } I_{C} = -100 \text{ mA}$ $V_{CE} = -1 \text{ V, } I_{C} = -800 \text{ mA}$	100 40	1 1	320 -	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = -500 \text{ mA}, I_B = -20 \text{ mA}$	-	_	-0.4	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -1 \text{ V, } I_{C} = -10 \text{ mA}$	-0.5	_	-0.8	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	_	120	_	MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	-	13	-	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

h_{FE1} Classification

Classification	0	Y	
h _{FE1}	100 ~ 200	160 ~ 320	

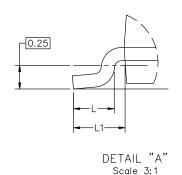




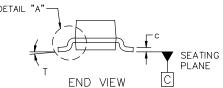
SOT-23 (TO-236) 2.90x1.30x1.00 1.90P **CASE 318 ISSUE AU**

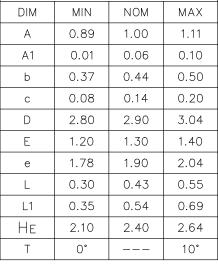
DATE 14 AUG 2024





DETAIL "A"





MILLIMETERS

NOTES:

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*

SIDE VIEW

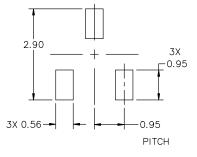


XXX = Specific Device Code

= Date Code

= Pb-Free Package

^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR		NODE D CONNECTION ATHODE	
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: STYLE 12: PIN 1. ANODE PIN 1. CA 2. CATHODE 2. CA 3. CATHODE-ANODE 3. AN	ATHODE PIN 1. SOURCE ATHODE 2. DRAIN	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE			STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: STYLE 24: PIN 1. ANODE PIN 1. GAT 2. ANODE 2. DR/ 3. CATHODE 3. SOU	TE PIN 1. ANODE AIN 2. CATHODE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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