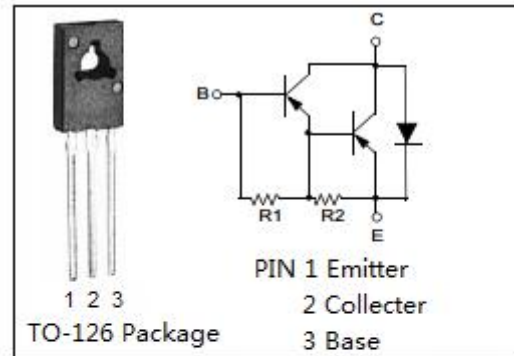


isc Silicon PNP Power Transistor

KSE700

DESCRIPTION

- High DC current gain
- Low Collector Saturation Voltage
- Complement to Type KSE800
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

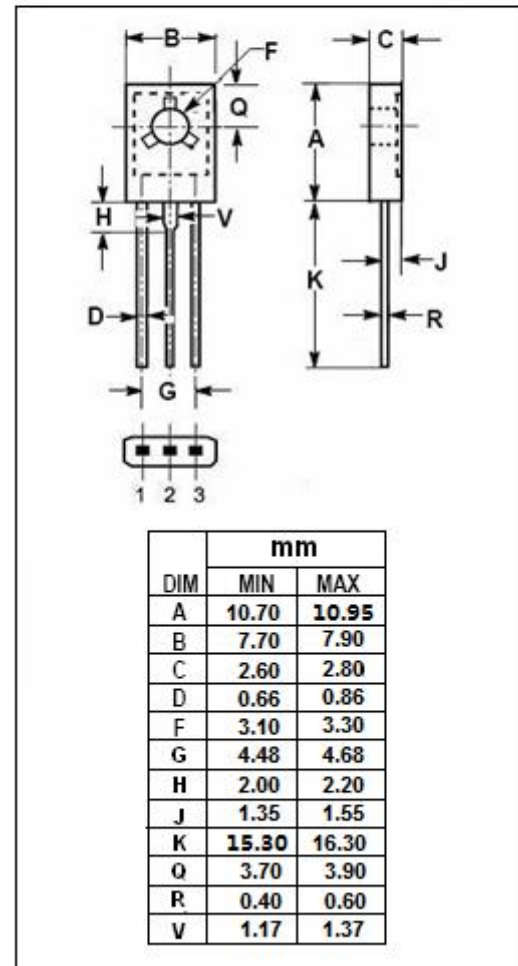


APPLICATIONS

- Monolithic construction with built-in-Base-Emitter resistor

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_c	Collector Current-Continuous	-4	A
P_c	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	40	W
	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	2	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



ELECTRICAL CHARACTERISTICST_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I _{CB0}	Collector Cutoff Current	V _{CB} = -60V; I _E = 0			-100	μ A
I _{EB0}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-2	mA
h _{FE-1}	DC Current Gain	I _C =- 1.5A; V _{CE} = -3V	750			
h _{FE-2}	DC Current Gain	I _C =- 4A; V _{CE} = -3V	100			
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = -1.5A; I _B =- 30mA			-2.5	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = -4A; I _B = -40mA			-3	V
V _{BE(ON)-1}	Base-Emitter On Voltage	I _C =- 1.5A; V _{CE} = -3V			-1.2	V
V _{BE(ON)-2}	Base-Emitter On Voltage	I _C =- 4A; V _{CE} = -3V			-3	V

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