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Part Number: KTIR0611S

Package Dimensions

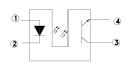
Features

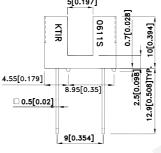
- Ultra-small.
- Minimal influence from stray light.
- Low collector-emitter saturation voltage.
- RoHS Compliant.

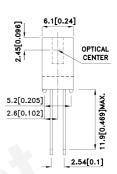
Applications

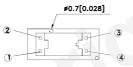
- •Optical control equipment.
- •Cameras.
- •Floppy disk drives.













Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.
- 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

*Absolute Maximum Ratings(Ta=25°C)

	Parameter	Symbol	Rating	Unit	
Input	Forward current		50	mA	
	Reverse voltage	V_R	6	V	
	Power dissipation	Pd	75	mW	
	Peak Forward Current (Pulse Width ≤100uS, Duty Cycle =1%)	I _{FP}	1	Α	
Output	Collector-emitter voltage	V_{CEO}	35	V	
	Emitter-collector voltage	V _{ECO}	6	V	
	Collector current	Ic	20	mA	
	Collector power dissipation	Pc	75	mW	
Operating temperature		Topr	-25~+85	°C	
Storage temperature		Tstg	-40~+100	°C	
soldering te	oldering temperature (1/16 inch from body for 5 seconds)		260	°C	





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*Electro-optical Characteristics(Ta=25°C)

Parameter		Symbol	Conditions	Min.	TYP.	Max.	Unit	
Input	Forward Voltage		V_{F}	I _F =20mA	1.0	1.2	1.5	V
	Reverse Current		I _R	V _R =6V	-	-	10	μΑ
	Peak Wavelength		λΡ	I _F =20mA	-	940	-	nm
Output	Collector dark current		I _{CEO}	V _{CE} =20V	-	-	100	nA
	Collector-emitter saturation voltage		V _{CE (SAT)}	I _C =1mA I _F =40mA	-	-	0.4	V
Transfer charact- eristics	Current transfer ratio		CTR	V _{CE} =5V I _F =20mA	2	14	-	%
	Response time	Rise time	tr	V _{CE} =2V	-	5	25	μsec
		Fall time	tf	I_C =2mA R_L =100 Ω	-	4	20	μsec

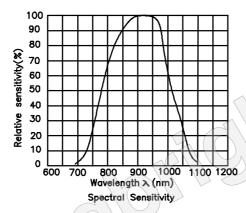


Fig. 1 Forward Current vs. Forward Voltage

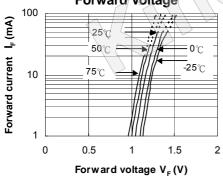


Fig. 3 Collector Current vs.
Collector-emitter Voltage

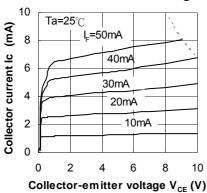
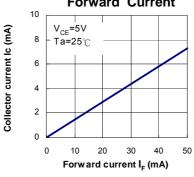


Fig. 2 Collector Current vs.
Forward Current



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Fig. 4 Collector Current vs.

Ambient Temperature

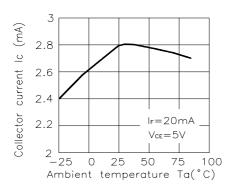


Fig.5 Collector-emitter Saturation Voltage vs.Ambient Temperature

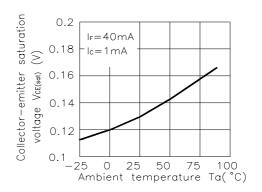


Fig.6 Relative Collector Current vs. Shield Distance (1)

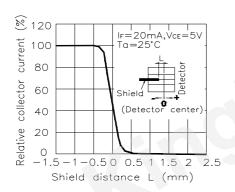


Fig.7 Relative Collector Current vs. Shield Distance (2)

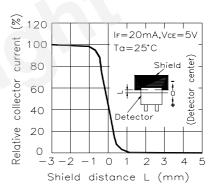
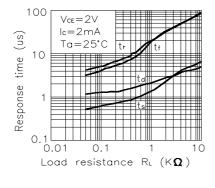
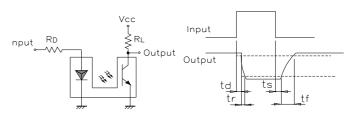


Fig.8 Response Time vs Load Resistance



Test Circuit for Response Time

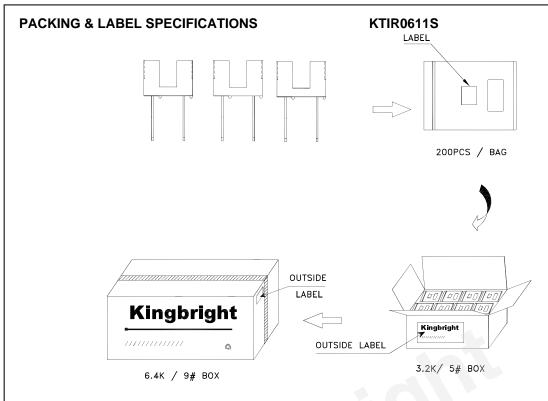


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