

Product Specifications

15.0" XGA Color TFT-LCD Module

Model Name: L150X3M

EC : -1

() Preliminary Specifications

(◆) Final Specifications

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ii Record of Revision

Version and Date	Page	Old description	New Description	Remark
0.1 2001/4/01	All	First Edition for Customer	All	
0.2 2001/7/25	10		Viewing angle (L/R/U/D) : 50/50/30/50 (min)	Add
			Color / Chromaticity : Rx : 0.603(min) / 0.663(max) Ry : 0.306(min) / 0.366(max) Gx : 0.264(min) / 0.324(max) Gy : 0.584(min) / 0.644(max) Bx : 0.115(min) / 0.175(max) By : 0.067(min) / 0.127(max) Wx : 0.283(min) / 0.343(max) Wy : 0.299(min) / 0.359(max)	Add
	16		LCD Drive Current : IDD : 600mA(typ.)/700mA(max) LCD Power Consumption : PDD : 2.0W(typ.) / 2.4W(max)	Add
	17		White Luminance : 170 cd/m2 (min) @ 6.5mA CCFL Ignition Voltage (25) : 1000Volt (min) CCFL Ignition Voltage (0) : 1300Volt (min)	Add
0.3 2001/8/08	10		Definition of Response time:	Add
	10	Response time :	Response time :	Change

		Tr: 16ms(max), Tf: 44ms(max) Tr+Tf : 60ms(max),	Tr: 15ms(max), Tf: 30ms(max) Tr+Tf : 45ms(max),	

1.0 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the CCFL Reflector edge. Instead, press at the far ends of the CFL Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure (LCD monitor housing, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- 13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source(2.11, IEC60950 or UL1950), or be applied exemption.
- 14) The LCD module is designed so that the CFL in it is supplied by Limited Current Circuit(2.4, IEC60950 or UL1950). Do not connect the CFL in Hazardous Voltage Circuit.

2.0 General Description

This specification applies to the 15.0 inch Color TFT-LCD Module L150X3M.

The display supports the XGA (1024(H) x 768(V)) screen format and 262,144 colors (RGB 6-bits data).

All input signals are 2 Channel TTL interface compatible.

This module does not contain an inverter card for backlight.

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Features

- XGA 1024(H) x 768(V) resolution
- 2 CCFLs (Cold cathode Fluorescent Lamp)
- High contrast ratio, high aperture ratio
- Wide viewing angle
- High speed response
- Low power consumption

Application

Desktop monitors

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 condition:

ITEMS	Unit	SPECIFICATIONS
Screen diagonal	[mm]	381 (15")
Outline dimension	[mm]	331.3 x 249.0 x 11.6 typ.
Display Area	[mm]	304.128 (H) x 228.096(38.1cm diagonal)
Resolution		1024(R,G,B x 3) x 768
Pixel Pitch	[mm]	0.297 x 0.297
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN mode, Normally White
Typical white Luminance.(5 points average)	[cd/m ²]	200nit (typ) @6.5mA,240nit (typ) @8mA (note 1)
Brightness uniformity		80% typ. (note 2)
Luminance uniformity		1.25 max.(Note 3)
Brightness variation		15% max.(note4)
Crosstalk (at 60Hz and 75 Hz)		1.2% max. (note 4)
Contrast Ratio		350 : 1 typ., 250:1 min
Support Colors		262,144 colors (6-bit for R,G,B)
Chromaticity(CIE1931)		0.313
White-x		
White-y		0.329
Color Gamut		60% typ., of NTSC coverage
Viewing angle		60(left),60(right),40(up),60(down)

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L150X3M-1 Ver0.3

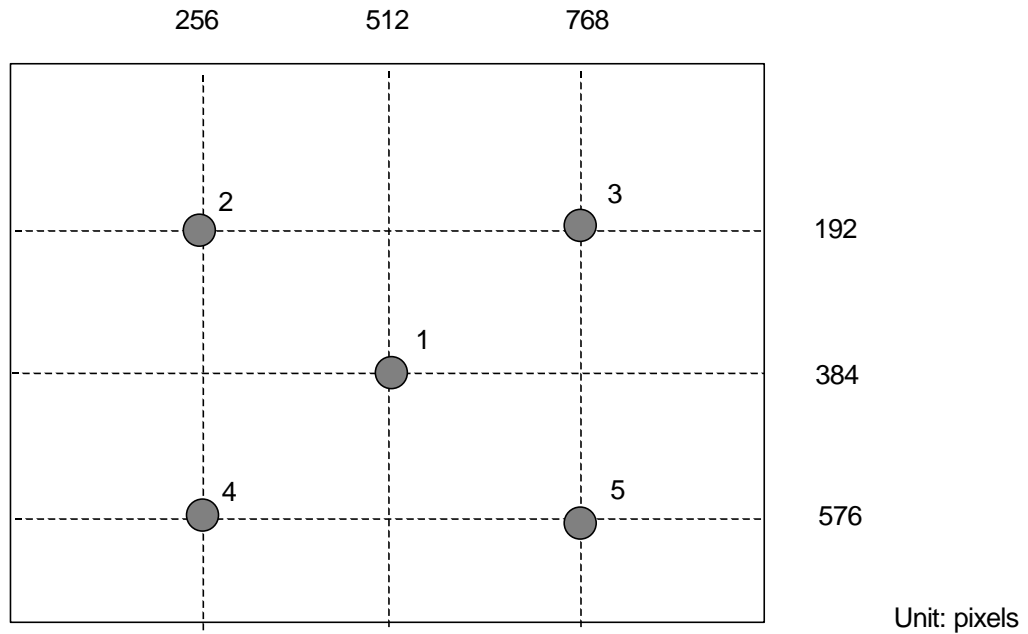
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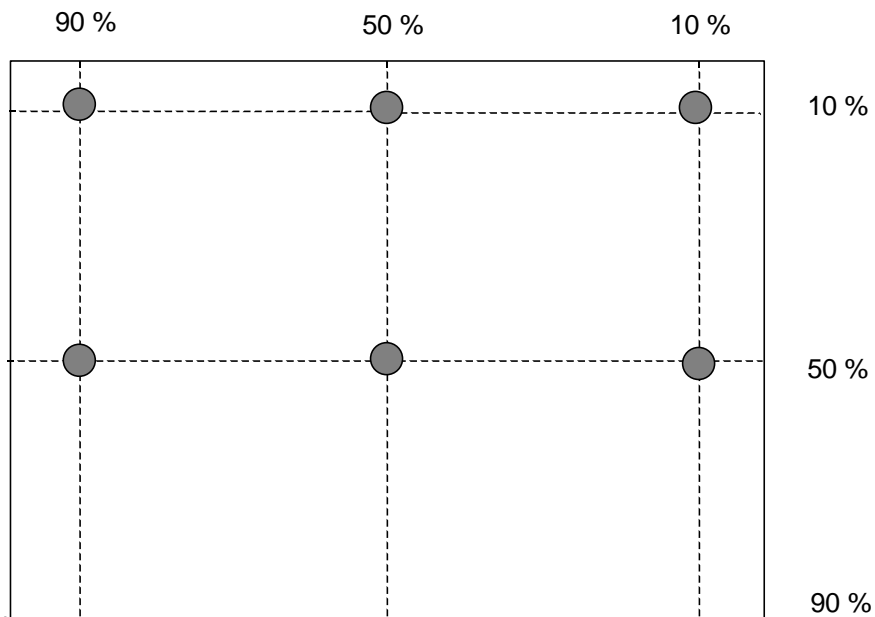
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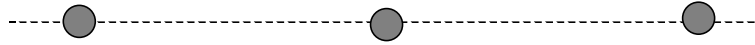
Response Time	[msec]	30ms typ. (Tr +Tf)
Nominal Input Voltage VDD	[Volt]	+3.3 V
Power Consumption (VDD line + CCFL line)	[Watt]	11 (typ.) @6.5mA
Electrical Interface		TTL 2 port
Frame rate	[Hz]	60Hz typ., 75Hz max.
Weight	[Grams]	1100 typ.
Mounting method		Side / Front mounting
Surface treatment		Anti-glare, hard coating (3H)
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60

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Note 1: Brightness is measured at the center point of brightness value at center location 1 with all pixels displaying white.





Unit: percentage of dimension of display area

Note 2: Brightness uniformity of these 9 points is defined as below:

$$| \text{Max. brightness} - \text{Min. brightness} | / \text{Max. brightness} \times 100\%$$

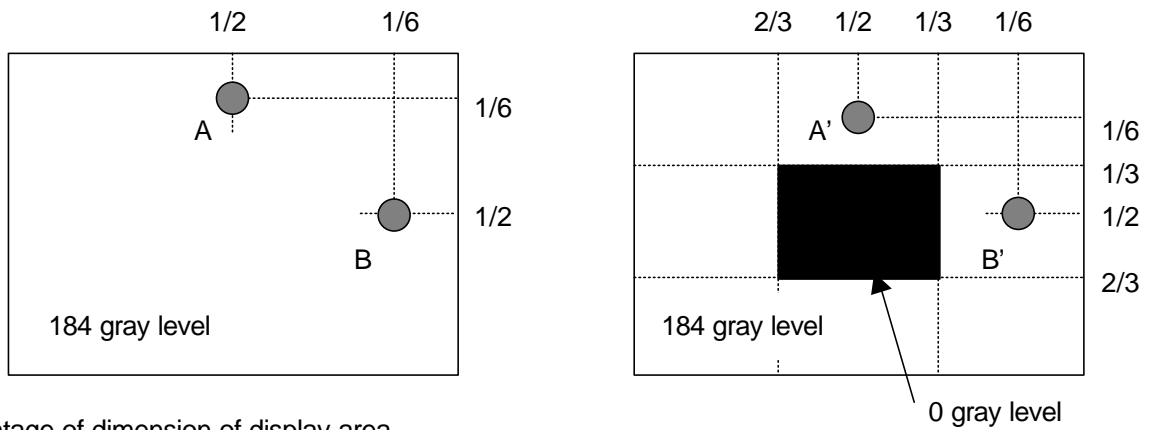
Note 3: TCO '99 Certification Requirements and test methods for environmental labeling of Display Report No. 2 defines

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Luminance uniformity as below:

$$((L_{\text{max},+30\text{deg.}} / L_{\text{min},+30\text{deg.}}) + (L_{\text{max},-30\text{deg.}} / L_{\text{min},-30\text{deg.}})) / 2$$

Note 4:



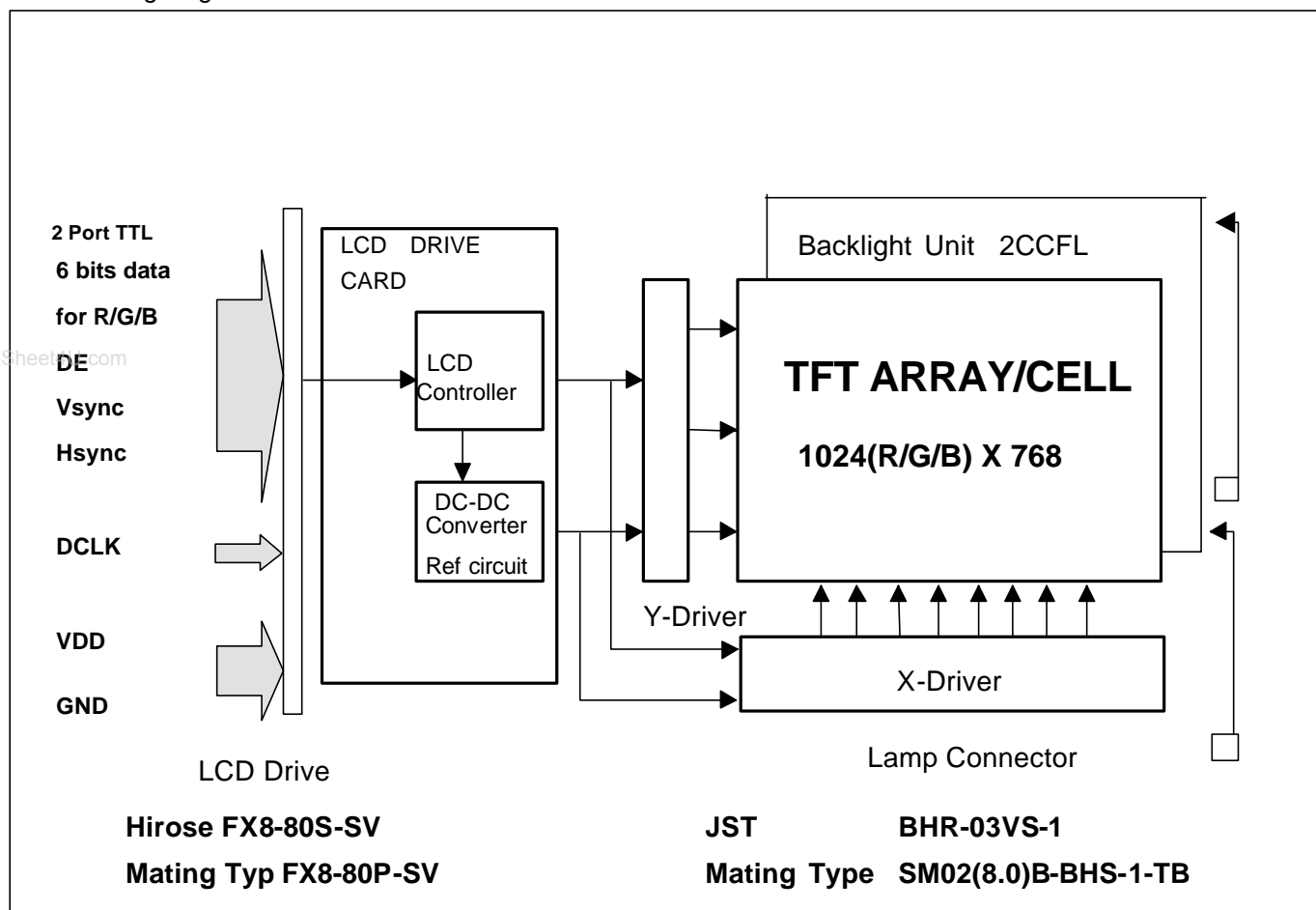
Unit: percentage of dimension of display area

$$| L_A - L_{A'} | / L_A \times 100\% = 1.2\% \text{ max.}, L_A \text{ and } L_B \text{ are brightness at location A and B}$$

$$| L_B - L_{B'} | / L_B \times 100\% = 1.2\% \text{ max.}, L_{A'} \text{ and } L_{B'} \text{ are brightness at location A' and B'}$$

2.2 Functional Block Diagram

The following diagram shows the functional block of 15.0 inches Color TFT-LCD Module:



3.0 Absolute Maximum Ratings

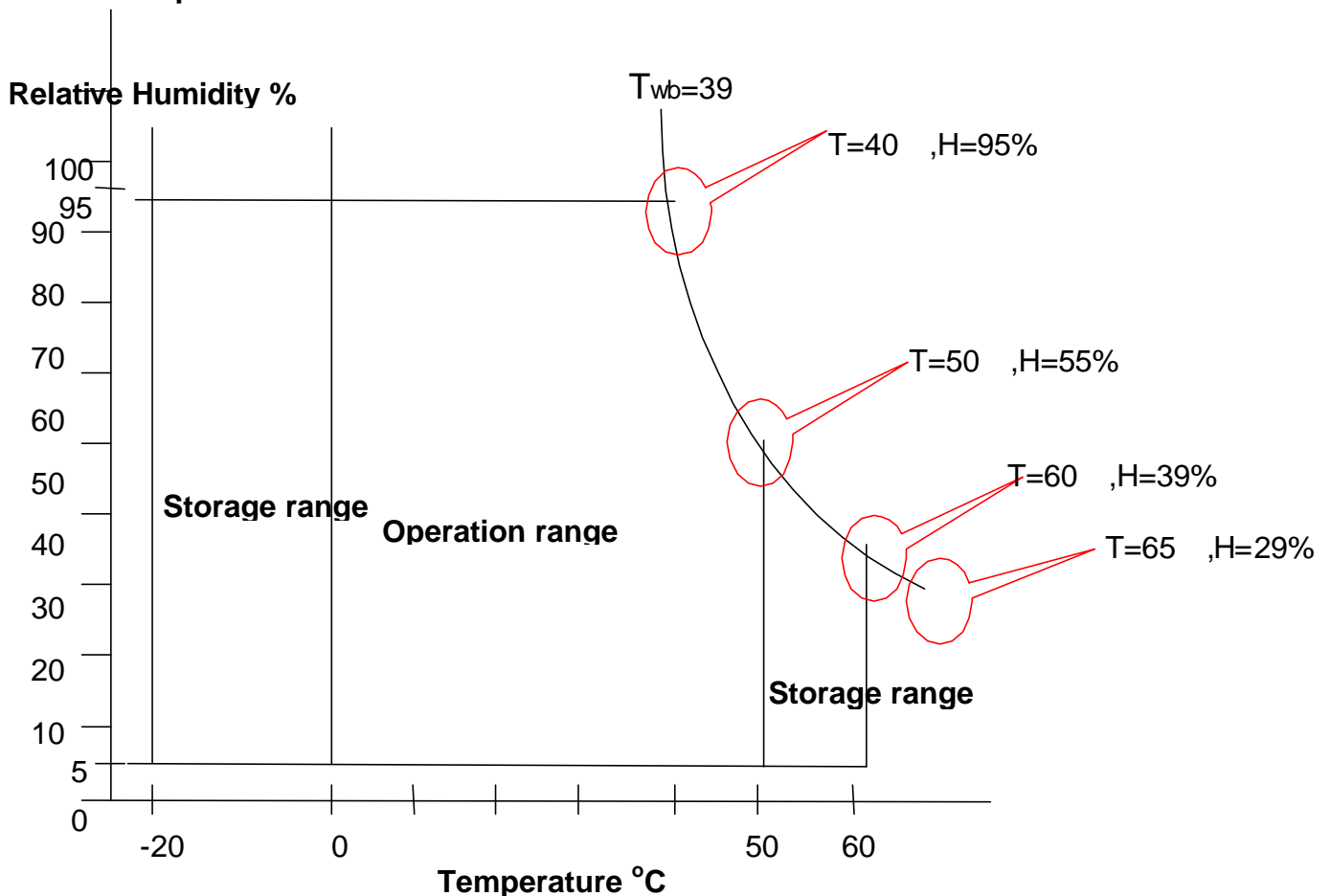
Absolute maximum ratings of the module is as following:

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	+3.6	[Volt]	
Input Voltage of Signal	Vin	-0.3	VDD+0.3	[Volt]	
CCFL Inrush current	ICFLL	-	20	[mA]	Note 1
CCFL Current	ICFL	-	8.0	[mA] rms	
CCFL Ignition Voltage	Vs	-	1,400	Vrms	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	20	85	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	5	95	[%RH]	Note 2
Vibration			1.5 / 10-200	[G / Hz]	
Shock			220 / 2	[G / ms]	Half sine wave
Assured Torque at Side Mount			2.0	[kgf.cm]	
Re-screw			3	[Times]	

Note 1 : Duration = 50msec

Note 2 : Maximum Wet-Bulb should be 39 and No condensation.

Wet bulb temperature chart



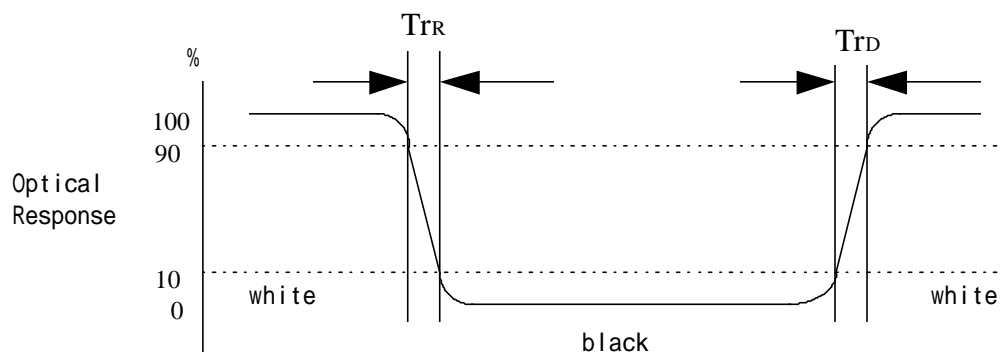
4.0 Optical Characteristics

The optical characteristics are measured under stable conditions at 25 (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.
Viewing Angle	[degree]	Horizontal (Right)	50	60	
	[degree]	CR = 10 (Left)	50	60	
CR: Contrast Ratio	[degree]	Vertical (Upper)	30	40	
	[degree]	CR = 10 (Lower)	50	60	
Contrast ratio		Normal Direction	250	350	-
Response Time(Note 1)	[msec]	Raising Time Ton (10%-90%)	-	8	15
	[msec]	Falling Time Toff (90%-10%)	-	22	30
	[msec]	Raising + Falling	-	30	45
Color / Chromaticity Coordinates (CIE)		Red x	0.603	0.633	0.663
		Red y	0.306	0.336	0.366
		Green x	0.264	0.294	0.324
		Green y	0.584	0.614	0.644
		Blue x	0.115	0.145	0.175
		Blue y	0.067	0.097	0.127
Color Coordinates (CIE) White		White x	0.283	0.313	0.343
		White y	0.299	0.329	0.359
Brightness Uniformity	[%]		75	80	-
White Luminance at CCFL 8.0mA	[cd/m ²]		200	240	-
White Luminance at CCFL 6.5mA			170	200	-
Crosstalk (in 75Hz)	[%]				1.2

Note 1: Definition of Response time:

The output signals of photodetector are measured when the input signals are changed from "Black" to "White" (falling time), and from "White" to "Black" (rising time), respectively. The response time interval between the 10% and 90% of amplitudes. Refer to figure as below.



5.0 Signal Interface

5.1 Module Interface Connectors

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components

Connector Name	Interface Connector
Manufacturer	Hirose or compatible
Type / Part Number	FX8-80S-SV
Mating Housing/Part Number	FX8-80P-SV

5.2 Module Connector Pin Configuration

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	GND	Ground	41	GND	Ground
2	GND	Ground	42	GND	Ground
3	GND	Ground	43	GND	Ground
4	RO0	Red data 0(odd)	44	GE0	Green data 0(even)
5	RO1	Red data 1(odd)	45	GE1	Green data 1(even)
6	GND	Ground	46	GND	Ground
7	RO2	Red data 2(odd)	47	GE2	Green data 2(even)
8	RO3	Red data 3(odd)	48	GE3	Green data 3(even)
9	RO4	Red data 4(odd)	49	GE4	Green data 4(even)
10	RO5	Red data 5(odd),MSB	50	GE5	Green data 5(even),MSB
11	GND	Ground	51	GND	Ground
12	GND	Ground	52	GND	Ground
13	GND	Ground	53	GND	Ground
14	GO0	Green data 0(odd)	54	BE0	Blue data 0(even)
15	GO1	Green data 1(odd)	55	BE1	Blue data 1(even)
16	GND	Ground	56	GND	Ground
17	GO2	Green data 2(odd)	57	BE2	Blue data 2(even)
18	GO3	Green data 3(odd)	58	BE3	Blue data 3(even)
19	GO4	Green data 4(odd)	59	BE4	Blue data 4(even)
20	GO5	Green data 5(odd),MSB	60	BE5	Blue data 5(even),MSB
21	GND	Ground	61	GND	Ground
22	GND	Ground	62	GND	Ground
23	GND	Ground	63	DCLK	Data input clock
24	BO0	Blue data 0(odd)	64	GND	Ground
25	BO1	Blue data 1(odd)	65	GND	Ground

26	GND	Ground	66	HSYNC	Horizontal sync signal
27	BO2	Blue data 2(odd)	67	GND	Ground
28	BO3	Blue data 3(odd)	68	GND	Ground
29	BO4	Blue data 4(odd)	69	DE	Data enable signal
30	BO5	Blue data 5(odd),MSB	70	VSYNC	Vertical sync signal
31	GND	Ground	71	VDD	Power supply +3.3V
32	GND	Ground	72	VDD	Power supply +3.3V
33	GND	Ground	73	VDD	Power supply +3.3V
34	RE0	Red data 0(even)	74	VDD	Power supply +3.3V
35	RE1	Red data 1(even)	75	VDD	Power supply +3.3V
36	GND	Ground	76	NC	No connection
37	RE2	Red data 2(even)	77	NC	No connection
38	RE3	Red data 3(even)	78	NC	No connection
39	RE4	Red data 4(even)	79	NC	No connection
40	RE5	Red data 5(even),MSB	80	GND	Ground

5.3 Backlight Connectors

Connector Name / Designation	For Lamp Connector
Manufacturer	JST or compatible
Type / Part Number	BHR-03VS-1
Mating Type / Part Number	SM02(8.0)B-BHS-1-TB

5.4 Backlight Connector Pin Configuration

Pin	Symbol	Description
1	HV	Lamp High Voltage
2	NC	No connection
3	LV	Ground

- ◆ Cable length: 115 +- 5 mm
- ◆ Connector-output position: right side(front view)
- ◆ Lamp assy design shall be easy for replacement and repair.

5.5 Signal Electrical Characteristics

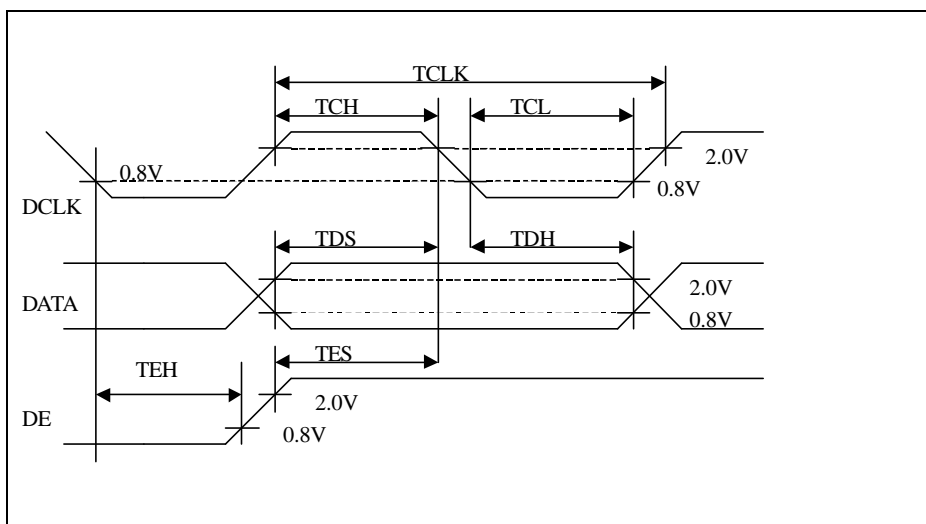
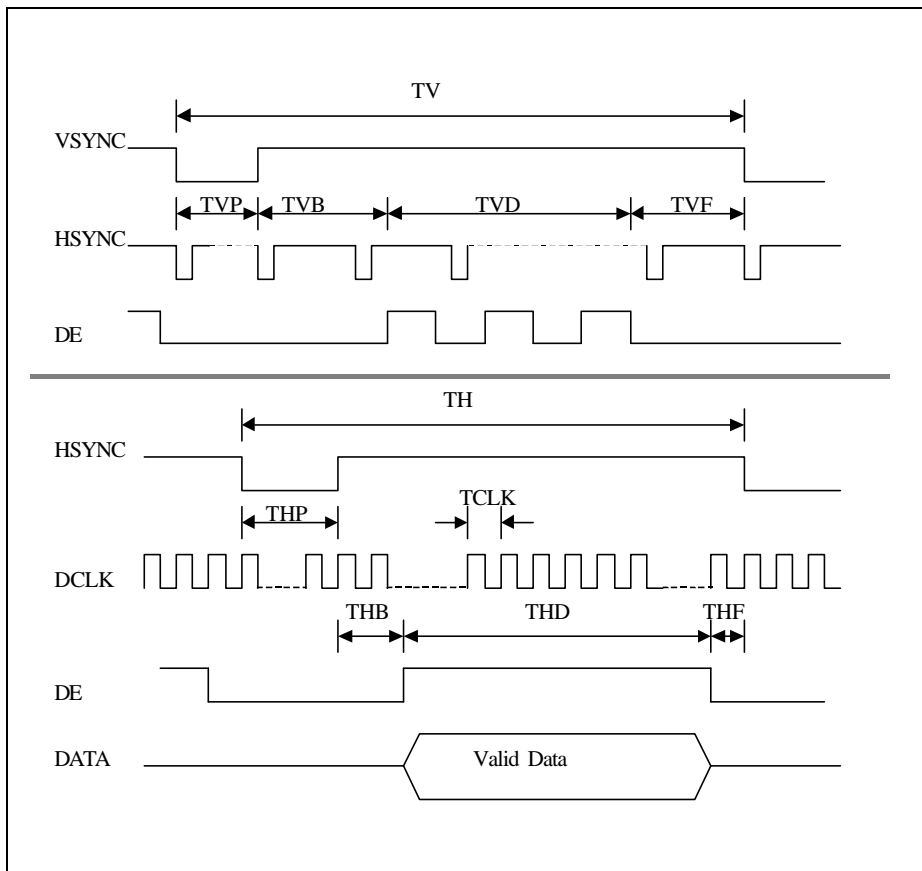
Each signal characteristics are as follows;

Item	Symbol	Min	Typ	Max	Unit
LCD Drive voltage	VDD	+3.0	+3.3	+3.6	[V]
“High” input signal voltage	Vih	2.0	-	-	[V]
“Low” input signal voltage	Vil	-	-	0.8	[V]

5.6 Interface Timings Characteristics

Signal	Item	Symbol	MIN	TYP	MAX	Unit
DCLK	Frequency	1/TDCLK	-	32.5	40.0	MHz
	Period	TDCLK	25	30.8	-	ns
	High time	TCH	0.4	0.5	0.6	TDCLK
	Low time	TCL	0.4	0.5	0.6	TDCLK
DATA	Setup time	TDS	5	-	-	ns
	Hold time	TDH	5	-	-	ns
Data Enable	Setup time	TES	5	-	-	ns
	Hold time	TEH	5	-	-	ns
Horizontal sync	Frequency	1/TH	-	48	60	KHz
	Pulse width	THP	2	68	-	TDCLK
Horizontal Signal	Back -porch	THB	1	80	-	TDCLK
	Display period	THD	512	512	512	TDCLK
	Front-porch	THF	0	12	-	TDCLK
	H total	TH	600	672	-	
Vertical sync	Frequency	1/TV	-	60	75	Hz
	Pulse width	TVP	1	6	-	TH
Vertical Signal	Back-porch	TVB	7	29	64	TH
	Display period	TVD	768	768	768	TH
	Front-porch	TVF	1	3	-	TH
	Vsync period + Vback-porch	TVP+TVB	5	-	15	

5.7 Interface Timing Definition



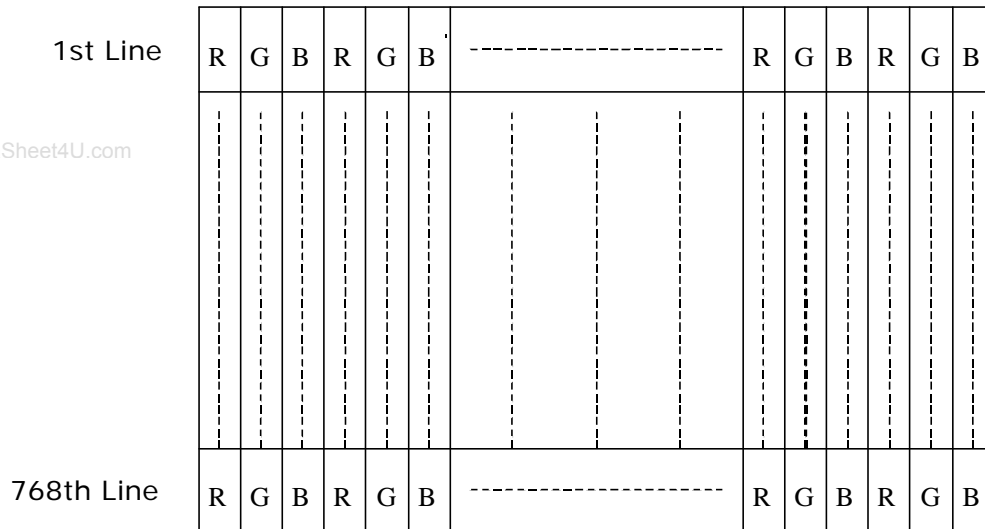
6.0 Pixel format image

Following figure shows the relationship of the input signals and LCD pixel format.

Odd : RO0~RO7,GO0~GO7,BO0~BO7

Even: RE0~RE7,GE0~GE7,BE0~BE7

Odd Even
 1 2 1023 1024



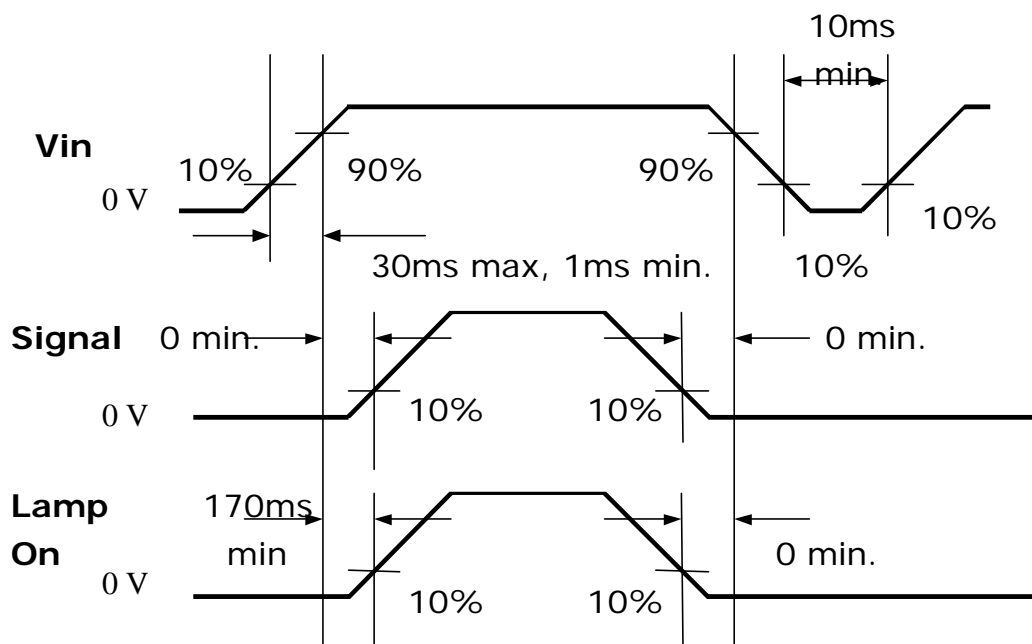
7.0 Power Consumption

Input power specifications are as follows;

Symbol	Parameter	Min	Typ	Max	Units	Condition
VDD	LCD Drive Voltage	3.0	3.3	3.6	[V]	
IDD	LCD Drive Current	-	600	700	[mA]	
PDD	LCD Drive power consumption	-	2.0	2.4	[Watt]	VDD=3.3v, All Black Pattern
VDDrp	Allowable LCD Drive Ripple Voltage			100	[mV] p-p	
VDDns	Allowable LCD Drive Ripple Noise			100	[mV] p-p	

8.0 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart.



9.0 Backlight Characteristics

9.1 Signal for Lamp connector

Pin #	signal Name
1	Lamp High Voltage
2	No connection
3	Ground

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9.2 Parameter guide line for CCFL Inverter

Symble	Parameter	Min	Typ	Max	Units	Condition
(L255)	White Luminance	170 @ 6.5mA	200 @ 6.5mA	-	[cd/m ²]	(Ta=25)
IRCFL	CCFL operation range	3.0	6.5	8.0	[mA] rms	(Ta=25)
ICFL	CCFL Inrush current	-	-	20	[mA]	Note 1
fCFL	CCFL Frequency	40	55	60	[KHz]	(Ta=25) Note 2
ViCFL (25) (reference)	CCFL Ignition Voltage	1000			[Volt] rms	(Ta= 25) Note 4
ViCFL (0) (reference)	CCFL Ignition Voltage	1300			[Volt] rms	(Ta= 0) Note 4
VCFL	CCFL Discharge Voltage (Reference)		630	690	[Volt] rms	(Ta=25) Note 3
PCFL	CCFL Power consumption @ 6.5mA (excluding inverter)		8.2	10	[Watt]	(Ta=25) Note 3

Note 1: Duration=50 [msec]

Note 2: CCFL Frequency should be carefully determined to avoid interference between inverter and TFT LCD

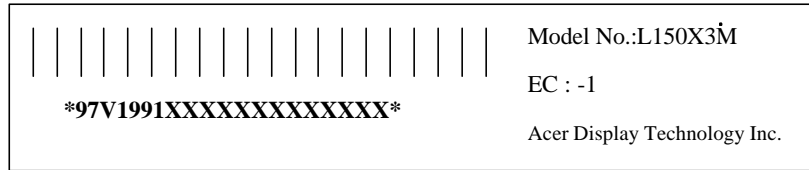
Note 3: Calculator value for reference (ICFL×VCFL=PCFL)

Note 4: CCFL inverter should be able to give out a power that has a generating capacity of over 1300 voltage.
Lamp units need 1300 voltage minimum for ignition

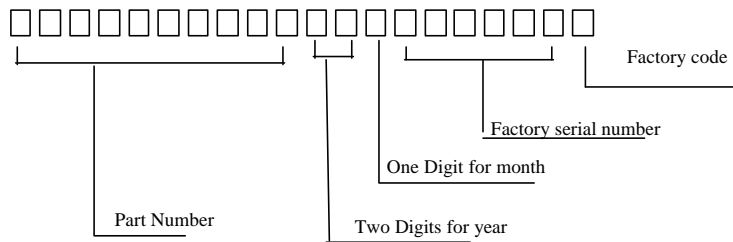
10.0 Label, Vibration, Shock, and Drop

10.1 Label

Unit mark is printed on a label. The label is shown as below:



Model No: L150X3M-1



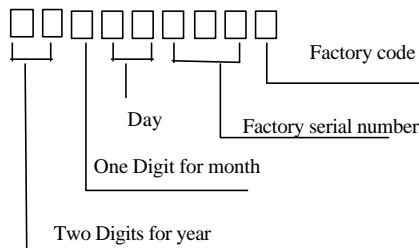
Serial No:

Factory Code: H: HsinChu, L: Lungtan

Year	Mark
1997	97
1998	98
1999	99
2000	00
2001	01
2002	02

Month	Mark	Month	Mark
1	1	7	7
2	2	8	8
3	3	9	9
4	4	10	A
5	5	11	B
6	6	12	C

CARTON NO:



10.2 Vibration & Shock

The module shall work error free after following vibration and shock condition. Likewise the module shall not sustain any damage after vibration and shock test.

10.2.1 Vibration Test Spec:

- Frequency: 10 - 200Hz
- Sweep: 30 Minutes each Axis (X, Y, Z)
- Acceleration: 1.5G(10~200Hz P- P)

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Acceleration (G)	1.5
Frequency (Hz)	10~200~10
Active time(min)	30

10.2.2 Shock Test Spec:

Acceleration (G) -a	50
Active time(ms) -b	20
Wave form	half-sin
Times	1

- Direction: $\pm X$, $\pm Y$, $\pm Z$

10.3 Drop

Package test: The drop height is defined as 60 cm.

11.0 Environment

The display module will meet the provision of this specification during operating condition or after storage or shipment condition specified below. Operation at 10% beyond the specified range will not cause physical damage to the unit.

11.1 Temperature and Humidity

11.1.1 Operating Conditions

The display module operates error free, when operated under the following conditions;

Temperature	0 °C to 50 °C
Relative Humidity	20% to 85%
Wet Bulb Temperature	39.0 °C

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11.1.2 Shipping Conditions

The display module operates error free, after the following conditions;

Temperature	-20 °C to 60 °C
Relative Humidity	5% to 95%
Wet Bulb Temperature	39.0 °C

11.2 Atmospheric Pressure

The display assembly is capable of being operated without affecting its operations over the pressure range as following specified:

	Pressure	Altitude
Maximum Pressure	1040 hPa	0 m = sea level
Minimum Pressure	601 hPa	3658 m = 12,000 feet

Note: Non-operation altitude limit of this display module = 40,000 feet. = 12193 m.

11.3 Thermal Shock

The display module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again.

Thermal shock cycle	-20 °C for 30min
	60 °C for 30min

Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before powering on.

12.0 Reliability

This display module and the packaging of that will comply following standards.

12.1 Failure Criteria

The display assembly will be considered as failing unit when it no longer meets any of the requirements stated in this specification. Only as for maximum white luminance, following criteria is applicable.

- **Maximum white Luminance shall be 75 cd/m² or more.**

12.2 Failure Rate

The average failure rate of the display module (from first power-on cycle till 1,000 hours later) will not exceed 1.0%. The average failure rate of the display module from 1,000 hours until 10,000 hours will not exceed 0.70% per 1000 hours.

12.2.1 Usage

The assumed usage for the above criteria is:

- 220 power-on hours per month
- 500 power on/off cycles per month
- Maximum brightness setting
- Operation to be within office environment (25°C typical)

12.2.2 Component De-rating

All the components used in this device will be checked the load condition to meet the failure rate criteria.

12.3 CCFL Life

The assumed CCFL Life will be longer than 50,000 hours under stable condition at 25 ± 5 °C;

Standard current at 6.5 ± 0.5 mA

Definition of life: brightness becomes 50% or less than the minimum luminance value of CCFL.

12.4 ON/OFF Cycle

The display module will be capable of being operated over the following ON/OFF Cycles.

ON/OFF	Value	Cycle
+VDD and CCFL power	36,000	10 seconds on / 10 seconds off

13.0 Safety

13.1 Sharp Edge Requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

13.2 Materials

13.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible ADT Toxicologist.

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13.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process. The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

13.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

13.4 Hazardous Voltages

Any point exceeding 42.4 volts meets the requirement of the limited current circuit. The current through a 2K resistance is less than $0.7 \times f$ (kHz) mA.

14.0 Other requirements

14.1 Smoke Free Design

By any single failure, any smoke nor strange smell shall not be observed by the operator.

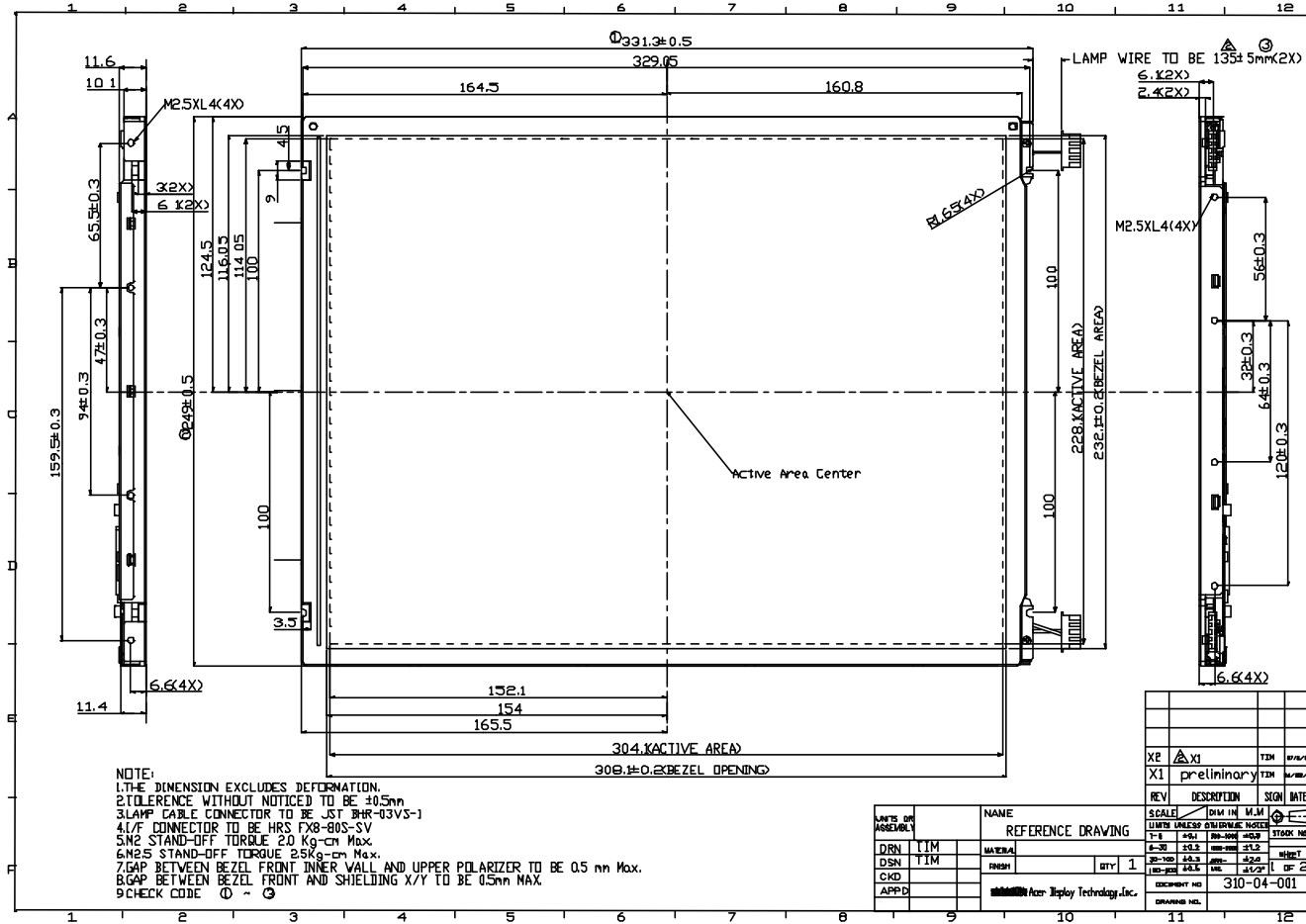
14.2 National Test Lab Requirement

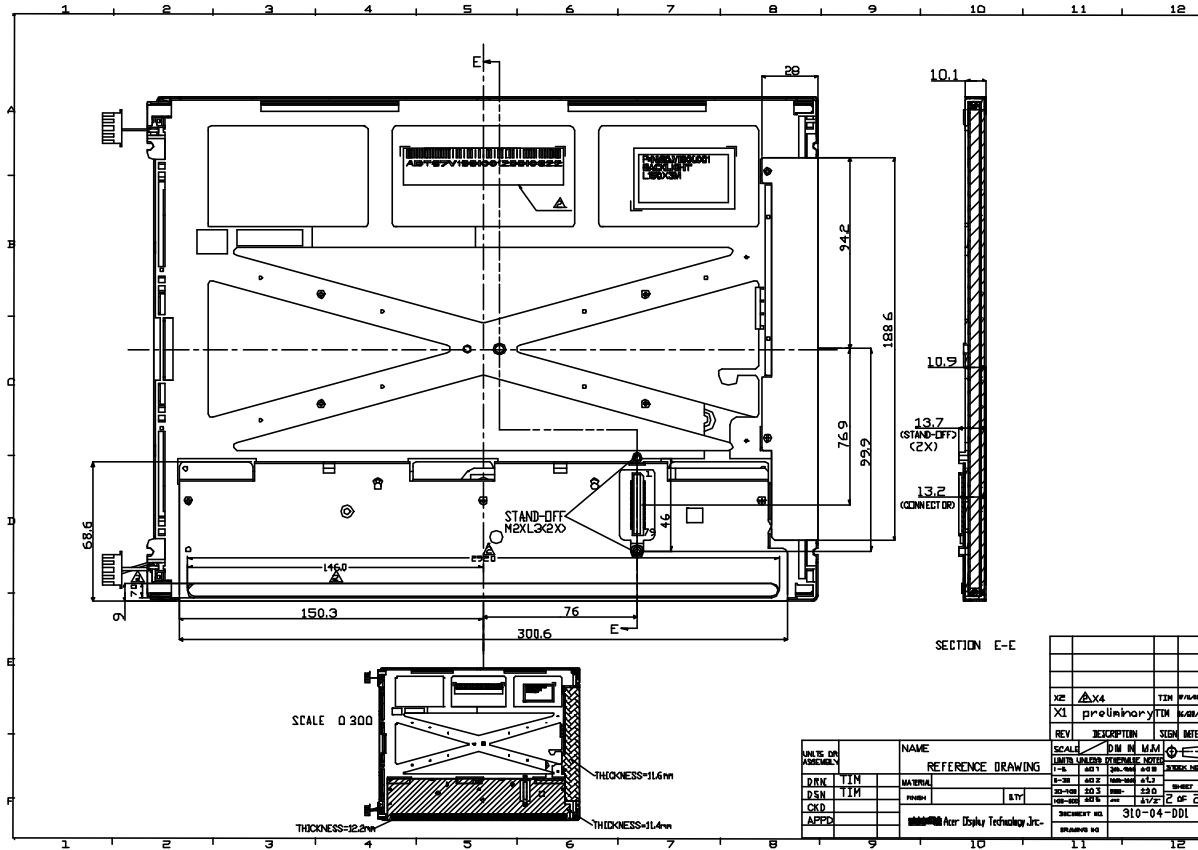
The display module will satisfy all requirements for compliance to the following requirement:

UL 1950, First Edition	U.S.A. Information Technology Equipment
CSA C22.2 No.950-M89	Canada, Information Technology Equipment
IEC 950	International, Information Technology Equipment
EN 60 950	International, Information Processing Equipment (European Norm for IEC950)

The construction of the display module is designed to suppress EMI. When mounted into a specified host system, the system will meet the following EMI requirement:

FCC Part 15 Class B
VCCI Class 2
CISPR 22, class B
Taiwan CNS standard
China CCIB standard





DATE OF ASSEMBLY	NAME	SCALE	DESCRIPTION	SIGN	DATE
DRN	TIN	1:1	ASSEMBLY		
DSN	TIP	1:1	DESIGN		
CKD					
APPD					

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