



L6219

LINEAR INTEGRATED CIRCUIT

DUAL FULL-BRIDGE PWM MOTOR DRIVER

DESCRIPTION

The **L6219** motor driver is designed to drive both windings of a bipolar stepper motor or bidirectionally control two dc motors. Both bridges are capable of sustaining 45V and include internal pulse-width modulation (PWM) control of the output current to 750mA .The outputs have been optimized for a low output saturation voltage drop (less than 1.8V total source plus sink at 500mA).

For PWM current control, the maximum output current is determined by the user's selection of a reference voltage and sensing resistor. Two logic-level inputs select output current limits of 0, 33, 67, or 100% of the maximum level. A phase input to each bridge determines load current direction.

The bridges include both ground clamp and flyback diodes for protection against inductive transients. Internally generated delays prevent cross-over currents when switching current direction. Special power-up sequencing is not required. Thermal protection circuitry disables the outputs if the chip temperature exceeds safe operating limits.

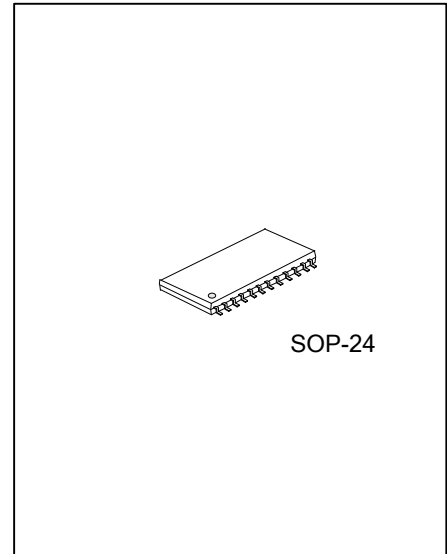
FEATURES

- *Interchangeable with SGS **L6219**
- *750mA Continuous Output Current
- *45V Output Sustaining Voltage
- *Internal Clamp Diodes
- *Internal PWM Current Control
- *low Output Saturation Voltage
- *Internal Thermal Shutdown Circuitry

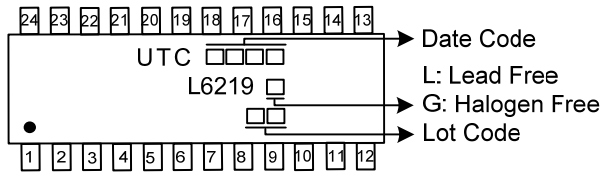
ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
L6219L-S24-R	L6219G-S24-R	SOP-24	Tape Reel

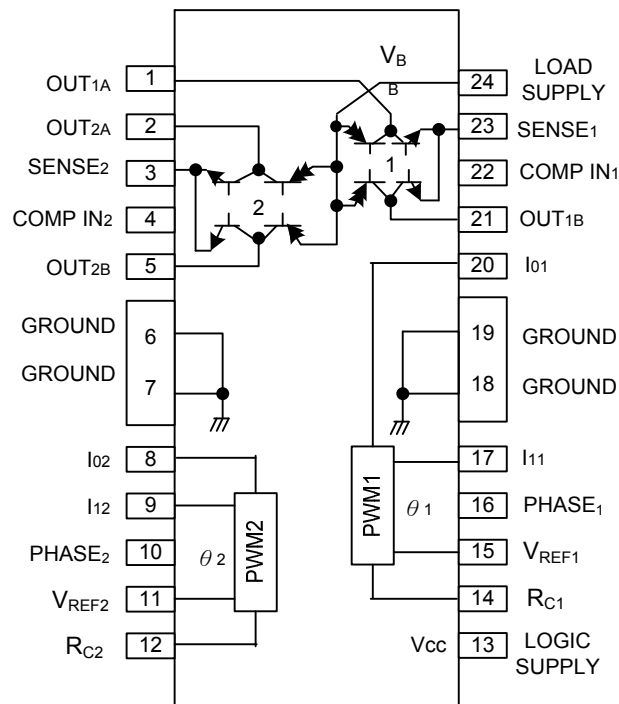
<p>L6219G-S24-R</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Green Package 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) S24: SOP-24 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



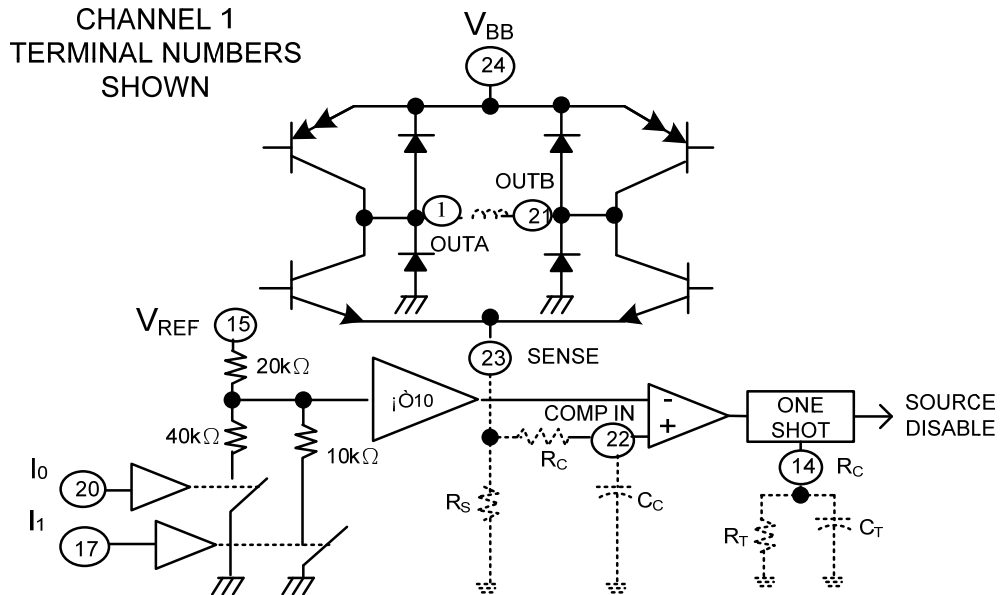
PIN CONFIGURATION



■ TRUTH TABLE

PHASE	OUTA	OUTB
H	H	L
L	L	H

■ PWM CURRENT-CONTROL CIRCUITRY



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Motor Supply Voltage	V_{BB}	45	V
Output Current	I_{OUT}	Peak	1.0
		Continuous	750
Logic Supply Voltage	V_{CC}	7.0	V
Logic Input Voltage Range	V_{IN}	-0.3 ~ +7.0	V
Output Emitter Voltage	V_{SENSE}	1.5	V
Package Power Dissipation	P_D	See graph	
Junction Temperature	T_J	+125	$^\circ\text{C}$
Operating Temperature	T_{OPR}	-20 ~ +85	$^\circ\text{C}$
Storage Temperature	T_{STG}	-40 ~ +150	$^\circ\text{C}$

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. Output current rating may be limited by duty cycle, ambient temperature, and heat sinking. Under any set of conditions, do not exceed the specified peak current rating or a junction temperature of +150 $^\circ\text{C}$

■ ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$, $V_{CC}=4.75\text{V}$ to 5.25V , $V_{BB}=45\text{V}$, $T_J\leq 150^\circ\text{C}$, $V_{REF}=5.0\text{V}$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Drivers(OUTA or OUTB)						
Motor Supply Range	V_{BB}		10	-	45	V
Output Leakage Current	I_{LEAK}	$V_{OUT}=V_{BB}$	-	<1.0	50	μA
		$V_{OUT}=0$	-	<-1.0	-50	μA
Output Sustaining Voltage	$V_{CE(SUS)}$	$I_{OUT}=\pm 750\text{mA}, L=3.0\text{mH}$	45	-	-	V
Output Saturation Voltage	$V_{CE(SAT)}$	Sink Driver, $I_{OUT}=+500\text{mA}$	-	0.4	0.6	V
		Sink Driver, $I_{OUT}=+750\text{mA}$	-	1.0	1.2	V
		Source Driver, $I_{OUT}=-500\text{mA}$	-	1.0	1.2	V
		Source Driver, $I_{OUT}=-750\text{mA}$	-	1.3	1.5	V
Clamp Diode Leakage Current	I_R	$V_R=45\text{V}$	-	<1.0	50	μA
Clamp Diode Forward Voltage	V_F	$I_F=750\text{mA}$	-	1.6	2.0	V
Driver Supply Current	$I_{BB(ON)}$	Both Bridges On, No Load	-	20	25	mA
	$I_{BB(OFF)}$	Both Bridge Off	-	5.0	10	mA
Control Logic						
Input Voltage	$V_{IN(1)}$	All Inputs	2.4	-	-	V
	$V_{IN(0)}$	All Inputs	-	-	0.8	V
Input Current	$I_{IN(1)}$	$V_{IN}=2.4\text{V}$	-	<1.0	20	μA
		$V_{IN}=0.8\text{V}$	-	-3.0	-200	μA
Reference Voltage Range	V_{REF}	Operating	1.5	-	7.5	V
Current Limit Threshold (at trip point)	V_{REF}/V_{COMPIN}	$I_0=I_1=0.8\text{V}$	9.5	10	10.5	-
		$I_0=2.4\text{V}, I_1=0.8\text{V}$	13.5	15	16.5	-
		$I_0=0.8\text{V}, I_1=2.4\text{V}$	25.5	30	34.5	-
Total Logic Supply Current	$I_{CC(ON)}$	$I_0=I_1=0.8\text{V}$, No Load	-	40	50	mA
	$I_{CC(OFF)}$	$I_0=I_1=2.4\text{V}$, No Load	-	10	12	
Thermal Shutdown Temperature	T_{SHDN}		-	170	-	$^\circ\text{C}$

■ APPLICATION INFORMATION

Logic Control Of Output Current

Two logic level inputs (I_0 and I_1) allow digital selection of the motor winding current at 100%, 67%, 33%, or 0% of the maximum level per the table. The 0% output current condition turns off all drivers in the bridge and can be used as an OUTPUT ENABLE function.

These logic level inputs greatly enhance the implementation of μ P-controlled drive formats.

During half-step operations, the I_0 and I_1 allow the μ P to control the motor at a constant torque between all positions in an eight-step sequence. This is accomplished by digitally selection 100% drive current when only one phase is on and 67% drive current when two phases are on. Logic highs on both I_0 and I_1 turn off all drivers to allow rapid current decay when switching phases. This helps to ensure proper motor operation at high step rates.

The logic control inputs can also be used to select a reduced current level (and reduced power dissipation) for "hold" conditions and/or increased current (and available torque) for start-up conditions.

Current-Control Truth Table

I_0	I_1	Output Current
L	L	$V_{REF}/10R_S = I_{TRIP}$
H	L	$V_{REF}/15R_S = 2/3 I_{TRIP}$
L	H	$V_{REF}/30R_S = 1/3 I_{TRIP}$
H	H	0

General

The PHASE input to each bridge determines the direction motor winding current flows. An internally generated dead time (approximately 2 μ s) prevents crossover currents that can occur when switching the PHASE input.

All four drivers in the bridge output can be tuned off between steps ($I_0=I_1 \geq 2.4V$) resulting in a fast current decay through the internal output clamp and flyback diodes. The fast current decay is desirable in half-step and high-speed applications. The PHASE, I_0 , and I_1 inputs float high.

Varying the reference voltage (V_{REF}) provides continuous control of the peak load current for microstepping applications.

Thermal protection circuitry turns off all drivers when the junction temperature reaches +170°C. It is only intended to protect the device from failures due to excessive junction temperature and should not imply that output short circuits are permitted. The output drivers are re-enabled when the junction temperature cools to +145°C.

The UTC L6219 output drivers are optimized for low output saturation voltages-less than 1.8V total (source plus sink) at 500mA. Under normal operating conditions, when combined with the excellent thermal properties of the batwing package design, this allows continuous operation of both bridges simultaneously at 500mA.

Current Sensing

To minimize current sensing inaccuracies caused by ground trace IR drops, each current-sensing resistor should have a separate return to the ground terminal of the device. For low-value sense resistors, the IR drops in the PCB can be significant and should be taken into account. The use of sockets should be avoided as their contact resistance can cause variations in the effective value of R_S .

Generally, larger values of R_S reduce the aforementioned effects but can result in excessive heating and power loss in the sense resistor. The selected value of R_S should not cause the absolute maximum voltage rating of 1.5 V, for the SENSE terminal, to be exceeded. The recommended value of R_S is in the range of:

$$R_S = 0.75 / I_{TRIP(max)} \pm 50\%$$

If desired, the reference input voltage can be filtered by placing a capacitor from REFIN to ground. The ground return for this capacitor as well as the bottom of any resistor divider used should be independent of the high-current power-ground trace to avoid changes in REFIN due to IR drops.

■ APPLICATION INFORMATION (Cont.)

Thermal Considerations

For reliable operation, it is recommended that the maximum junction temperature be kept below 110°C to 125°C. The junction temperature can be measured best by attaching a thermocouple to the power pins (6, 7, 18 and 19) of the device and measuring the pin temperature, T_{TAB} . The junction temperature can then be approximated by using the formula:

$$T_J = T_{TAB} + (2 \times I_{LOAD} \times V_F \times R_{\theta JT})$$

where V_F can be chosen from the electrical specification table for the given level of I_{LOAD} . The value for $R_{\theta JT}$ is approximately 6°C/W.

The power dissipation can be improved 20% to 30% by adding a section of printed circuit board copper (typically 6 to 18 square centimeters) connected to the power pins of the device.

The thermal performance in applications that run at high load currents, high duty cycles, or both can be improved by adding external diodes from each output to ground in parallel with the internal diodes. Fast-recovery (≤ 200 ns) diodes should be used to minimize switching losses.

Load Supply Terminal

The load supply terminal, V_{BB} , should be decoupled with an electrolytic capacitor ($\geq 47\mu\text{F}$ is recommended), placed as close to the device as is physically practical. To minimize the effect of system ground IR drops on the logic and reference input signals, the system ground should have a low-resistance return to the load supply voltage.

Fixed Off-Time Selection

With increasing values of t_{OFF} , switching losses decrease, low level load current regulation improves, EMI reduces, PWM frequency decreases, and ripple current increases. The value of t_{OFF} can be chosen for optimization of these parameters. For applications where audible noise is a concern, typical values of t_{OFF} should be chosen in the range of 15 to 35 μs .

■ PWM CURRENT CONTROL

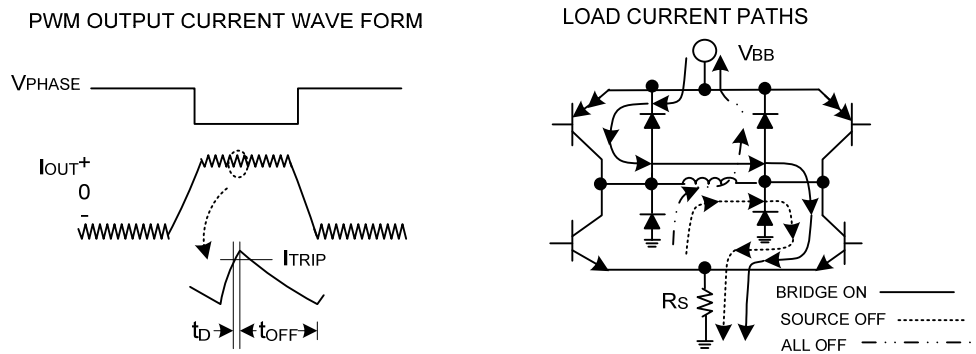
The UTC **L6219** dual bridge is designed to drive both windings of a bipolar stepper motor. Output current is sensed and controlled independently in each bridge by an external sense resistor (R_s), internal comparator, and monostable multivibrator.

When the bridge is turned on, current increases in the motor winding and it is sensed by the external sense resistor until the sense voltage (V_{COMPIN}) reaches the level set at the comparator's input: $I_{TRIP} = V_{REF} / 10R_s$

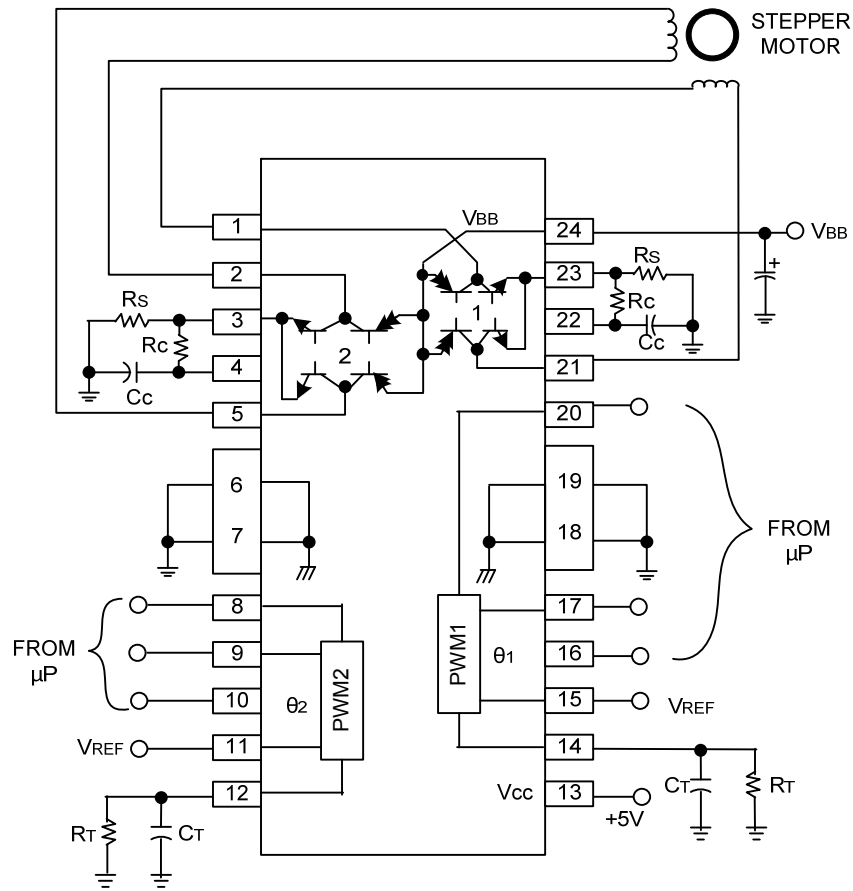
The comparator then triggers the monostable which turns off the source driver of the bridge. The actual load current peak will be slightly higher than the trip point (especially for low-inductance loads) because of the internal logic and switching delays. This delay (t_d) is typically $2\mu s$. After turn-off, the motor current decays, circulating through the ground-clamp diode and sink transistor. The source driver's off time (and therefore the magnitude of the current decrease) is determined by the monostable's external R_C timing components, where $t_{OFF} = R_T C_T$ within the range of $20k\Omega$ to $100k\Omega$ and $100pF$ to $1000pF$

When the source driver is re-enabled, the winding current (the sense voltage) is again allowed to rise to the comparator's threshold. This cycle repeats itself, maintaining the average motor winding current at the desired level.

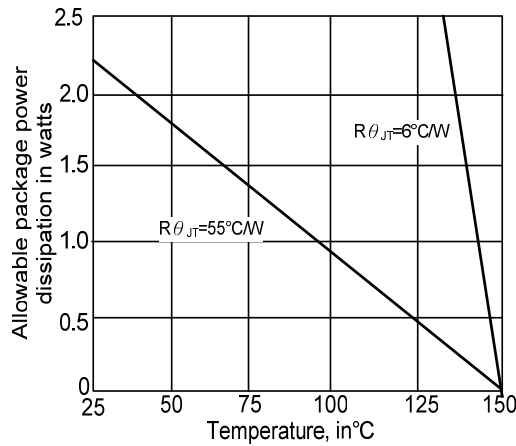
Loads with high distributed capacitances may result in high turn-on current peaks. This peak (appearing across R_s) will attempt to trip the comparator, resulting in erroneous current control or high-frequency oscillations. An external $R_C C_C$ time delay should be used to further delay the action of the comparator. Depending on load type, many applications will not require these external components (SENSE connected to COMP IN).



■ TYPICAL APPLICATION



■ TYPICAL CHARACTERISTIC



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