

SANYO Semiconductors **DATA SHEET**



Monolithic Linear IC Separately-excited Step-down Switching Regulator (Variable Type)

Overview

The LA5779N is a Separately-excited step-down switching regulator (variable type).

Functions

- High efficiency.
- Six external parts.
- Time-base generator (160kHz) incorporated.
- Current limiter incorporated.
- Thermal shutdown circuit incorporated.
- ON/OFF function.

Specifications Absolute Maximum Ratings at $Ta = 25^{\circ}C$

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Parameter	Symbol	Conditions	Ratings	Unit
Maximum Input voltage	V _{IN} max		30	V
Maximum Output current	I _O max		3	А
SW pin application reverse voltage	V _{SW}		-1	V
Allowable power dissipation	Pd max1	Infinitely large heat sink.	7.5	W
	Pd max2	No heat sink.	1.75	W
Operating temperature	Topr		-30 to +125	°C
Storage temperature	Tstg		-40 to +150	°C

Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage range	V _{IN}		4.5 to 28	V

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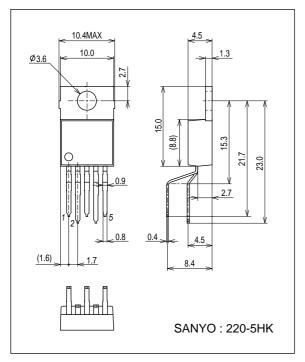
Electrical Characteristics at Ta = 25 °C, $V_O = 3.3$ V

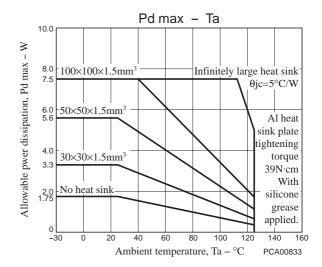
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Deremeter	Cumbal	Conditions		Ratings		
Parameter	Symbol		min	typ	max	Unit
Reference voltage	VOS	V _{IN} = 12V, I _O = 1.0A	1.193	1.23	1.267	V
Efficiency	η	V _{IN} = 15V, I _O = 1.0A, Set V _O = 5V		84		%
Switching frequency	f	V _{IN} = 15V, I _O = 1.0A	128	160	192	kHz
Switching frequency when short-circuit protection is active	fshort	V _{IN} = 15V, V _{OS} = 0V	15	30	45	kHz
Line regulation	$\Delta V_O LINE$	$V_{IN} = 8$ to 20V, $I_{O} = 1.0A$		40	100	mV
Load regulation	∆V _O LOAD	$V_{IN} = 15V, I_{O} = 0.5 \text{ to } 1.5A$		10	30	mV
Output voltage temperature coefficient	∆V _O /∆Ta	Designed target value. *		±0.5		mV/°C
Ripple attenuation factor	RREJ	f = 100 to 120Hz		45		dB
Output leak current	l _O leak	V _{IN} = 15V, SW _{OUT} = -1V			50	μΑ
Current limiter operating voltage	IS	V _{IN} = 15V	3.1			А
Operating current	IVIN	V _{IN} = 15V		5.6		mA
Standby current	ISTBY	V _{IN} = 15V, ENA = 5V			200	μΑ
ENA pin LOW voltage range	VENAL				0.6	V
ENA pin HIGH voltage range	V _{ENA} H		2.4		V _{IN}	V
Thermal shutdown operating temperature	TSD	Designed target value. *		165		°C
Thermal shutdown Hysteresis width	∆TSD	Designed target value. *		15		°C

* Design target value: No measurement made.

Package Dimensions

unit : mm (typ) 3343

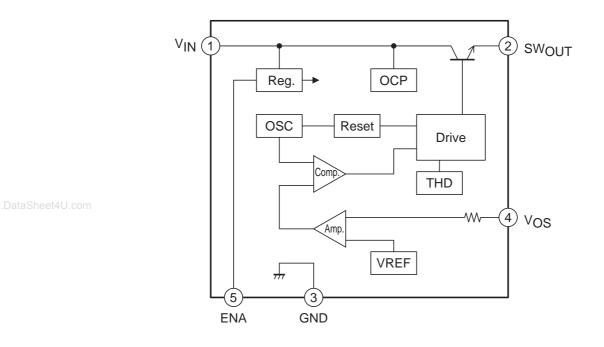




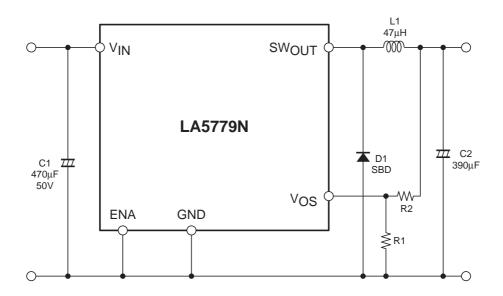
Pin Assignment

(1) $V_{\mbox{IN}}$ (2) $SW_{\mbox{OUT}}$ (3) GND (4) $V_{\mbox{OS}}$ (5) ENA

Block Diagram



Application Circuit Example



Description of Functional Settings

Calculation equation to set the output voltage

This IC controls the switching output so that the V_{OS} pin voltage becomes 1.23V (typ). The equation to set the output voltage is as follows:

$$V_O = \left(I + \frac{R2}{R1}\right) \times 1.23V(typ)$$

The VOS pin has the inrush current of $1\mu A$ (typ). Therefore, the error becomes larger when R1 and R2 resistance values are large.

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