Ordering number : ENN7192

Monolithic Digital IC

LA6581T

BTL Driver Single-Phase Full-Wave Fan Motor Driver

Overview

The LA6581T is a low-saturation BTL output linear driving motor driver for single-phase bipolar fan motors. It features quite, low power, high efficiency drive that suppresses reactive current. It is optimal for use in applications that require miniaturization and low noise, such as CPU cooling fan motors and 5 to 12 V electronic game products.

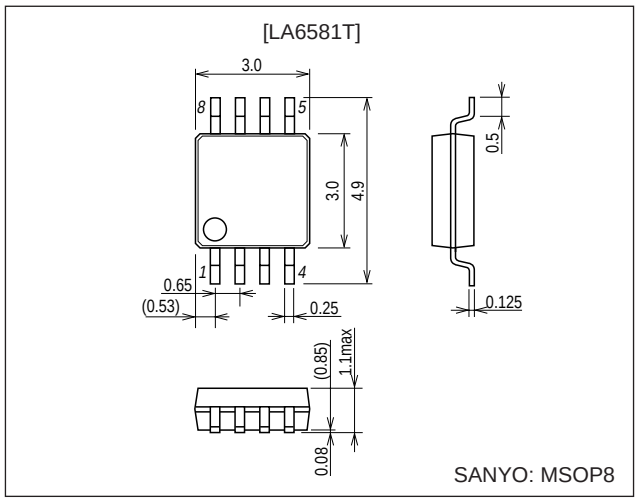
Functions and Features

- Single-phase full-wave linear drive with BTL output (gain resistance 500-284k, 55dB): Suitable for the equipment requiring silent operation, such as game equipment, CPU cooler, etc. because of its freedom from switching noise
- Low-voltage operation possible, with wide operable voltage range (3 to 16 V)
- Low saturation output (Upper + lower saturation voltage: $V_O$sat(total) = 0.3 Vtyp, I_O = 100 mA): High coil efficiency with low current drain. IC itself does not generate much heat.
- High impedance of Hall input pin
- FG output (rotation speed detection output: open collector output)
- Heat protection circuit: When the large current flows because of output short-circuit, raising the IC chip temperature above 180°C, the heat protection circuit suppresses the drive current, preventing IC burn and breakdown.
- Ultraminiature package (MSOP-8: 3.0 × 4.0 × 0.93 mm typ): Small substrate while allowing larger blades.

Package Dimensions

unit: mm

3245A-MSOP8



SANYO Electric Co.,Ltd. Semiconductor Company

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Output voltage	V_{CC} max	Mounted on a specified board*	18	V
Allowable dissipation	Pd max	V_{CC}2 pin	400	mW
Output current	I_{OUT}	Pins UL, VL, WL, UH, VH, WH	0.30	A
Output withstand voltage	V_{OUT} max		18	V
FG output withstand	V_{FG} max		18	V
FG output current	I_{FG} max		5	mA
Operating temperature	Topr		–20 to +90	°C
Storage temperature	Tstg		–55 to +150	°C

Note*: Mounted on a board (20.0 × 10.1 × 0.8 mm: Paper Phenol, wiring density 20%)

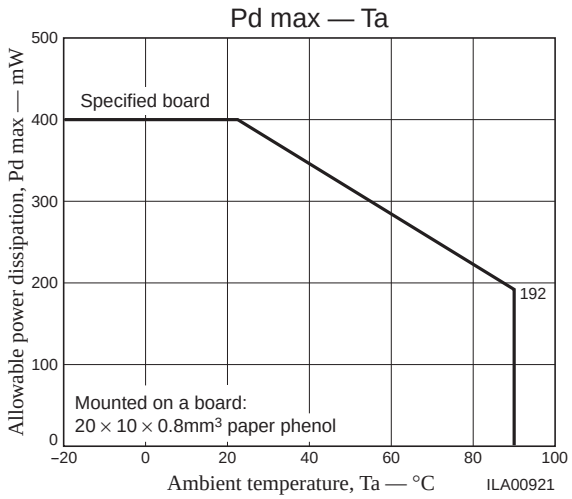
Recommended Operating Range at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V_{CC}		2.5 to 16	V
Common-phase input voltage range of Hall input	V_{ICM}		0.3 to V_{CC} – 1.5	V

Electrical Characteristics at Ta = 25°C, V_{CC} = 12.0 V, unless especially specified.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Circuit Current	I_{CC}	IN– = 5.8 V, IN+ = 6.0 V, RL = ∞		14	19	mA
OUT output low voltage	V_{OL}	I_O = 100 mA		0.1	0.2	V
OUT output high voltage	V_{OH}	I_O = 100 mA		0.1	0.2	V
Hall bias voltage	V_{HB}	RH = 360 Ω + 91 Ω	1.85	1.95	2.05	V
Hall amplifier gain	Vg		52	55	58	dB
Hall amplifier input current	V_{INR}		–10	–2	10	µA
FG output low voltage	V_{FG}	I_{FG} = 3 mA		0.2	0.3	V
FG output leakage current	I_{FGL}	V_{FG} = 7 V			30	µA
Thermal protection circuit	Th	*Design guarantee	150	180	200	°C

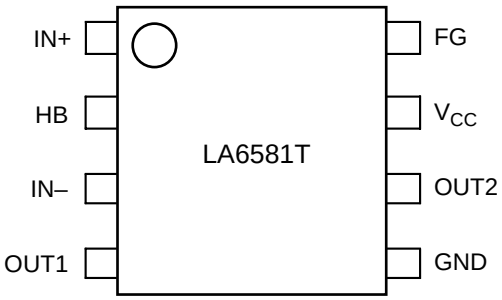
Note*: Design guarantee: Design target. Measurement with a single unit not made.



Truth Table

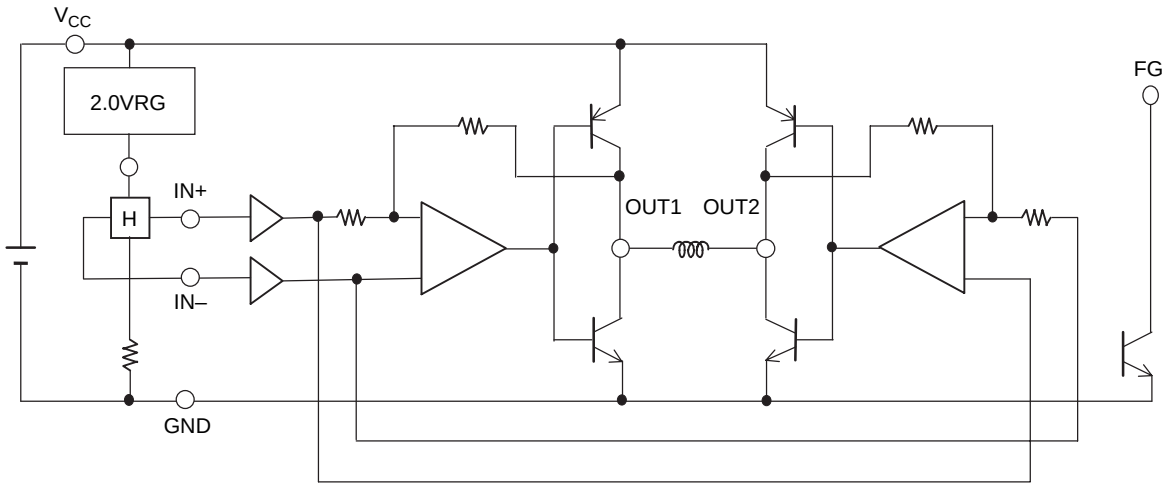
IN–	IN+	OUT1	OUT2	FG	Mode
H	L	H	L	L	During rotation
L	H	L	H	Off	
—	—	Off	Off	—	During overheat protection

Pin Assignment

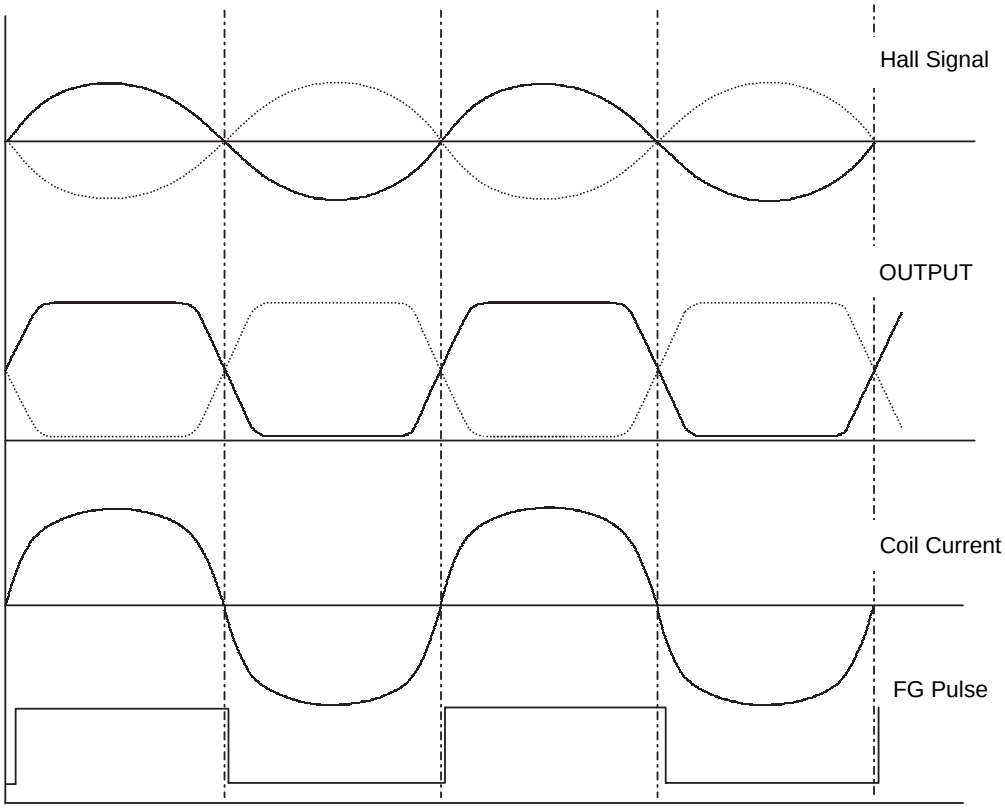


Top View

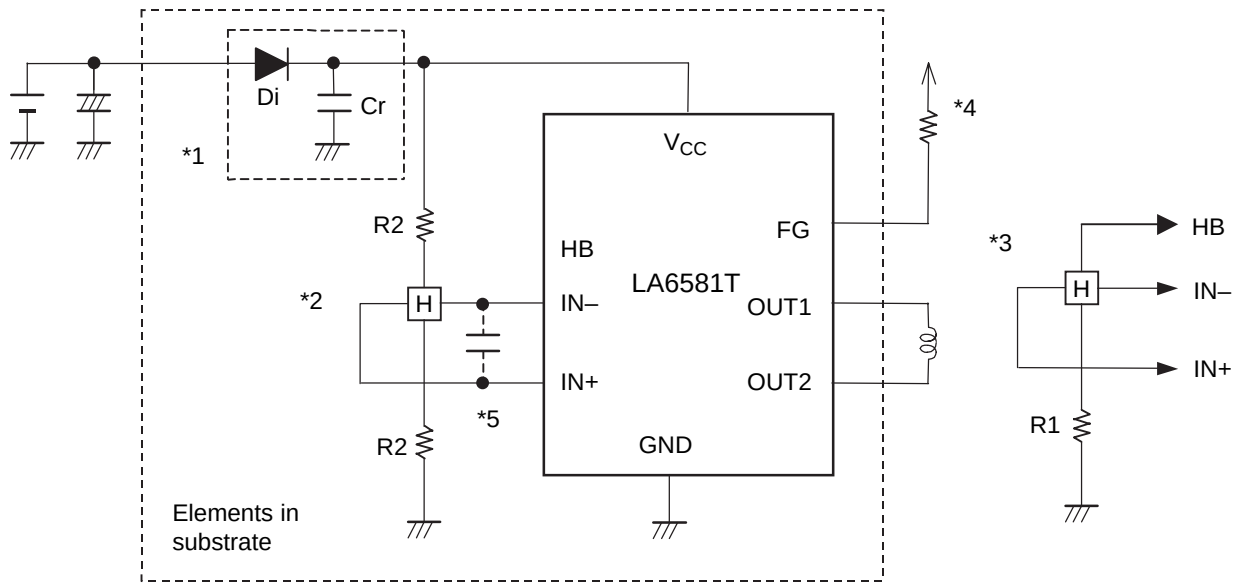
Block Diagram



Timing Chart



Sample Application Circuit



Notes 1. When Di to prevent breakdown in case of reverse connection is used, it is necessary to insert a capacitor Cr to secure the regenerative current route. Similarly, Cr is necessary to enhance the reliability when there is no capacitor near the fan power line.

2. To obtain Hall bias from V_{CC}, carry out $1/2 \times V_{CC}$ bias as shown in the figure. Linear driving is made through voltage control of the coil by amplifying the Hall output. When the Hall element output is large, the startup performance and efficiency are improved. Adjustment of the Hall element can reduce the noise further.
3. When the Hall bias is taken from the HB pin, constant-voltage bias is made with about 2.0 V. Therefore, the Hall element can provide the output satisfactory in temperature characteristics. Adjustment of the Hall output amplitude is made with R1. (When V_{CC} = 12 V, the step *2 above proves advantageous for IC heat generation.)
4. Keep this open when not used.
5. When the wiring from the Hall output to IC Hall input is long, noise may be carried through the wiring. In this case, insert the capacitor as shown in the figure.