

SANYO Semiconductors DATA SHEET

LA76600M — Monolithic Linear IC Video Signal Y/C Separator IC

Overview

The LA76600M separates a video signal into Y and C components. It includes an on-chip 2H CCD delay line and achieves a significant reduction in flicker and noise by using a 3-line Y/C separation circuit.

Functions

• For Y/C Separate (Digital Clocked CCD 3Line Comb Filter).

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply rating	V _{CC} max		6.0	V
Allowable power dissipation	Pd max	Ta ≤ 65°C	400 (*1)	mW
Operating temperature	Topr		-10 to +65	°C
Storage temperature	Tstg		-40 to +125	°C

* Mounted on a 114.3×76.1×1.6mm glass epoxy 4-layer circuit board

Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		5.0	V
Allowable operating supply voltage range	V _{CC} opg		4.8 to 5.2	V

- Any and all SANYO Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO Semiconductor representative nearest you before using any SANYO Semiconductor products described or contained herein in such applications.
- SANYO Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor products described or contained herein.

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 5.0V$

Parameter	Symbol	Input	Output	Conditions		Ratings	1	Unit
ww.DataSheet4U.co				Conditions	min	typ	max	
Analog Supply Current	ICC			Measure the current at 5PIN.	25	30	35	mA
Digital Supply Current	IDD			Measure the current at 6PIN.		30	35	mA
REG Voltage	REG		T7	Measure the output voltage.	3.8	4	4.2	V
VCO Voltage	VCO		T8	Measure the output voltage.	2.1	2.3	2.5	V
RD Voltage	RD		T12	Measure the output voltage.	9	10	11	V
PLL Voltage	PLL		T13	Measure the output voltage.	3	3.5	4	V
YIN1 Input Level	Y _{IN} 1	T14A	T14	VIN = 1Vp-p, Video signal SW2:2	0.8	1	1.2	Vp-
	· IIN ·			Measure the input level.	0.0	•		• •
YIN2 Input Level	Y _{IN} 2	T3A	T3	VIN = 1Vp-p, Video signal SW2:1	0.8	1	1.2	Vp-
-				Measure the input level.				
Chroma Input Level	CIN	T1A	T1	VIN = 0.7Vp-p, Chroma signal	0.5	0.7	0.9	Vp-
				Measure the input level.				
FSC Input Level	FSCIN	T11A	T11	VIN = 0.3Vp-p, SIN Signal	0.2	0.3	0.5	Vp-
				Measure the input level.				
Yout Level 1	YLVC	T14A	T16	VIN = 1Vp-p, Video signal SW1:2	0.89	1	1.12	Vp-
(COMB Mode)		T3A		Measure the output level.				
Yout Level 2	YLVT	T14A	T16	VIN = 1Vp-p, Video signal SW1:1	0.89	1	1.12	Vp-
(THR Mode)		T3A		Measure the output level.	-3		_	
Yout f-Character	Y _{FC} 1	T14A	T16	VIN = 1Vp-p, 5MHz, CW signal SW1:2		0	3	dB
(COMB Mode)		T3A	- - 1 -	Measure the Gain between the input and the output.				10
Yout f-Character	Y _{FC} 2	T14A	T16	VIN = 1Vp-p, 7MHz, CW signal SW1:2 Measure the Gain between the input and the output.		-20	-15	dB
(COMB Mode)	V	T3A	TAC	· · · · ·		0		-10
Yout f-Character	Y _{FT}	T14A	T16	VIN = 1Vp-p, 10MHz, CW signal SW1:1	-3	0	3	dB
(THR Mode)	Value	T3A T14A	T16	Measure the Gain between the input and the output.	200	400	600	
Y Signal Delay 1 (COMB Mode)	YDLYC	T3A	110	VIN = 1Vp-p, Video signal SW1:2 Measure the delay between the input and the output	200	400	600	ns
		134		of Y signal.				
Y Signal Delay 2	YDLYT	T14A	T16	VIN = 1Vp-p, Video signal SW1:1	0	10	50	ns
(THR Mode)	DLII	T3A		Measure the delay between the input and the output	-			
(of Y signal.				
Output Clamp Level 1	YCLPC	T14A	T16	VIN = 1Vp-p, Video signal SW1:2	1.5	2	2.5	V
(COMB Mode)	OLI O	ТЗА		Measure the clamp level of output signal.				
Output Clamp Level 2	YCLPT	T14A	T16	VIN = 1Vp-p, Video signal SW1:1	1.5	2	2.5	V
(THR Mode)		ТЗА		Measure the clamp level of output signal.				
Y Signal S/N ratio 1	YSNC	T14A	T16	VIN = 1Vp-p, Video signal SW1:2			-50	dB
(COMB Mode)		T3A		Measure the S/N ratio of Y signal.				
Y Signal S/N ratio 2	Y _{SNT}	T14A	T16	VIN = 1Vp-p, Video signal SW1:1			-50	dB
(THR Mode)		T3A		Measure the S/N ratio of Y signal.				
Clock Leak	YCLKC	T14A	T16	VIN = 1Vp-p, Video signal SW1:2			-40	dB
(COMB Mode)		T3A	ļ	Measure the clock leak.				
Clock Leak	YCLKT	T14A	T16	VIN = 1Vp-p, Video signal SW1:1			-60	dB
(THR Mode)		T3A		Measure the clock leak.				
Y_Comb Depth 1	YCOMB1	T14A	T16	VIN = 1Vp-p, Video signal SW1:2		-35	-30	dB
		T3A		Measure the comb depth at Fsc.				
Y_Comb Depth 2	YCOMB2	T14A	T16	VIN = 1Vp-p, Video signal SW1:2		-15	-10	dB
		T3A		Measure the comb depth at (227.5-59)/227.5Fsc.				
Y_Comb Depth 3	YCOMB3	T14A	T16	VIN= 1Vp-p, Video signal SW1:2		-15	-10	dB
		T3A		Measure the comb depth at (227.5+59)/227.5Fsc.				

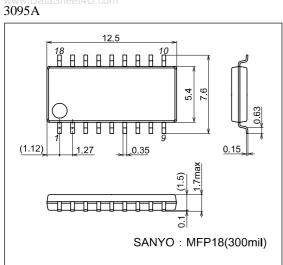
Continued on next page.

LA76600M

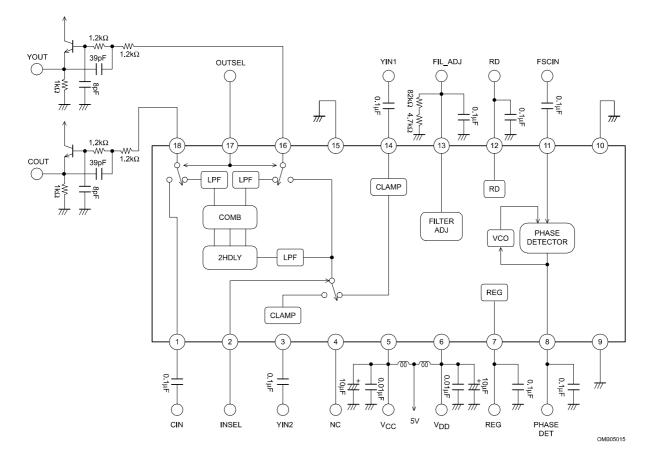
Determeter Ourshall		Increase		Que d'élèce e	Ratings			Linit
Parameter	Symbol	Input	Output	Conditions	min	typ	max	Unit
Chroma Out Level	C _{LV}	T14A	T18	VIN = 0.7Vp-p, Video signal SW1:2	0.5	0.7	0.9	Vp-p
(COMB Mode)		ТЗА		Measure the output level.				
Chroma Out Level	CLV	T1A	T18	VIN = 0.7Vp-p, Video signal SW1:1		0.7	0.9	Vp-p
(THR Mode)				Measure the output level.				
Chroma f-Character 1	C _{FC} 1	T14A		VIN = 1Vp-p, 1.5MHz, CW signal SW1:2	-6	-3	0	dB
(COMB Mode)	10	ТЗА	T18	Measure the Gain between the input and the Output.				
Chroma f-Character 2	C _{FC} 2	T14A	T18	VIN = 1Vp-p, 4.5MHz, CW signal SW1:2	-6	-3	0	dB
(COMB Mode)	10	ТЗА		Measure the Gain between the input and the Output.				
Chroma f-Character 3	C _{FC} 3	T1A	T18	VIN = 1Vp-p, 10MHz, CW signal SW1:1	-3	0	3	dB
(THR Mode)	-10-			Measure the Gain between the input and the Output.		-	-	
Chrominance Output	CDCC	T14A	T18	VIN = 1Vp-p, Video signal SW1:2	2	2.5	3	V
DC Level 1	ODCC	T3A	110	Measure the chrominance output DC level.	2	2.0	0	v
(COMB Mode)		10/1						
Chrominance Output	C = ==	T14A	T18	VIN = 1Vp-p, Video signal SW1:1	2	2.5	3	V
•	C _{DCT}		110		2	2.5	3	v
DC Level 2		T3A		Measure the chrominance output DC level.				
(THR Mode)	-		T 10					15
C Signal S/N ratio 1	CSNC	T14A	T18	VIN = 1Vp-p, Video signal SW1:2			-50	dB
(COMB Mode)		T3A		Measure the S/N ratio of C signal.				
C Signal S/N ratio 2	C _{SNT}	T14A	T18	VIN = 1Vp-p, Video signal SW1:1			-50	dB
(THR Mode)		T3A		Measure the S/N ratio of C signal.				
Clock Leak	CCLKC	T14A	T18	VIN = 1Vp-p, Video signal SW1:2			-30	dB
(COMB Mode)		T3A		Measure the clock leak.				
Clock Leak	C _{CLKT}	T14A	T18	VIN = 1Vpp, Video signal SW1:1			-60	dB
(THR Mode)		T3A		Measure the clock leak.				
C Signal Delay	CDLYC	T14A	T18	VIN = 1Vp-p, Video signal SW1:2	200	400	600	ns
(COMB mode)		T3A		Measure the delay between the input and the output				
				of C signal.				
C Signal Delay	C _{DLYT}	T1A	T18	VIN = 1Vp-p, Video chroma SW1:1	0	10	50	ns
(THR Mode)	DETT			Measure the delay between the input and the output				
				of C signal.				
C_Comb Depth 1	C _{COMB} 1	T14A	T18	VIN = 1Vp-p, Video signal SW1:2		-35	-30	dB
	COMB.	ТЗА	110	Measure the comb depth at Fsc.		00	00	üD
C_Comb Depth 2	C	T14A	T18	VIN = 1Vp-p, Video signal SW1:2		-15	-10	dB
C_COILD Depth 2	C _{COMB} 2		110			-15	-10	uБ
	<u> </u>	T3A	T 40	Measure the comb depth at (227.5-59)/227.5Fsc.		45	10	5
C_Comb Depth 3	C _{COMB} 3	T14A	T18	VIN = 1Vp-p, Video signal SW1:2		-15	-10	dB
		T3A		Measure the comb depth at (227.5+59)/227.5Fsc.		-		
Difference Of Delay	YCDLY	T14A	T16	VIN = 1Vp-p, Video signal SW1:2	-20	0	20	ns
Between Y and C		T3A	T18	Measure the difference of delay between the Y				
				output and C output.				-
INPUTSEL_L	INSELL	T2	T2	YIN1 ON, measure the SW voltage.		0	1	V
(YIN1)								
INPUTSEL_H	INSELH	T2	T2	YIN2 ON, measure the SW voltage.	2.5	5		V
(YIN2)								
OUTPUTSEL_H	OUTSELH	T17	T17	COMB Mode, measure the SW voltage.		0	1	V
(COMB Mode)								
OUTPUTSEL_L	OUTSELL	T17	T17	COMB Mode, measure the SW voltage.	2.5	5		V
(THR Mode)	JLL			č				
Cross talk between	INCRS	T14A	T16	VIN = 1Vp-p, Video signal SW2:2			-50	dB
Different input 1			T18	Measure the cross talk between different input.				
Cross talk between	INcre	ТЗА	T16			-50	dB	
Different input 2	INCRS	134	T18	Measure the cross talk between different input.			-30	uB

Package Dimensions





Block Diagram



Pin Function Pin No. DC Pin name Input/Output form Signal waveform voltage 18PIN CIN 2.5V 1 CHROMA-SIGNAL 80 §§ 200 0.7Vp-p 25V 500u 0 2 INSEL GND: YIN2 2PIN V_{CC}: YIN1 Š₹ 5k --W 5k -₩⁄ Š₹ 3 YIN2 2.5V 3PIN VIDEO-SIGNAL 2.7V ₿Å 50µ K 1Vp-p 5μ

5

6

7

BIP-VCC

CCD-V_{CC}

REG

5V

5V

4V

DC

DC

DC

₹

 $\overline{}$

Continued on next page.

(unit: Ω)

1PIN

Ο

AS 2 BIP GND

H BIP GND

> BIP GND

 \overline{m}

Y

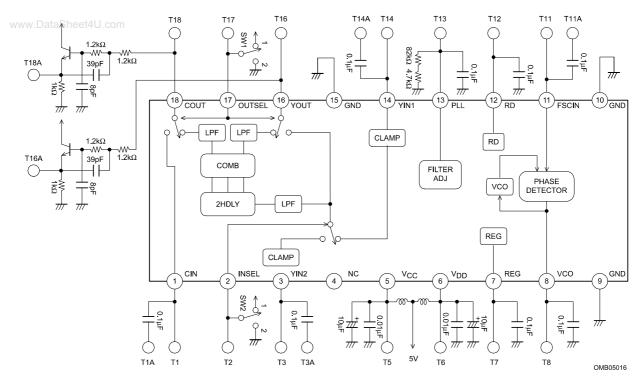
Continued f	rom preceding pag	re.		(unit: Ω)
Pin No.	Pin name	DC voltage	Signal waveform	Input/Output form
8 ww.Dat		2.3V	DC	BPIN BPIN BPIN BPIN CCD M CCD M GND
9	CCD-GND	GND		
10	CCD-GND	GND		
11	FSC_IN	0.8V	SIN-WAVE:3.58MHz	11PIN 11PIN CCD M GND
12	RD	10V	DC	RD_GENE
13	AFC	3.5V	DC	13PIN 2.7V 2.7V 200 200 U BIP GND

Continued on next page.

LA76600M

Continued fr	om preceding page.			(unit: Ω)
Pin No.	Pin name	DC voltage	Signal waveform	Input/Output form
14 www.Data	Sheet4U.com		VIDEO-SIGNAL	2.7V 00 00 00 00 00 00 00 00 00 0
15	BIP-GND	GND		
16	YOUT	2.5V	Y-SIGNAL	16PIN 16PIN 16PIN 16PIN 10
17	OUTSEL		GND: COMB V _{CC} : THROUGH	17PIN 5k 5k BIP 7001 77 BIP 6ND
18	COUT	2.5V	CHROMA-SIGNAL	18PIN 1PIN 18PIN 2200 18PIN 200 200 1005 200 1005 200 1005 200 1005 200 1005 200 1005 1

Test Circuit



- Specifications of any and all SANYO Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Semiconductor Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Semiconductor Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of August, 2005. Specifications and information herein are subject to change without notice.