

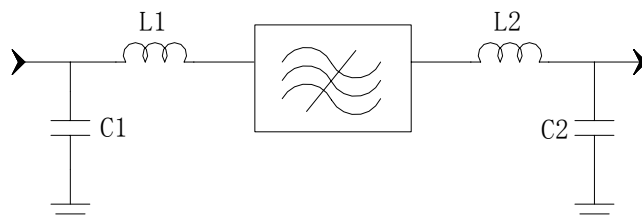
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	69.88	70	70.12
Insertion Loss	dB	-	14.7	16
1 dB Bandwidth	MHz	1.3	1.52	-
3 dB Bandwidth	MHz	2	2.05	-
40 dB Bandwidth	MHz	-	3.95	4.25
Passband Variation	dB	-	0.3	1
Absolute Delay	usec	-	1.51	-
Group Delay Variation($f_0 \pm 0.7\text{MHz}$)	nsec	-	130	340
Phase Linearity($f_0 \pm 0.7\text{MHz}$)	deg	-	14	-
Material Temperature coefficient	KHz/°C	-1.26		
Ambient Temperature	°C	25		
Package Size	SMP-53C (13.3x6.5mm Nominal Footprint)			

Notes:

- All specifications are based on the test circuit shown
- In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
- Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
- This is the optimum impedance in order to achieve the performance show

Matching Configuration




$$L1=L2=180\text{nH}$$

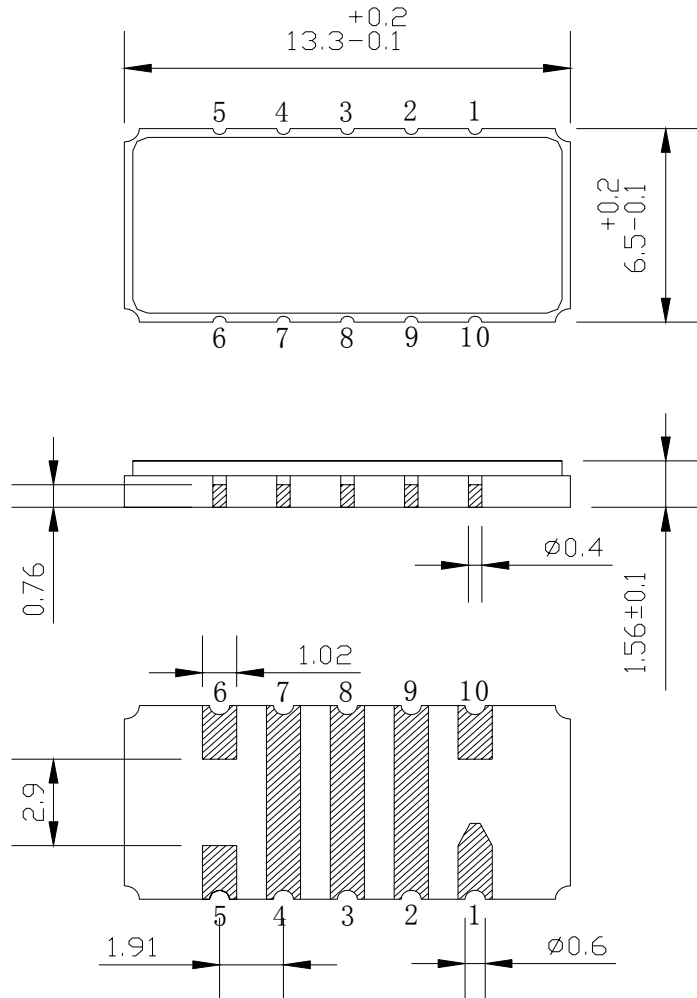
$$C1=100\text{pF} \quad C2=33\text{pF}$$

Source/Load Impedance=50 ohm


Notes - Component values may change depending
on board layout.

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Package Dimension

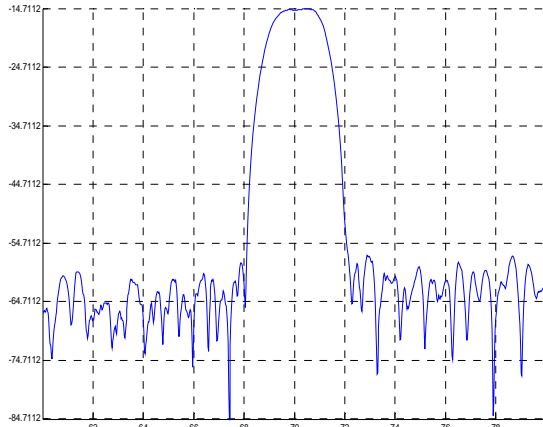


Input:10
Output:5

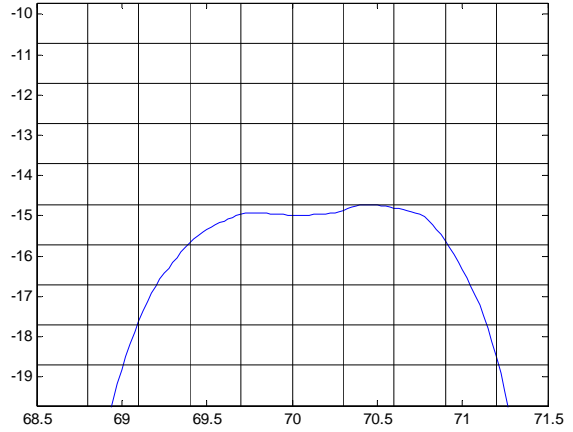
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Typical Performance

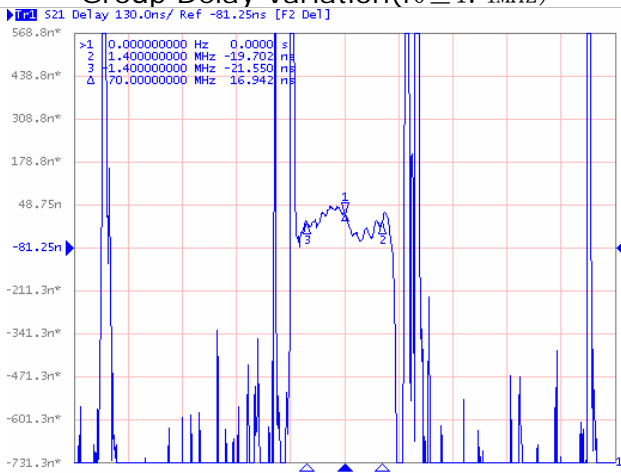
Frequency Respond



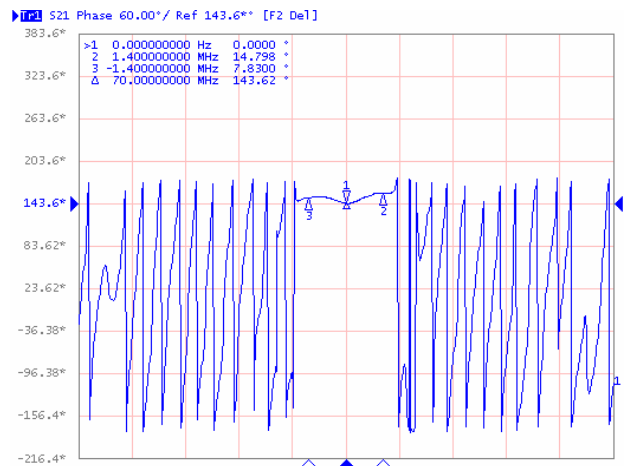
Passband Respond



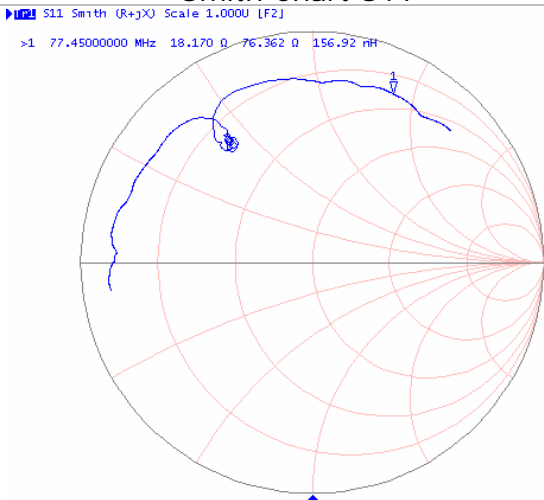
Group Delay Variation($f_0 \pm 1.4\text{MHz}$)



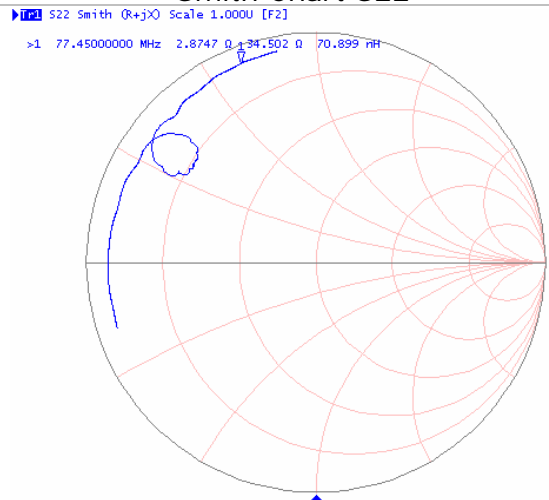
Phase Linearity($f_0 \pm 1.4\text{MHz}$)



Smith Chart S11



Smith Chart S22



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