

SANYO Semiconductors

DATA SHEET



Monolithic Digital IC For Fan Motor **Two-Phase Half-Wave Driver**

Overview

The LB11669M is a two-phase uni-polar brushless motor driver for fan motor.

Functions

- Two-phase half-wave drive.
- RD (lock detection) outputs incorporated.
- FG (rotation detection) outputs incorporated.
- Thermal shutdown circuit incorporated.
- Lock protection and automatic return function incorporated.
- Output protection zener diode incorporated.
- Hall input amplifier incorporated.

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum inflow current	I _{IN} max		100	mA
Output current	I _{OUT} ave		400	mA
	I _{OUT} peak	Only when starting and lock protection is activated	800	mA
Output withstand voltage	V _{OUT} max		Internal	V
FG/RD output current	I _{FG/RD} max		10	mA
FG/RD output withstand voltage	V _{FG/RD} max		30	V
Allowable power dissipation	Pd max	Mounted on a board *	800	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

* Specified board : 114.3mm × 76.1mm × 1.6mm, glass epoxy board.

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LB11669M

Recommended Operating Conditions at $Ta = 25^{\circ}C$

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Parameter	Symbol	Conditions	Ratings	Unit	
Inflow current range	I _{IN}		5 to 25	mA	
Common-mode input voltage range	VCOM		0.2 to V _{IN} -2.3	V	

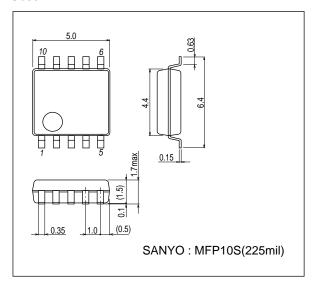
Electrical Characteristics at Ta = 25°C, V_{CC} =24V, R1=1k Ω , unless otherwise specified.

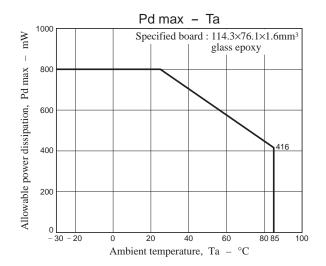
Decemeter	Symbol	Quanditiana		Ratings			
Parameter		Conditions	min	typ	max	Unit	
V _{IN} voltage	VIN	I _{IN} = 6mA	5.95	6.3	6.65	V	
CT capacitor charging current	I _{CT} 1	CT = 0.5V	1.8	2.55	3.4	μA	
CT capacitor dis-charging current	I _{CT} 2	CT = 6.0V	0.15	0.23	0.31	μA	
capacitor charging / dis-charging current ratio	R _{CT}	$R_{CT} = I_{CT}1 / I_{CT}2$	10.5	11	14.5		
CT charging voltage	V _{CT} H	V _{CT} / V _{IN}	74	79	84	%	
CT dis-charging voltage	VCTL	V _{CT} / V _{IN}	41	46	51	%	
Output limit withstand voltage	V _O LM	I _O = 100mA	49	52	55	V	
Output saturation voltage	V _O L1	I _O = 200mA		0.85	1.1	V	
Hall input sensitivity	V _{HN}	Including offset and hysteresis		±15	±30	mV	
FG/RD output saturation voltage	V _{FG/RD}	I _{FG/RD} = 5mA		0.15	0.3	V	
FG/RD output leak current	IFG ^L /RD ^L	V _{FG/RD} = 14V		0.1	10	μA	
Thermal protection function operating temperature	TSD	Design target value *		180		°C	

* Design target value and is not measured.

Package Dimensions

unit : mm (typ) 3086B

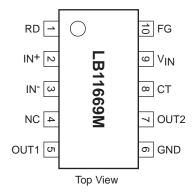




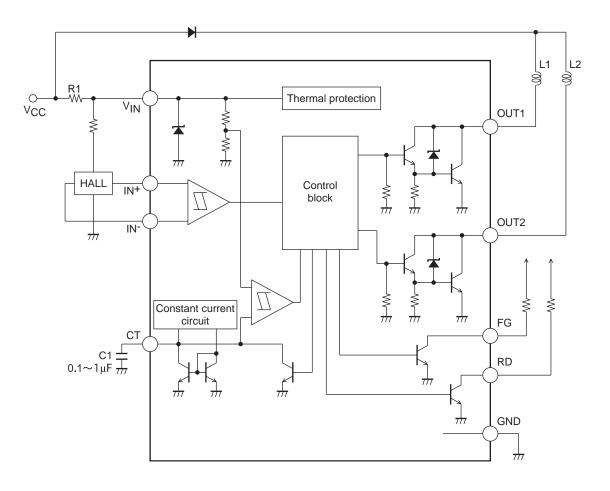
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Pin Assignment

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Block Diagram

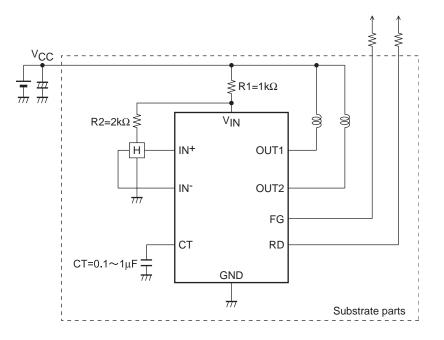


Truth table

IN-	IN+	СТ	OUT1	OUT2	FG	RD	Mode
н	L	L	Н	L	L	L	Datation
L	н		L	Н	OFF	L	Rotation
н	L	Н	OFF	OFF	L	OFF	
L	н		OFF	OFF	OFF	OFF	Lock protection

Application Circuit Example 24V power supply

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Notice

- Take care not to cause interference due to wiring of IN- and OUT1.
- Wiring need to be short to prevent carrying of the noise. If the noise is carried, insert a capacitor between IN⁺ and IN⁻.
- In application of connecting the CT pin to GND, lock protection and restart function are not effective.
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