

SANYO Semiconductors DATA SHEET

Monolithic Digital IC

LB11699H — For CD-ROM Drives Spindle Motor Driver IC

Overview

The LB11699H is a spindle motor driver IC for CD-ROM drives.

Features

• Three-phase brushless motor driver

Functions

- Current linear drive
- Voltage controlled amplifier
- The use of high side current detection means that there is no loss (or voltage drop) due to the current detection resistor.
- Built-in short-circuit braking circuit
- Built-in reverse rotation prevention circuit
- Hall sensor FG output
- Built-in start/stop function
- Built-in current limiter circuit (adjustable)
- Built-in Hall sensor power supply
- Built-in thermal shutdown circuit
- Supports 3.3V DSPs
- Maximum current rating: 2.0A

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Specifications

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Maximum Ratings at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|--------------------------|-----------------------|------------------------------------|-------------------|------|
| Supply voltage1 | V _{CC} 1 max | | 7.0 | ٧ |
| Supply voltage 2 | V _{CC} 2 max | | 14.4 | V |
| Output apply voltage | V _O max | | 14.4 | ٧ |
| Input apply voltage | V _I max | | V _{CC} 1 | V |
| Output current | I _O max | | 2.0 | Α |
| Allowable internal power | Pd max | Independent IC | 0.8 | W |
| dissipation | | When mounted on a circuit board *1 | 1.9 | |
| Operating temperature | Topr | | -20 to +75 | °C |
| Storage temperature | Tstg | | -55 to +150 | °C |

^{*1} Specified circuit board : 114.3 \times 76.1 \times 1.6mm³, glass epoxy.

Operating Conditions at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------|------------------------------|---------------------|-----------|------|
| Supply voltage 1 | V _{CC} 1 | | 4 to 6 | V |
| Supply voltage 2 | V _{CC} ² | ≥ V _{CC} 1 | 4 to 13.6 | V |

Application Example at $Ta = 25^{\circ}C$

(1) 12V model

| Power supply pin | Conditions | Ratings | Unit |
|------------------------------|---------------------|-----------|------|
| V _{CC} 1 | Regulated voltage | 4 to 6 | V |
| V _{CC} ² | Unregulated voltage | 4 to 13.6 | V |

Electrical Characteristics at Ta = 25°C, V_{CC}1 = 5.0V, V_{CC}2 = 12V (unless otherwise specified)

| Parameter | Symbol Conditions | Conditions | Ratings | | | 1.1 |
|---------------------------------|---------------------|------------------------|---------|-----|---------------------|-------|
| Parameter | Symbol | Conditions | min | typ | max | Unit |
| Supply Current | | | | | | |
| Supply current 1 | I _{CC} 1 | VC = VCREF | | 6.0 | 9.0 | mA |
| Supply current 2 | I _{CC} 2 | VC = VCREF | | | 1.0 | mA |
| Output stop current 1 | I _{CC} 10Q | VS/S = 0V | | | 200 | μΑ |
| Output stop current 2 | I _{CC} 2OQ | VS/S = 0V | | | 350 | μΑ |
| Output Block | | | | | | |
| High-side saturation voltage 1 | VOU1 | I _O = -0.5A | | 1.0 | 1.5 | V |
| Low-side saturation voltage 1 | VOD1 | I _O = 0.5A | | 0.3 | 0.5 | V |
| High-side saturation voltage 2 | VOU2 | I _O = -1.5A | | 1.1 | 1.8 | V |
| Low-side saturation voltage 2 | VOD2 | I _O = 1.5A | | 0.6 | 1.2 | V |
| Hall Sensor Amplifier Block | | | | • | | |
| Common-mode input voltage range | VHCOM | | 1.2 | | V _{CC} 1 - | V |
| Input bias current | VHIB | | | 1 | | μА |
| Hall sensor minimum input level | VHIN | | 60 | | | mVp-p |
| S/S Pin | | | | | | |
| High-level voltage | VS/SH | | 2.0 | | V _{CC} 1 | V |
| Low-level voltage | VS/SL | | | | 0.7 | V |
| Input current | IS/SI | VS/S = 5V | | | 200 | μА |
| Leakage current | IS/SL | VS/S = 0V | -30 | | | μА |

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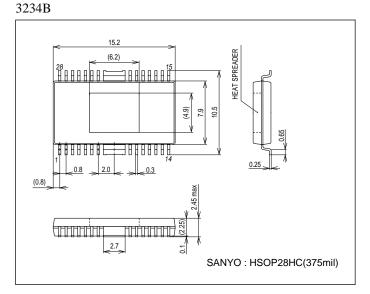
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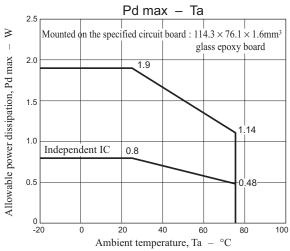
| D | 0 | Q - Ittle | | Ratings | v.Datasne | 3014U.CO |
|---|-------------------|--------------------------------------|----------|---------|-----------|---------------|
| Parameter | Symbol Conditions | | min | typ | max | Unit |
| Control Block | | | <u> </u> | | | |
| VC pin input current | IVC | VC = VCREF = 1.65V | -1 | | | μΑ |
| VCREF pin input current | IVCREF | VC = VCRE F= 1.65V | -1 | | | μΑ |
| Voltage gain | GVCO | ΔVRF/ΔVC | | 0.4 | | Times/ deg |
| Rising voltage | VCTH | VCREF = 1.65V | 1.5 | | 1.8 | ٧ |
| Rising voltage width | ΔVCTH | VCREF = 1.65V | 50 | | 150 | mV |
| Hall Sensor Power Supply | | | | | | |
| Hall sensor supply voltage | VH | I _H = 5mA | | 0.8 | | ٧ |
| Allowable current | IH | | 20 | | | mA |
| Thermal Shutdown Circuit | | | <u> </u> | | | |
| Thermal shutdown operating temperature | TTSD | Design target value* | 150 | 180 | 210 | °C |
| Thermal shutdown temperature hysteresis | ΔTTSD | Design target value* | | 15 | | °C |
| Short-Circuit Braking | | | <u> </u> | | | |
| BRAKE pin high-level voltage | VBRH | | 2.5 | | 5 | V |
| BRAKE pin low-level voltage | VBRL | | 0 | | 1 | V |
| Single Hall Sensor/Three Hall Se | nsor FG Switch | ing | | | | |
| FGSEL pin high-level voltage | VFSH | | 2.5 | | 5 | V |
| FGSEL pin low-level voltage | VFSL | | 0 | | 1 | V |
| Current Limiter Setting | | | | • | | |
| Current limiter set voltage | VCL1 | REF = 0.39Ω , VLMC = VREF 0.4 | | | V | |
| VLMC pin input voltage range | VLMCC | Design target value* 0 | | 1.3 | V | |
| Reference voltage | VREF | IVREF =10μA | | 1.25 | | V |

Note: The Hall comparator output goes to the high level when the S/S pin is in the off state (standby mode).

Package Dimensions

unit : mm (typ)



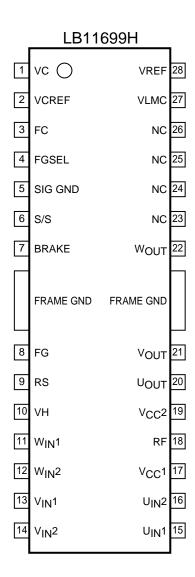


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 $[\]ensuremath{^{\star}}$ The design specification items are design guarantees and are not measured.

Pin Assignment

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Top view

Truth Table

| | Source → Sink | | Input | | Control voltage | |
|---|---|----------|----------|--------|-----------------|--|
| | Source → Sink | U | V | W | Control voltage | |
| 1 | W phase → V phase | I II ale | LUmb | 1 | High | |
| 1 | $V \text{ phase} \rightarrow W \text{ phase}$ | High | High | Low | Low | |
| | W phase \rightarrow U phase | 115.1 | | | High | |
| 2 | U phase \rightarrow W phase | Hign | High Low | Low | Low | |
| | $V \text{ phase} \rightarrow W \text{ phase}$ | 1 | 1 | High | High | |
| 3 | W phase \rightarrow V phase | Low | Low | | Low | |
| 4 | U phase → V phase | 1 | LUmb | Low | High | |
| 4 | V phase → U phase | Low | High | | Low | |
| _ | V phase → U phase | I Cala | 1 | I Code | High | |
| 5 | U phase → V phase | High | Low | High | Low | |
| | U phase \rightarrow W phase | Low | Lliah | Lliada | High | |
| 6 | W phase \rightarrow U phase | Low | High | High | Low | |

Input high: Input 1 is at least 0.2V higher than input 2 for a given phase.

Input low: Input 2 is at least 0.2V higher than input 1 for a given phase.

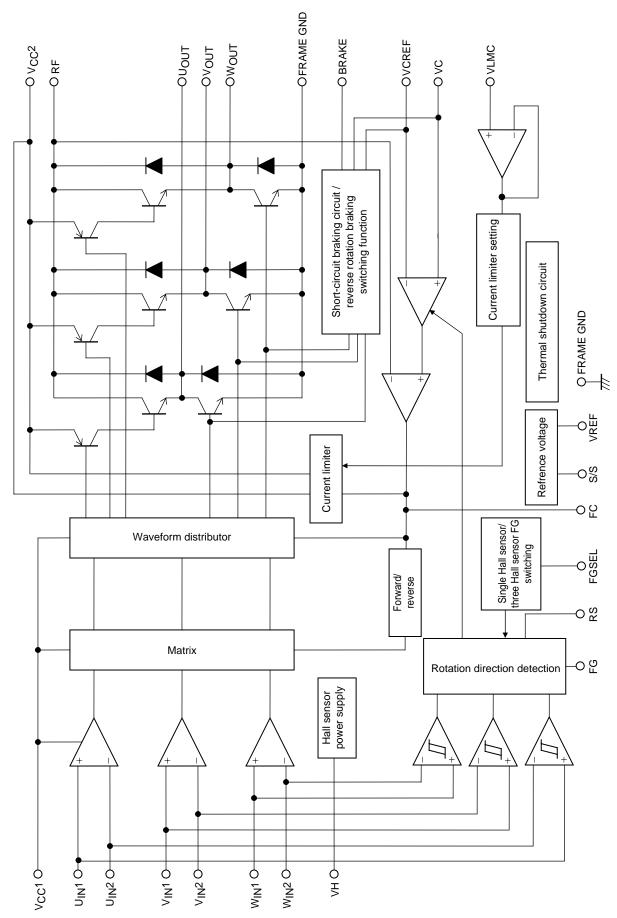
Brake Operation Truth Table

| BRAKE pin | Operation at VC < VCREF | |
|-------------|-------------------------|--|
| High | Short-circuit braking | |
| Low or open | Reverse torque braking | |

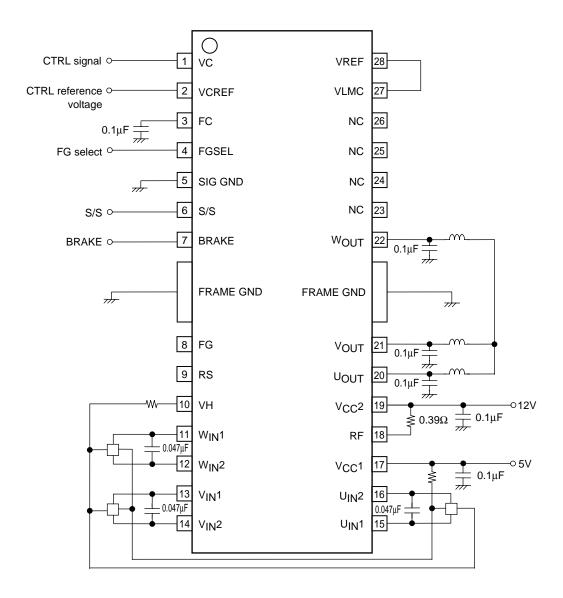
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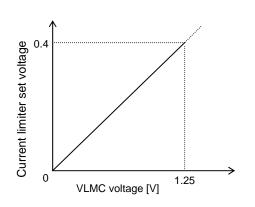


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The values of the capacitors between power supply and ground, between output and ground, and between the Hall sensor inputs vary depending on the motor used. In particular, the Hall sensor input capacitors may not be required for some motors.

<Information> Current Limiter Setting



The LB11699H current limiter set voltage is the VLMC pin voltage, and varies as shown in the figure to the left. When the VLMC pin voltage is 0V, the current limiter set voltage will be 0V and no output current will flow.

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| Pin Fu | nctions | | | www.DataSheet4U.com |
|---------|------------------------------|------------------------------------|---|--|
| Pin No. | Pin | Pin voltage | Description | Equivalent Circuit |
| 19 | V _{CC} ² | 4V to 13.6V | Power supply that provides the source side predriver voltage. Power supply that provides the constant current control amplifier voltage. | |
| 17 | V _{CC} 1 | 4V to 6V | Power supply that provides all voltages other than those for the output transistors, the source side predrivers, and the low-current control amplifier. | |
| 9 | RS | | Reverse rotation detection Outputs a high level for forward rotation. Outputs a low level for reverse rotation. | 100μA 10kΩ 8 9 |
| 8 | FG | | One or three Hall sensor Schmitt trigger comparator synthesized output | |
| 15 | U _{IN} 1 | 1.2V to V _{CC} 1-1V | U phase Hall sensor input and reverse rotation detection U phase Schmitt trigger comparator input. | |
| 16 | U _{IN} 2 | | The logical high state indicates the state where $U_{IN}1 > U_{IN}2$. | V _{CC} 1 |
| 13 | V _{IN} 1 | | V phase Hall sensor input and reverse rotation detection V phase Schmitt trigger comparator input. | 25μAΨ (16) (3) 200Ω (12) |
| 14 | V _{IN} 2 | | The logical high state indicates the state where $V_{\mbox{IN}1} > V_{\mbox{IN}2}$. | 25µA 25µA |
| 11 | W _{IN} 1 | | W phase Hall sensor input and reverse rotation detection W phase Schmitt trigger comparator input. | |
| 12 | W _{IN} 2 | | The logical high state indicates the state where $W_{IN}1>W_{IN}2. \label{eq:win1}$ | |
| 10 | VH | | Provides the Hall sensor low side device voltage. | 75μA ψ 10 30kΩ ≱ 2kΩ ≱ 110 |
| 6 | S/S | 0V to V _{CC} 1 | All internal circuits are stopped by setting this pin to 0.7V or lower or by setting it open. Set this pin to 2.0V or higher when driving the motor. | V _{CC} 1 6 75kΩ 50kΩ 75kΩ 75kΩ |
| 5 | SIG GND | | Ground for all systems except the output | |
| | | 1 | system. | |

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|----------------|------------------|------------------------------|---|--|
| Pin No. | Pin | Pin voltage | Description | Equivalent Circuit |
| 3 | FC | | Control loop frequency characteristics correction Current control system closed loop oscillations can be prevented by connecting a capacitor between this pin and ground. | Vcc1 \$\bigs_{2kΩ}\$ \$\frac{1}{2kΩ}\$ \$\frac{1} |
| 2 | VCREF | 0V | Control system reference voltage input | |
| | | to V _{CC} 1-1.5V | The control system start voltage is determined by this voltage. | 15μA(Ψ) 25μA(Ψ) (Ψ) (Ψ)15μA |
| 1 | VC | oV to VGC1 | Speed control voltage input This is a voltage controlled system in which: The motor turns in the forward direction when VC > VCREF, and The motor turns decelerates when VC < VCREF. (Since the LB11699H includes a reverse rotation prevention circuit, the motor will never turn in the reverse direction.) | 100μA 25μA 200Ω 200Ω 200Ω 200Ω 200Ω 200Ω 200Ω 200 |
| 22 | W _{OUT} | | W phase output | |
| | FRAME GND | | Output transistor ground | Vcc2 |
| 21 | Vout | | V phase output | |
| 20 | UOUT | | U phase output | 3.90 |
| 18 | RF | | High side output transistor collector (common to all three phases) Connect a resistor between the RF pin and V _{CC} 3 for current detection. The LB11699H detects this voltage to operate the constant current control and current limiter functions. | 3.9Ω (20) (21) (22) FRAME GND |
| 27 | VLMC | 0 to 1.3V | This pin determines the current limiter set voltage. The current limiter set voltage can be changed by applying a voltage to this pin. | V _{CC} 1 6μA Φ 6μA Φ 6μA Φ 200Ω 37 |
| 28 | VREF | | Reference voltage (1.25V typical) output | Continued on next page. |

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| Continued from | m preceding pag | e. | | |
|----------------|-----------------|------------------|--|---|
| Pin No. | Pin | Pin voltage | Description | Equivalent Circuit |
| 7 | BRAKE | ov to Vcc1 | Short-circuit braking pin BRAKE : High → brake Low → drive Open | 75kΩ T |
| 4 | FGSEL | ov to Vcc1 | Single Hall sensor/three Hall sensor FG switching pin FGSEL : High → three Hall senso Low → single Hall sensor Open | 75kΩ 4 *** 50kΩ 4 |

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