

SANYO Semiconductors DATA SHEET

LB1731J — High-Voltage, Current-Sink Output Driver

Overview

The LB1731J is a 4-channel high-voltage current sink output driver. Inputs are active-low CMOS/TTL logic-level, and outputs are high-voltage open-collector NPN Darlington pairs.

Each driver in the LB1731J sinks up to 1.5A and withstands collector voltages of up to 85V.

The LB1731J is available in a 16-pin DIP package.

Features

- For independent high-voltage high-current drivers.
- Output clamp diodes.
- Input protection diodes.
- 5V CMOS- and TTL-compatible logic-level inputs.

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{DD} max		7.0	V
	V _{CC} max		82	V
Applied output voltage	V _O max		85	V
Applied input voltage	V _{IN} max	$V_{IN} \ge GND$	V _{DD} -7.0 to V _{DD} +10.0	V
Output current	I _O max		1.5	Α
Clamp diode forward current	IFS		1.5	Α
Allowable power dissipation	Pd max	Independent IC	1.9	W
Operating temperature	Topr		-40 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

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Allowable Operating Ranges at Ta = 25°C

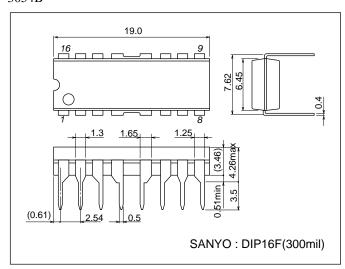
Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage range	V_{DD}		3.5 to 7.0	V
Input ON-level voltage	V _{IN} on	$V_{IN} \ge GND$, $I_O = 1.0A$	V _{DD} -7.0 to V _{DD} -2.6	V
Input OFF-level voltage	V _{IN} off	I _O ≤ 30μA	V _{DD} -0.3 to V _{DD} +10.0	V

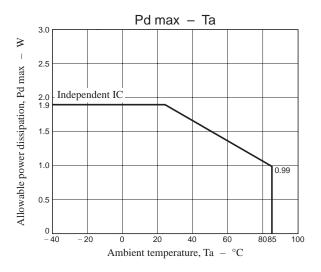
Electrical Characteristics at Ta = 25°C, $V_{DD} = 5.0$ V

Parameter	Symbol	Conditions	Ratings			1.1
			min	typ	max	Unit
Output saturation voltage	V _O sat1	$V_{IN} = V_{DD}$ -5.0V, $I_{O} = 0.5A$			1.2	V
	V _O sat2	$V_{IN} = V_{DD}$ -5.0V, $I_{O} = 1.0A$			1.5	V
	V _O sat3	$V_{IN} = V_{DD}$ -5.0V, $I_{O} = 1.5A$			2.0	V
Output sustain voltage	V _O (sus)	I _O = 100mA	85			V
Input current	I _{IN}	$V_{DD} = 7.0V, V_{IN} = V_{DD}-7.0V$			0.5	mA
Clamp diode forward voltage	V _{FS}	I _{FS} = 1.5A			3.0	V
Clamp diode reverse current	I _{RS}	V _{CC} = 62V, V _O = 0V			30	μΑ

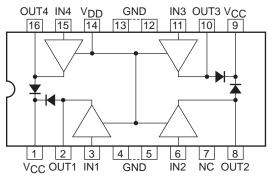
Package Dimensions

unit : mm (typ) 3054B





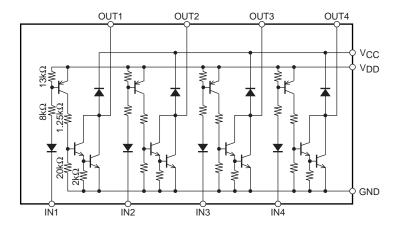
Pin Assignment and Block Diagram



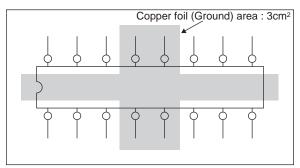
Top view

Pins 1 and 9 are shorted internally.

Equivalent Circuit



Recommended Circuit Board Layout



Circuit board (80×60mm)

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