LB1930MC

Monolithic Digital IC Low-Voltage, Low-Saturation Bidirectional Motor Driver



Overview

The LB1930MC is single-channel forward/reverse DC brush motor driver. This device is optimal for CD, DVD and Blue Ray Disk player loading motors. And it is possible to use it for others as a general-purpose product.

Features

- The low saturation voltage reduces IC internal heating and allows a high voltage to be applied to the motor. Thus this device can be used even in environments with a high operating ambient temperature.
 - Output saturation voltage: Vsat1 = 0.25V typical (I_O = 0.2A)
 - (High side + low side): Vsat2 = 0.55V typical (I_O = 0.5A)
 - Operating temperature range: Ta = -30 to $+85^{\circ}C$
- The LB1930MC features the wide operating voltage range of 2.2 to 10.8V and the low standby current drain of 0.1μ A, and therefore can easily be used in battery operated systems.
- To minimize through currents, the LB1930MC internal logic passes through an internal standby state when switched by the input signals between forward/reverse and brake, or between forward and reverse.
- There are no constraints on the relationship between the input voltage and the supply voltage. For example, the LB1930MC can be used with $V_{CC} = 3V$, and $V_{IN} = 5V$.
- If the IC chip exceeds 180°C due to an output short causing a large current flow, the built-in thermal protection circuit suppresses the drive current to prevent fires or destruction of the IC.

Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

	0			
Parameter Sy	mbol	Conditions	Ratings	Unit
Supply voltage	V _{CC} max		11	V
Output current	I _{OUT} max		1000	mA
Output voltage handling	V _{OUT} max		V _{CC} + V _{SF}	V
Applied input voltage	I _H max		10.5	V
Allowable power dissipation	Pd max	Mounted on a specified board *	750	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

* Specified board: 114.3mm \times 76.1mm \times 1.6mm, glass epoxy board.

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

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Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC}		2.2 to 10.8	V
High-level input voltage	VIH		2.0 to 10	V
Low-level input voltage	VIL		-0.3 to +0.3	V

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 3V$

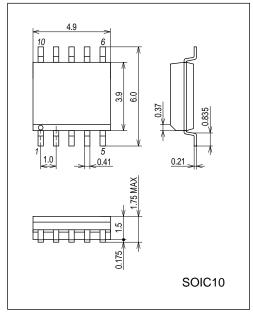
Parameter Sy	mhol	Conditions	Ratings			Linit
	IOdm	mbol Conditions	min ty	р	max	Unit
Current drain	I _{CC} 1 Standby	mode		0.1	5	μΑ
	I _{CC²}	Forward or reverse drive operation		15	21	mA
	I _{CC} 3 Brakin	g		22	31	mA
Output saturation voltage	V _O (sat)1	Forward or reverse drive: High side + low side, $I_{O} = 200 \text{mA}$		0.25 0	.35	V
	V _O (sat)2	Forward or reverse drive: High side + low side, $I_{O} = 500 \text{mA}$		0.55 0	.75	V
	V _O (sat)3	Forward or reverse drive: High side only, $I_{O} = 200 \text{mA}$		0.15 0	.25	V
Input current	IIN	V _{IN} = 5V		70	95	μΑ
Thermal detection operating temperature	THD	Design guarantee value*	150	180	200	°C
Spark Killer diode						
Forward voltage	V _{SF}	I _O = 200mA		0.9	1.7	V
Reverse current	I _{RS}	V _{OUT} = 10V		0.1	5	μΑ

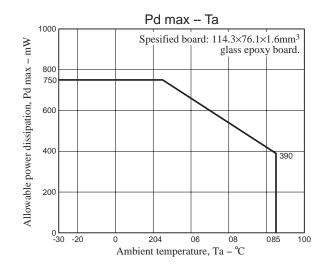
* Design guarantee value, Do not measurement.

Package Dimensions

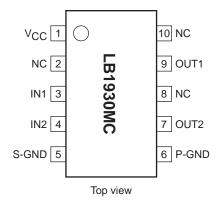
unit : mm (typ)

3426A

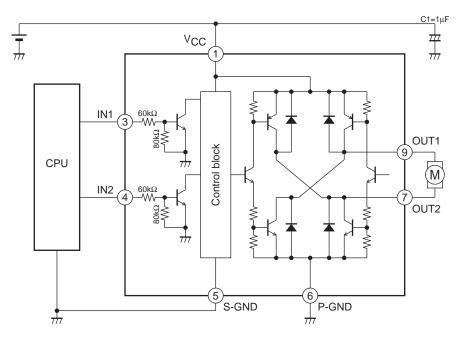




Pin Assignment

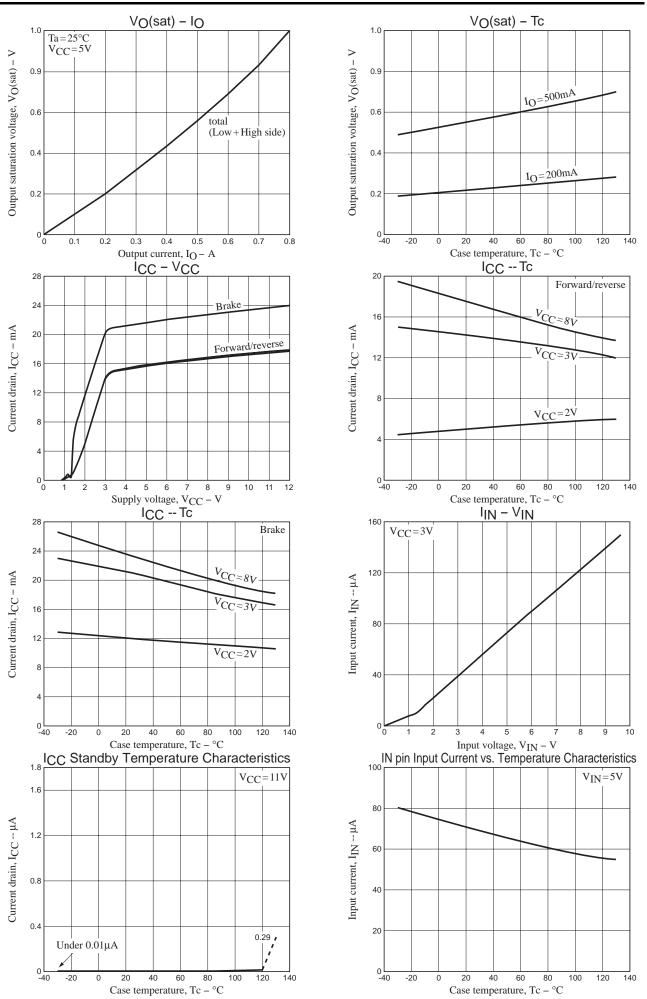


Block Diagram and Application Circuit Example



Truth Table

IN1 IN2	OUT1	OUT2	Mode		
LL	OFF	OFF	Standby		
HL	ΗL		Forward		
LH	LH		Reverse		
нн	Н	н	Brake		



Usage Notes

Oscillation may occur in the V_{CC} and P-GND lines, since these lines carry a wide range of currents. The following may help if this is a problem.

- (1) Lower the inductance of the wiring by making lines wider and shorter.
- (2) Insert capacitors with good frequency characteristics close to the IC.
- (3) Consider adopting the following methods if the CPU and this IC are mounted on different printed circuit boards that could easily have different ground potentials.
 - Connect S-GND to the CPU ground and connect P-GND to the power system ground.
 - Insert resistors of about $10k\Omega$ in series between the controller outputs and the inputs on this IC.

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