

LB1S – LB10S

G

n

Dim

Α

В

С

D

Ε

G

J

Κ

L

MBL-S

Min

4.50

3.60

0.10

6.40

0.70

1.30

2.20

0.56

All Dimensions in mm

0.8A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

В

Κ



Max

5.10

4.60

0.35

0.20

7.20

1.10

1.60

2.60

0.84

Features

- Ultra-Slim 1.6mm Max. Case Height
- Glass Passivated Die Construction
- High Reliability
- Low Forward Voltage Drop
- High Surge Current Capability
- Designed for Surface Mount Application
- Plastic Material UL Flammability 94V-0

Mechanical Data

- Case: MBL-S, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: As Marked on Case
- Weight: 0.10 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- Lead Free: For RoHS / Lead Free Version, Add "-LF" Suffix to Part Number, See Page 4

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	LB1S	LB2S	LB4S	LB6S	LB8S	LB10S	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	100	200	400	600	800	1000	V
RMS Reverse Voltage	VR(RMS)	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) $@T_A = 40^{\circ}C$ Average Rectified Output Current (Note 2) $@T_A = 40^{\circ}C$	lo	0.8 0.5				А		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	35			A			
Forward Voltage per diode $@I_F = 0.4A$ $@I_F = 0.8A$	Vfm	1.0 1.1			V			
Peak Reverse Current $@T_A = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_A = 125^{\circ}C$	Irm	5.0 500			μA			
Typical Junction Capacitance per diode (Note 3)	CJ	13			pF			
Thermal Resistance Junction to Ambient (Note 2) Thermal Resistance Junction to Ambient (Note 1) Thermal Resistance Junction to Lead (Note 2)	R JA R JA R JL	134 76 20		°C/W				
Operating and Storage Temperature Range	TJ, Tsтg	-55 to +150			°C			

Note: 1. Mounted on aluminum substrate PCB with 1.3 x 1.3mm pad areas.

2. Mounted on glass epoxy PCB with 1.3 x 1.3mm pad areas.

3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

LB1S - LB10S

1.0

0.8

0.6

0.4

0.2

0

50

40

30

20

10

0

2.0

1.6

1.2

0.8

0.4

0

0

I_{F(AV)}, AVERAGE FORWARD CURRENT (A)

Fig. 5 Forward Power Dissipation

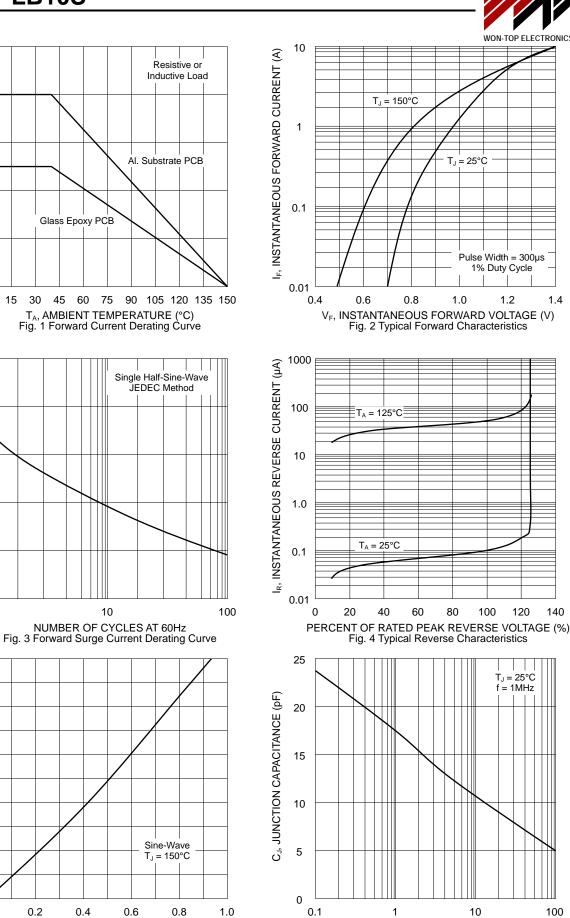
P_D, POWER DISSIPATION (W)

1

IFSM, PEAK FORWARD SURGE CURRENT (A)

0

I_(AV), AVERAGE FORWARD RECTIFIED CURRENT (A)



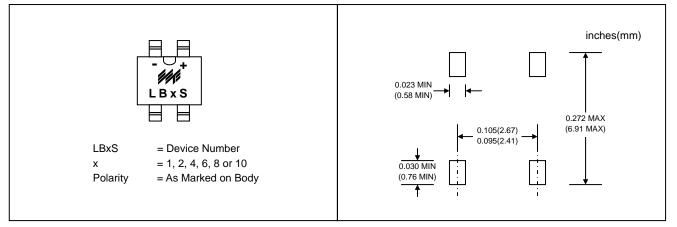
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V_R, DC REVERSE VOLTAGE (V)

Fig. 6 Typical Junction Capacitance

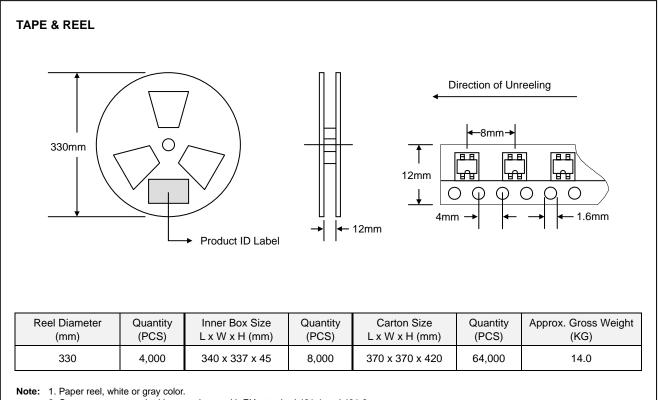


MARKING INFORMATION



RECOMMENDED FOOTPRINT

PACKAGING INFORMATION



2. Components are packed in accordance with EIA standard 481-1 and 481-2.



Product No.	Package Type	Shipping Quantity			
LB1S-T3	MBL-S	4000/Tape & Reel			
LB2S-T3	MBL-S	4000/Tape & Reel			
LB4S-T3	MBL-S	4000/Tape & Reel			
LB6S-T3	MBL-S	4000/Tape & Reel			
LB8S-T3	MBL-S	4000/Tape & Reel			
LB10S-T3	MBL-S	4000/Tape & Reel			

ORDERING INFORMATION

1. Shipping quantity given is for minimum packing quantity only. For minimum order quantity, please consult the Sales Department. To order RoHS / Lead Free version (with Lead Free finish), add "-LF"

2. suffix to part number above. For example, LB1S-T3-LF.

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