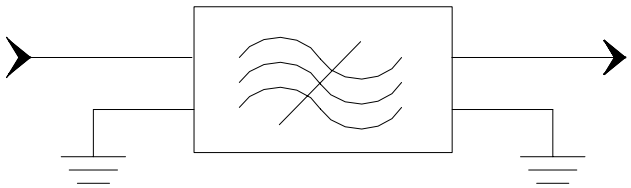
Specifications

Parameter		Unit	Minimum	Typical	Maximum
Center Frequency		MHz	69.95	70	70.05
Insertion Loss		dB	-	21.5	24
3 dB Bandwidth		MHz	7.75	7.8	7.86
Selectivity	F0±4.3MHz	dBc	35	44	-
	F0±4.5MHz	dBc	45	58	-
	F0±4.9MHz	dBc	50	68	-
	F0±8.9MHz	dBc	55	60	-
Phase Linearity		deg	-	4.1	-
Passband Variation		dB	-	0.6	1.2
Ultimate Rejection(f0±15MHz)		dB	55	60	-
Absolute delay		usec	-	3.75	-
Subatrate Material			$YZ\text{-}LiNbO_3$		
Ambient Temperature		°C	25		
Package Size		DIP3512 (35.2x12.7x5.2mm^3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

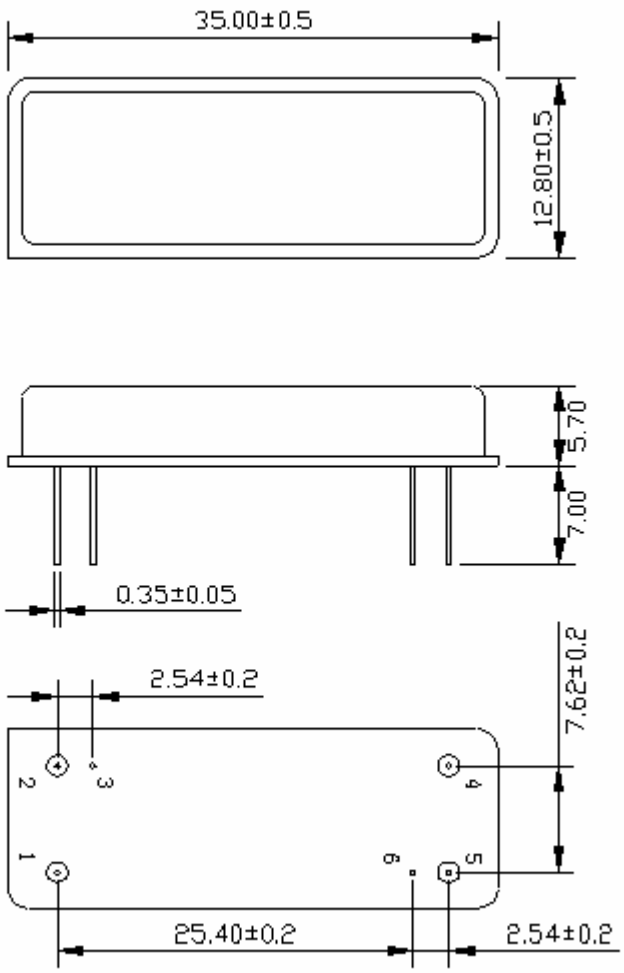
Matching Configuration



Source/Load Impedance=50 ohm

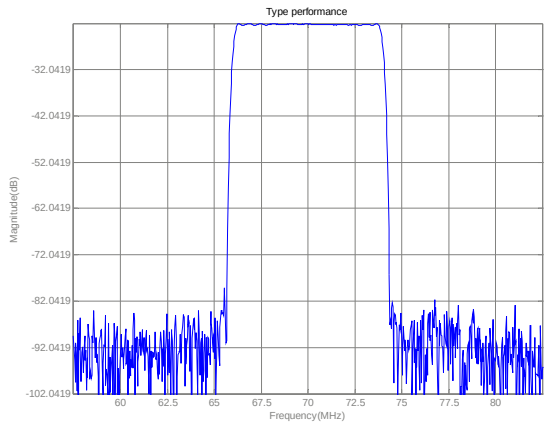
Notes - Component values may change depending on board layout.

Package Dimension

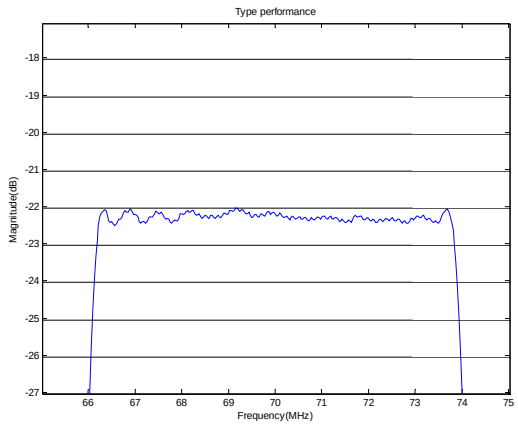


Typical Performance

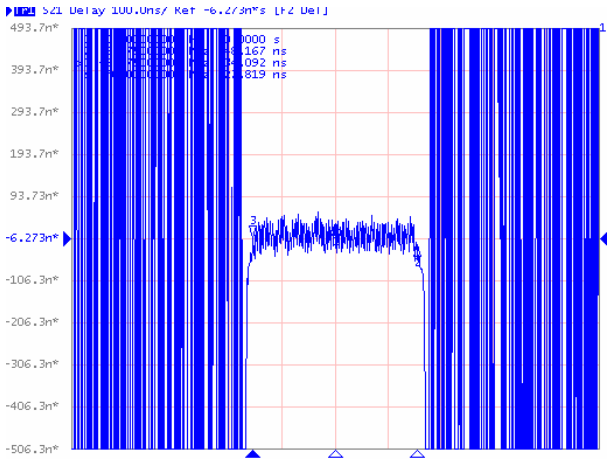
Frequency Respond



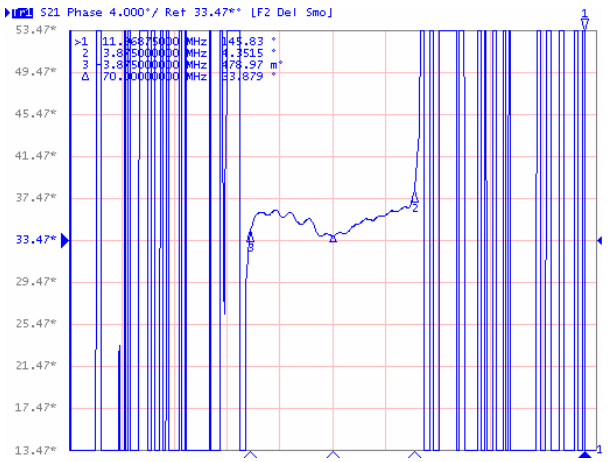
Passband Respond



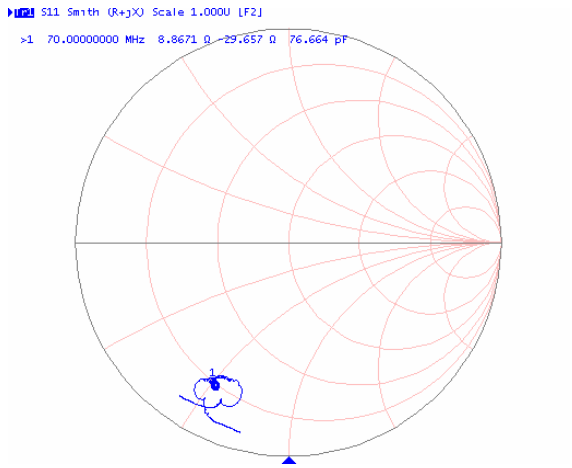
Group delay Variation(f0±3.875MHz)



Phase Linearity(f0±3.875MHz)



Simth Chart S11



Simth Chart S22