

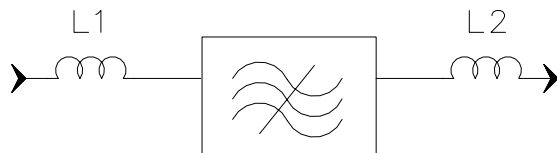
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	60.9	70	70.1
Insertion Loss	dB	-	24.5	26
3 dB Bandwidth	MHz	13.95	14	14.05
38 dB Bandwidth	MHz	-	14.77	14.8
45 dB Bandwidth	MHz	-	14.82	15.2
50 dB Bandwidth	MHz	-	14.86	16
Passband Variation	dB	-	0.9	1.2
Absolute Delay	usec	-	3.6	-
Ultimate Rejection($f_0 \pm 15\text{MHz}$)	dB	55	57	-
Material Temperature coefficient	KHz/°C	-6.58		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.2x12.7x5.2mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show


Matching Configuration



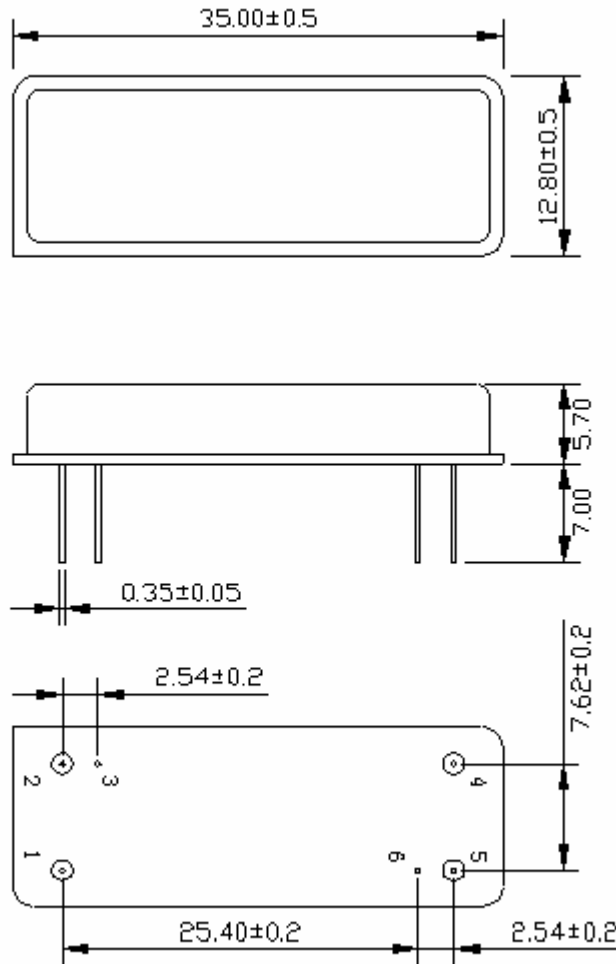
$$L1 = 82\text{nH} \quad L2 = 100\text{nH}$$

Source/Load Impedance=50 ohm


Notes - Component values may change depending
on board layout.

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		Rev. Date	2005-8-31	
		Rev.	1.0	Page 1/3

Package Dimension

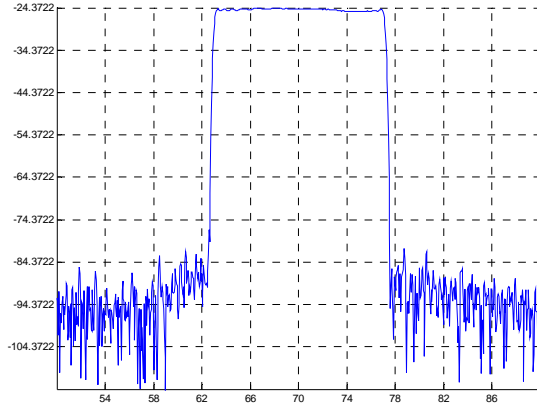


Input:1
Output:5

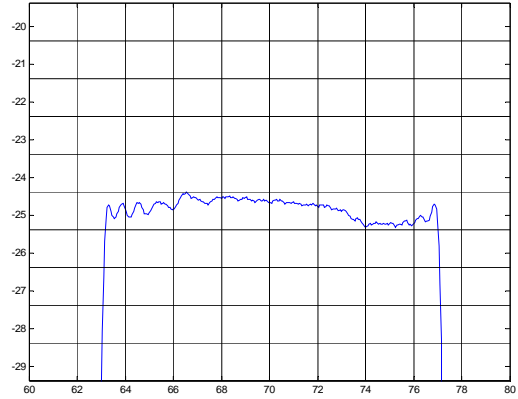
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Typical Performance

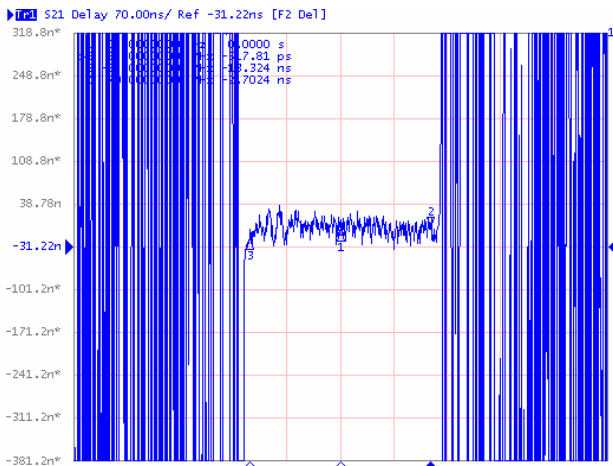
Frequency Respond



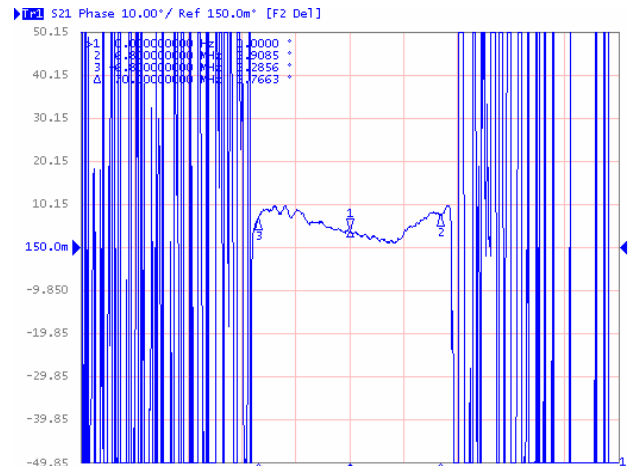
Passband Respond



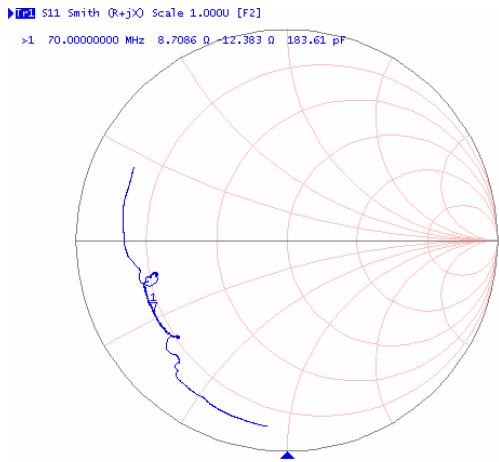
Group Delay Variation($f_0 \pm 6.8\text{MHz}$)



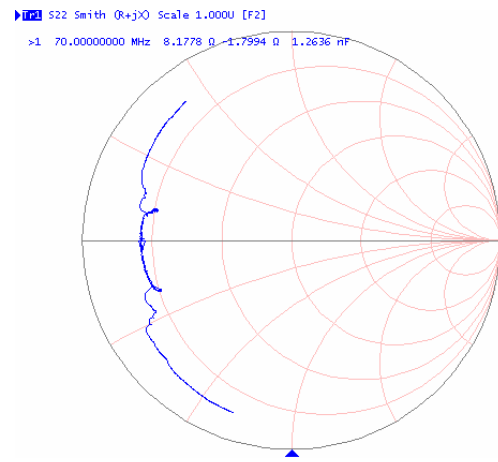
Phase Linearity($f_0 \pm 6.8\text{MHz}$)



Smith Chart S11



Smith Chart S22



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Part Number	LBN70A05	
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Rev.	1.0	Page 3/3