

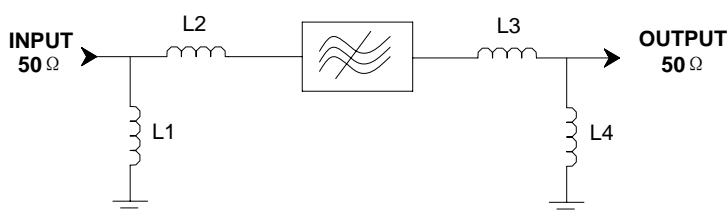
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	69.9	70	70.1
Insertion Loss	dB	-	30	31
3 dB Bandwidth	MHz	24.4	24.5	24.6
35 dB Bandwidth	MHz	-	25.25	25.3
40 dB Bandwidth	MHz	-	25.3	-
45dB Bandwidth	MHz	-	25.34	25.7
50dB Bandwidth	MHz	-	25.39	26.5
55dB Bandwidth	MHz	-	25.43	34.5
Near Rejection	dB	45	52	-
Ultimate Rejection (outside $f_0 \pm 25\text{MHz}$)	dB	50	55	-
Passband Variation	dB	-	0.7	1.5
Absolute Delay	usec	-	3.73	4
Material Temperature coefficient	KHz/°C	-6.58		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.2x12.7x5.2mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=180nH L2=56nH
L3=82nH L4=330nH
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.



SIPAT Co., Ltd.
 (CETC No. 26 Research Institute)
 Nanping Huayuan Road No. 14
 Chongqing, China, 400060

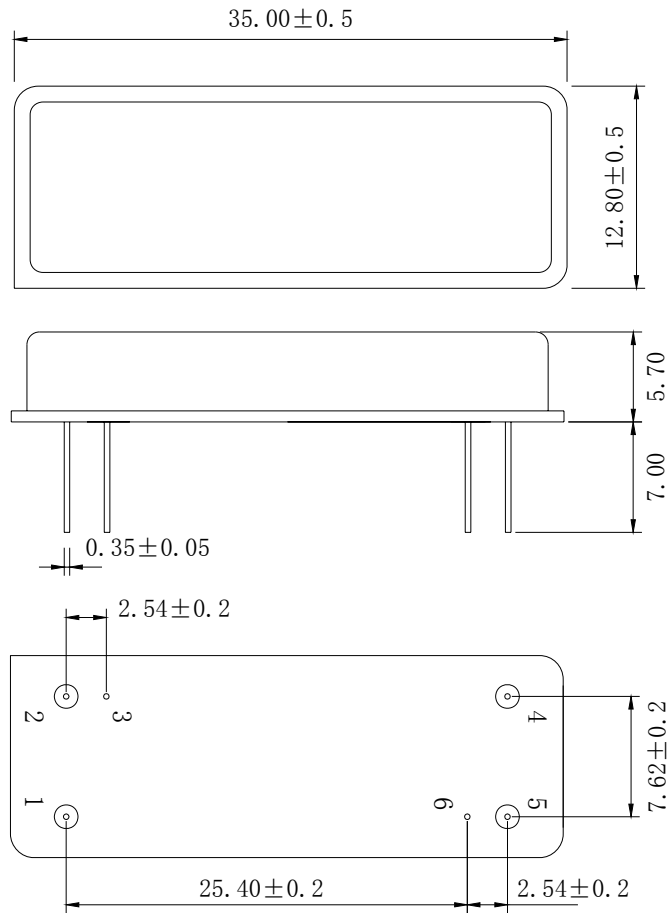
Part Number LBN70A24

Rev. Date 2006-7-3

Rev. 2.0

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Package Dimension



Package: DIP3512

Unit: mm

Input	1
Output	5
Ground	2,3,4,6

Package: DIP3512

Unit: mm

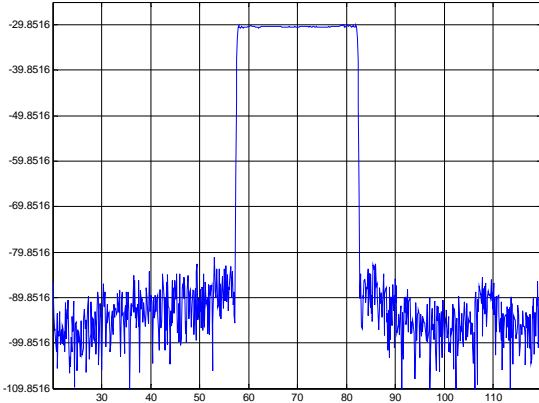


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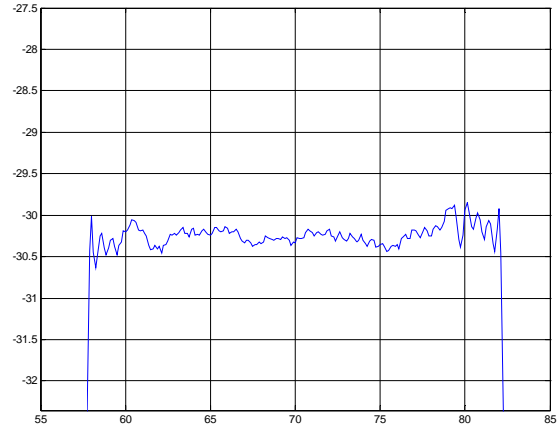
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Typical Performance

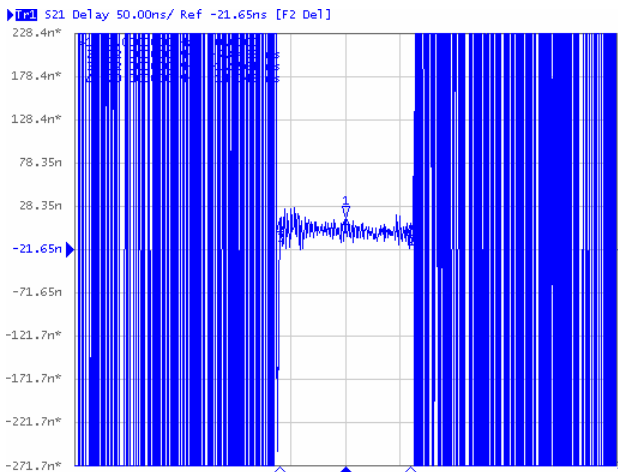
Frequency Respond



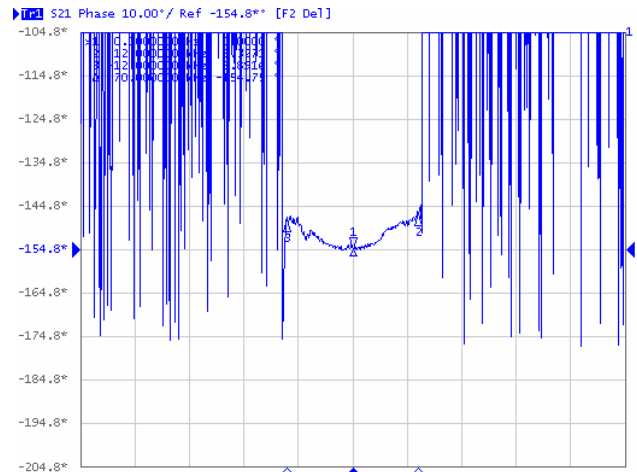
Passband Respond



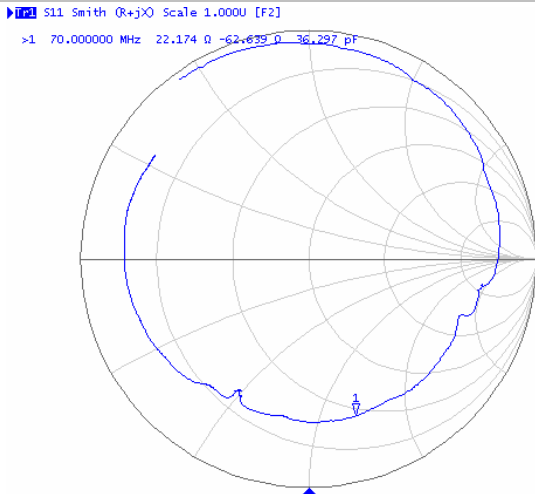
Group Delay Variation($f_0 \pm 12\text{MHz}$)



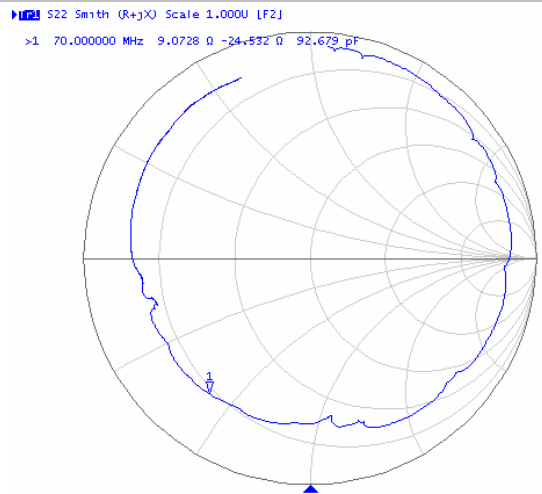
Phase Linearity($f_0 \pm 12\text{MHz}$)



Smith Chart S11



Smith Chart S22



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