

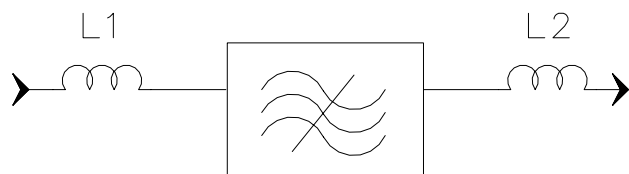
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	69.92	70	70.08
Insertion Loss	dB	-	22.5	25
1 dB Bandwidth	MHz	1.3	1.53	-
3 dB Bandwidth	MHz	1.5	1.72	-
40 dB Bandwidth	MHz	-	2.5	2.8
50 dB Bandwidth	MHz	-	2.63	-
Passband Variation	dB	-	0.4	0.5
Absolute Delay	usec	-	3.75	-
Phase Linearity($f_0 \pm 0.7\text{MHz}$)	deg	-	5	6
Group Delay Variation($f_0 \pm 0.7\text{MHz}$)	nsec	-	180	200
Ultimate Rejection	dB	50	53	-
Material Temperature coefficient	KHz/°C	0.07		
Ambient Temperature	°C	25		
Package Size	DIP2712 (27.2x12.7x5.2mm3)			

Notes:


- All specifications are based on the test circuit shown
- In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
- Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
- This is the optimum impedance in order to achieve the performance show

Matching Configuration

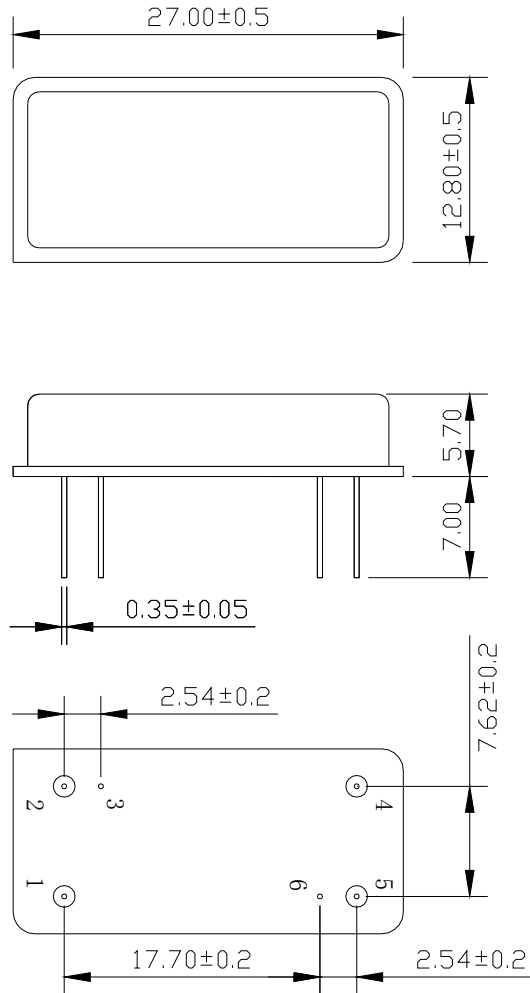


L1=270nH L2=330nH
Source/Load Impedance=50 ohm


Notes - Component values may change depending
on board layout.

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Package Dimension

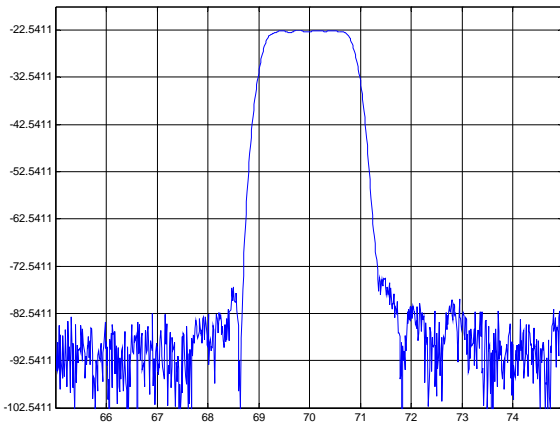


Input:1
Output:5

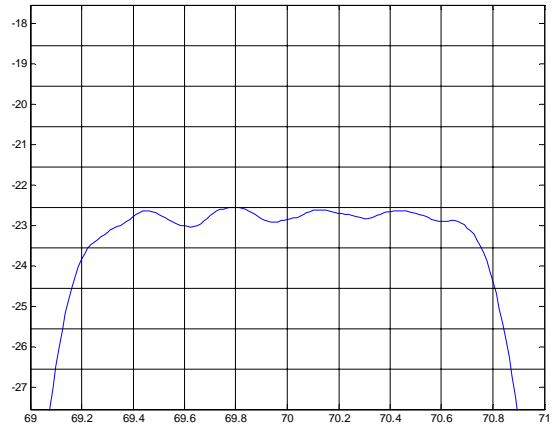
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Typical Performance

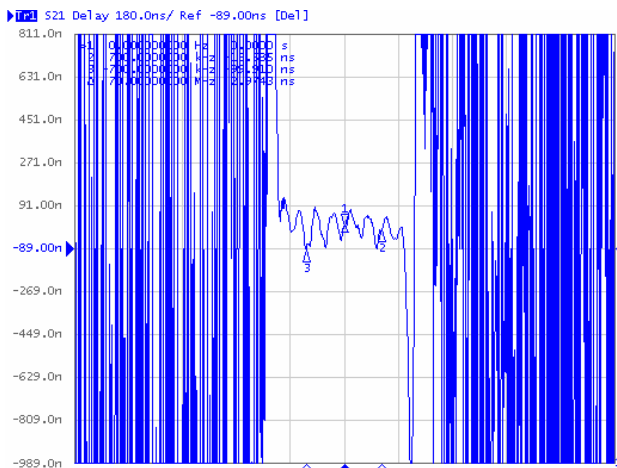
Frequency Respond



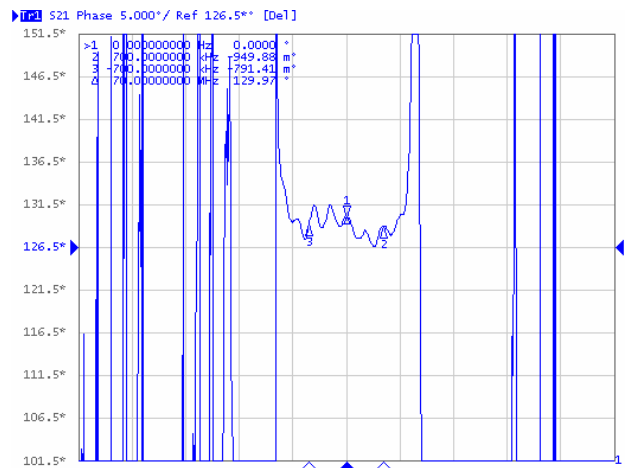
Passband Respond



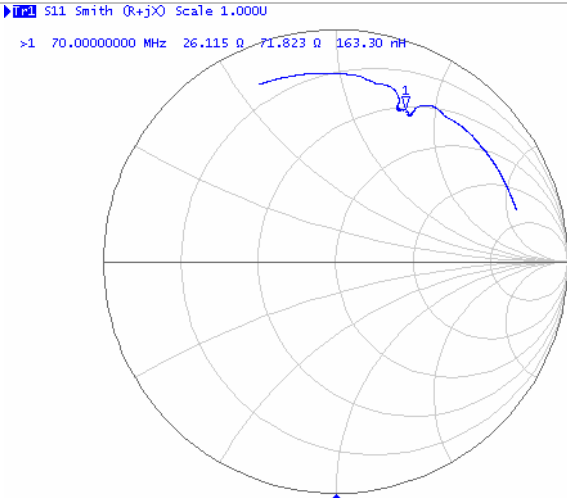
Group Delay Variation($f_0 \pm 0.7\text{MHz}$)



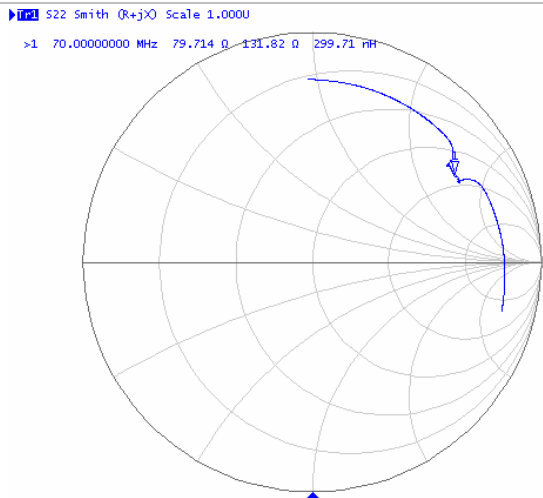
Phase Linearity($f_0 \pm 0.7\text{MHz}$)



Smith Chart S11



Smith Chart S22



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