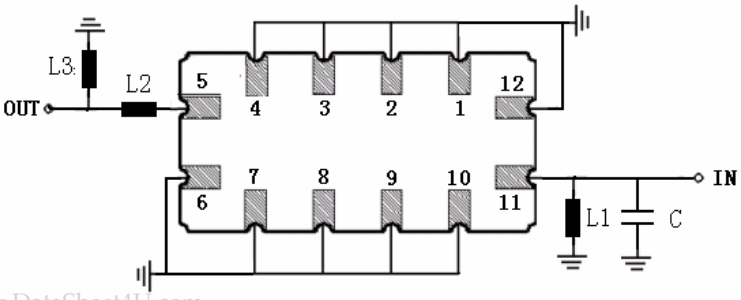
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	175.9	176	176.1
Insertion Loss	dB	-	11.4	13
1 dB Bandwidth	MHz	-	3.67	-
3 dB Bandwidth	MHz	-	4.57	-
30 dB Bandwidth	MHz	-	7.04	-
40 dB Bandwidth	MHz	-	7.5	-
Passband Variation	dB	-	0.8	1.0
Phase Linearity (F0± 1.4MHz)	degree	-	5	-
Ultimate Rejection(F0± 6MHz)	dB	40	45	-
Absolute Delay	usec	-	0.9	-
Subatrate Material		112LT		
Ambient Temperature	°C	25		
Package Size	SMD13.3*6.5 (13.3 x 6.5 mm Nominal Footprint)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

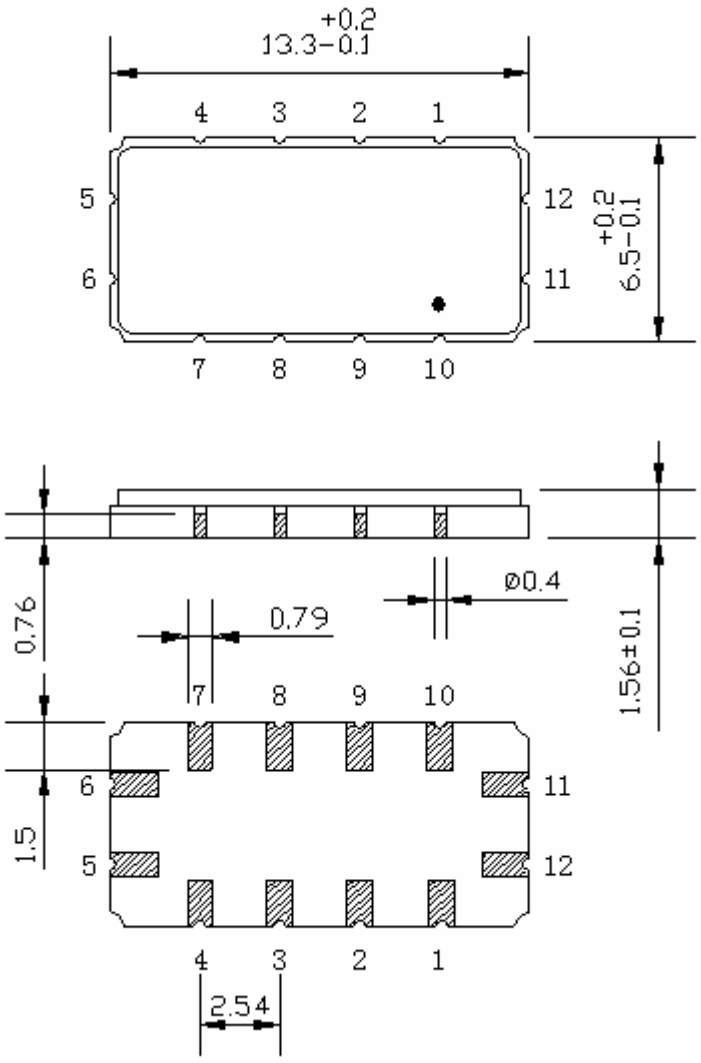


C=47pF L1=18nH
L2=15nH L3=18nH

Source/Load Impedance=50 ohm

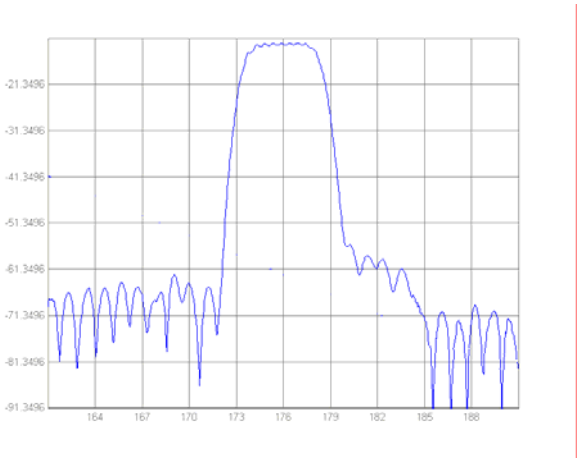
Notes - Component values may change depending on board layout.

Package Dimension

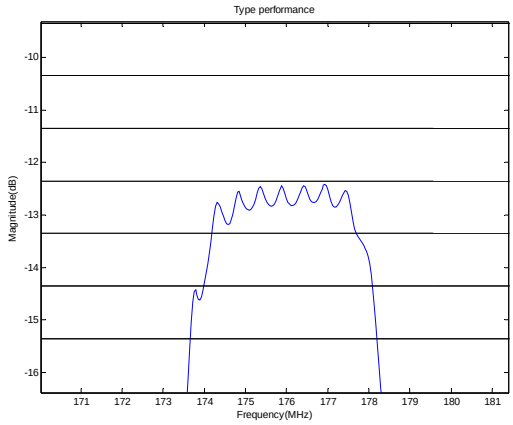


Typical Performance

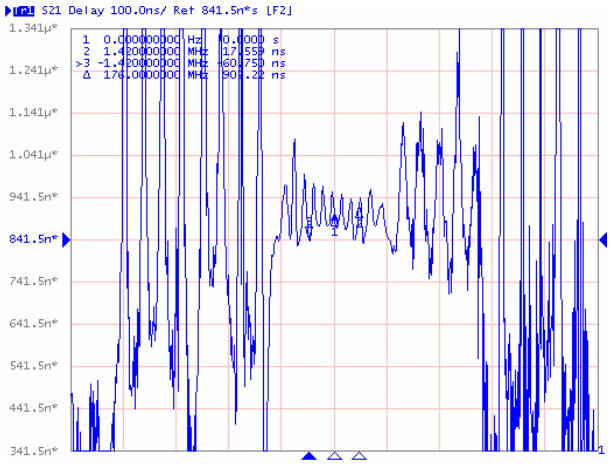
Frequency Respond



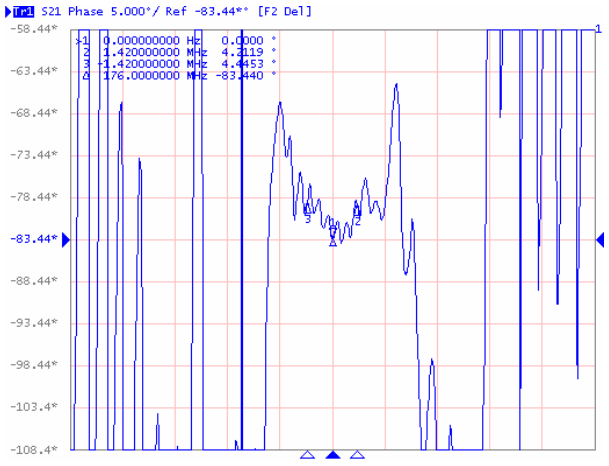
Passband Respond



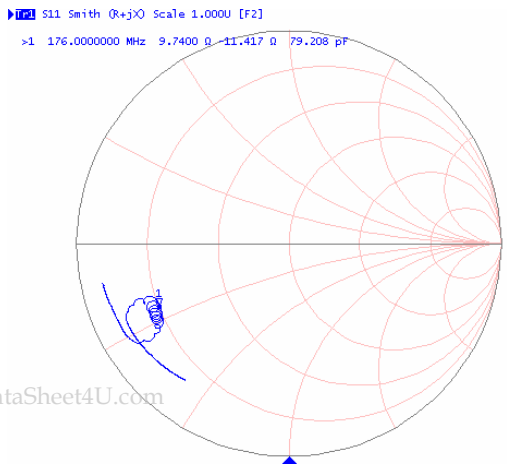
$F_0 \pm$ 1.4MHz Group delay Variation



$F_0 \pm$ 1.4MHz Phase Linearity



Simth Chart S11



Simth Chart S22