



LC1463

300mA High PSRR, Fast Response Linear Regulator

DESCRIPTION

LC1463 series is a group of positive voltage output, low power consumption, low dropout voltage regulator.

LC1463 can provide output value in the range of 1.0V~4.5V every 0.1V step. It also can be customized on command.

LC1463 includes high accuracy voltage reference, error amplifier, current limit circuit and output driver module with discharge capability.

LC1463 has excellent load and line transient response and good temperature characteristics, which can assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$.

LC1463 is available in SOT-23-5, SC-70-5 and SOT23-3 packages which is lead free.

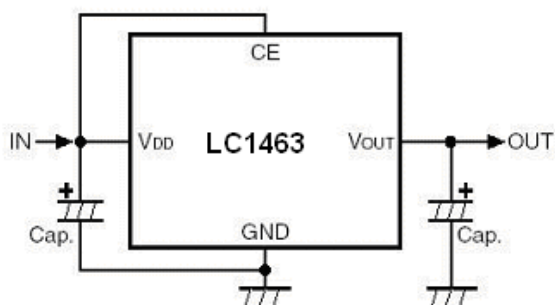
FEATURES

- Low Power Consumption: 25uA (Typ.)
- Low output noise (47uVRMS)
- Standby Mode: 0.1uA
- Low dropout Voltage: 0.16V@300mA (Typ.)
- High Ripple Rejection: 73dB@100Hz (Typ.)
- Low Temperature Coefficient: $\pm 100\text{ppm}/^\circ\text{C}$
- Excellent Line regulation: 0.05%/V
- Build-in chip enable and discharge circuit
- Output Voltage Range: 1.0V~4.5V (customized on command every 0.1V step)
- Highly Accurate: $\pm 2\%$
- Output Current Limit

APPLICATIONS

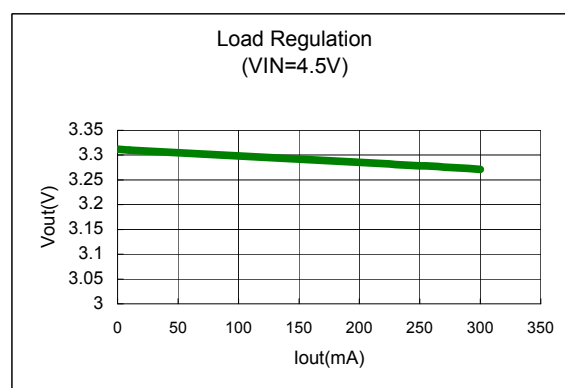
- Power source for cellular phones and various kind of PCSs
- Battery Powered equipment
- Power Management of MP3, PDA, DSC, Mouse, PS2 Games
- Reference Voltage Source
- Regulation after Switching Power

TYPICAL APPLICATION



NOTE: Input capacitor ($C_{in}=1\mu\text{F}$) and Output capacitor ($C_{out}=3.3\mu\text{F}/4.7\mu\text{F}$) are recommended in all application circuit.

ELECTRICAL CHARACTERISTICS



ORDERING INFORMATION

LC1463 ① ② ③ ④ ⑤

Code	Description
①	Temperature&RoHS: C:-40~85°C ,Pb Free RoHS Std.
②	Package type: A5:SC-70-5 B5A:SOT-23-5(A) B5B:SOT-23-5(B) B3: SOT23-3
③	Packing type: TR:Tape&Reel (Standard)
④	Output voltage: e.g. 15=1.5V 18=1.8V 45=4.5V

MARKING DESCRIPTON

\bar{F} : Product Code
X: Output Voltage

Vout	Code	Vout	Code	Vout	Code
1.0V	0	2.3V	$\bar{3}$	3.6V	$\bar{6}$
1.1V	1	2.4V	$\bar{4}$	3.7V	$\bar{7}$
1.2V	2	2.5V	$\bar{5}$	3.8V	$\bar{8}$
1.3V	3	2.6V	$\bar{6}$	3.9V	$\bar{9}$
1.4V	4	2.7V	$\bar{7}$	4.0V	$\bar{0}$
1.5V	5	2.8V	$\bar{8}$	4.1V	$\bar{1}$
1.6V	6	2.9V	$\bar{9}$	4.2V	$\bar{2}$
1.7V	7	3.0V	$\bar{0}$	4.3V	$\bar{3}$
1.8V	8	3.1V	$\bar{1}$	4.4V	$\bar{4}$
1.9V	9	3.2V	$\bar{2}$	4.5V	$\bar{5}$
2.0V	$\bar{0}$	3.3V	$\bar{3}$		
2.1V	$\bar{1}$	3.4V	$\bar{4}$		
2.2V	$\bar{2}$	3.5V	$\bar{5}$		

Z: The Year of manufacturing, "7" stands for year 2007, "8" stands for year 2008, and "0" stands for year 2010.

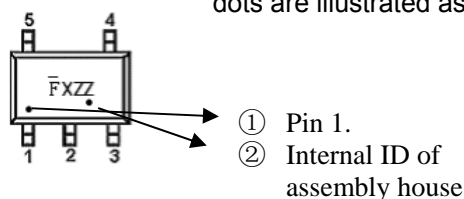
Z: The week of manufacturing. "A" stands for week 1, "Z" stands for week 26, "A" stands for week 27, "Z" stands for week 52.

PIN CONFIGURATION

Product Classification	LC1463CA5TR□□
\bar{F} : Product Code	
X: Output Voltage	
ZZ: Date Code	
Product Classification	LC1463CB5ATR□□
\bar{F} : Product Code	
X: Output Voltage	
ZZ: Date Code	
Product Classification	LC1463CB5BTR□□
\bar{F} : Product Code	
X: Output Voltage	
ZZ: Date Code	
I: B type	
Product Classification	LC1463CB3TR□□
F: Product Code	
X: Output Voltage	
ZZ: Date Code	
Vss	Ground Pin
Vin	Supply Voltage Input
Vout	Output Voltage
CE	Chip Enable
NC	No Connection

Optional marking information:

From one source of assembly house, there are dots in addition to formal marking information. One is the pin1 indicator and the other is for internal identification of the assembly house. The dots are illustrated as below:



ABSOLUTE MAXIMUM RATING

Parameter		Value
Max Input Voltage		8V
Operating Junction Temperature(Tj)		125°C
Output Current		300mA
Ambient Temperature(Ta)		-40°C -85°C
Power Dissipation	SC70-5	250mW
	SOT-23-5	250mW
	SOT-23-3	250mW
Storage Temperature(Ts)		-40°C -150°C
Lead Temperature & Time		260°C, 10S

Note:

Exceed these limits to damage to the device.

Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMENDED WORK CONDITIONS

Item	Min	Recommended	Max.	Unit
Input Voltage Range	2		6	V
Ambient Temperature	-40		85	°C

ELECTRICAL CHARACTERISTICS

(Test Conditions: Cin=1uF, Cout=3.3uF, TA=25°C, unless otherwise specified.)

LC1463, For Arbitrary Output Voltage

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Vin	Input Voltage		2		6	V
Vout	Output Voltage	V _{OUT} >1.5V	Vin=Set Vout+1V 1mA≤I _{out} ≤30mA	Vout x0.98	Vout	Vout X1.02 Vout +0.03
		V _{OUT} ≤1.5V				
I _{out} (Max.)	Maximum Output Current	Vin-Vout=1V	300			mA
V _{drop} ¹	Dropout Voltage, Vout≥2.8V	I _{out} =100mA		50	80	mV
		I _{out} =300mA		160	220	mV
$\frac{\Delta V_{out}}{\Delta V_{in} \cdot V_{out}}$	Line Regulation	I _{out} =40mA 2.8V≤Vin≤6V		0.05	0.2	%/V
$\Delta V_{out} / \Delta I_{out}$	Load Regulation	Vin=Set Vout+1V 1mA≤I _{out} ≤300mA		60	100	mV
I _{ss}	Supply Current	Vin=Set Vout+1V		25	50	uA
I _{standby}	Supply Current (Srandby)	Vin=Set Vout+1V Vce=GND		0.1	1.0	uA
$\frac{\Delta V_{out}}{\Delta T \cdot V_{out}}$	Output Voltage Temperature Coefficiency	I _{out} =30mA		± 100		ppm/°C

LC1463

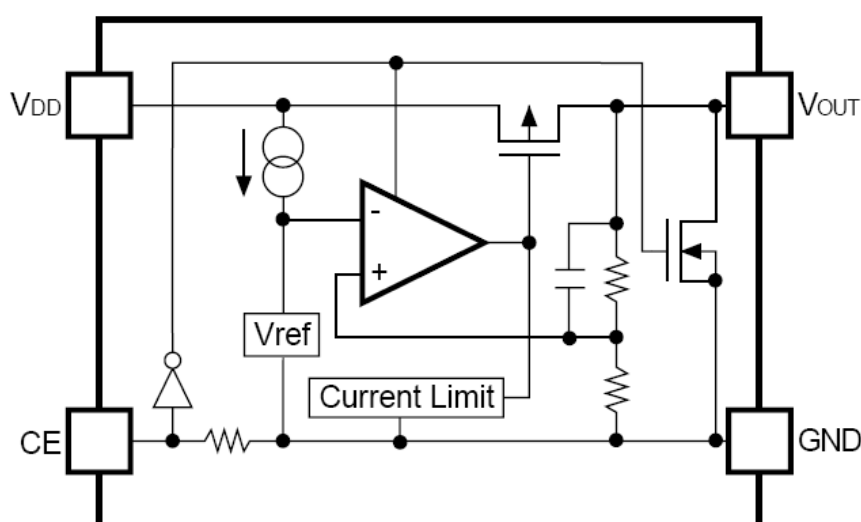
PSRR	Ripple Rejection	F=100Hz, Ripple=0.5Vp-p Vin=Set Vout+1V		70		dB
Ilim	Short Current Limit	Vout=0V		500		mA
Rpd	CE Pull down Resistance		2.0	5.0	10.0	MΩ
Vceh	CE Input Voltage "H"		1.5		Vin	V
Vcel	CE Input Voltage "L"		0		0.25	V
en	Output Noise	BW=10Hz~100kHz		47		uVrms

NOTE:

$V_{drop} = V_{in1} - (V_{out2} * 0.98)$ V_{out2} is the output voltage when $V_{in} = V_{out1} + 1.0V$ and $I_{out} = 300mA$.

V_{in1} is the input voltage at which the output voltage becomes 98% of V_{out1} after gradually decreasing the input voltage.

BLOCK DIAGRAM



Explanation

LC1463 series is a group of positive voltage output, low noise, low power consumption, low dropout voltage regulator.

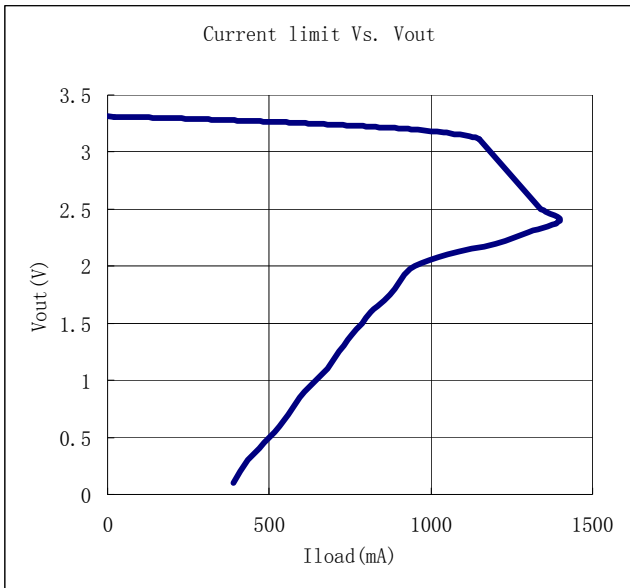
LC1463 can provide output value in the range of 1.5V~4.5V every 0.1V step. It also can be customized on command.

LC1463 includes high accuracy voltage reference, error amplifier, current limit circuit and output driver module.

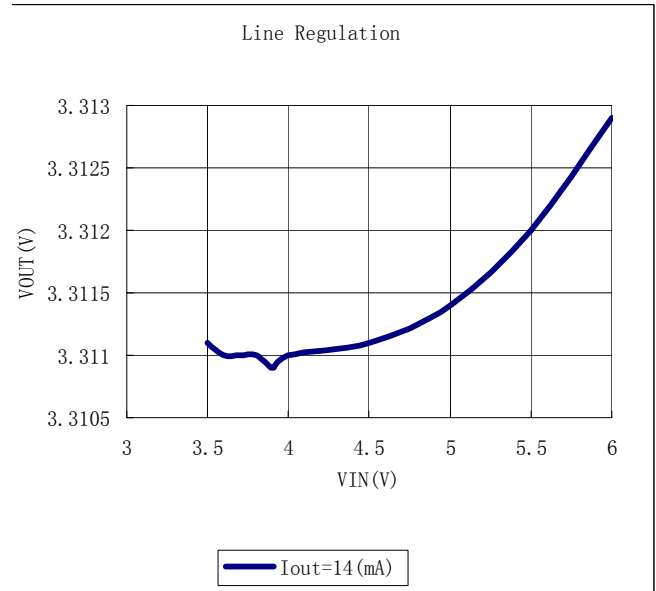
LC1463 has excellent load and line transient response and good temperature characteristics, which can assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$.

TYPICAL PERFORMANCE CHARACTERISTICS

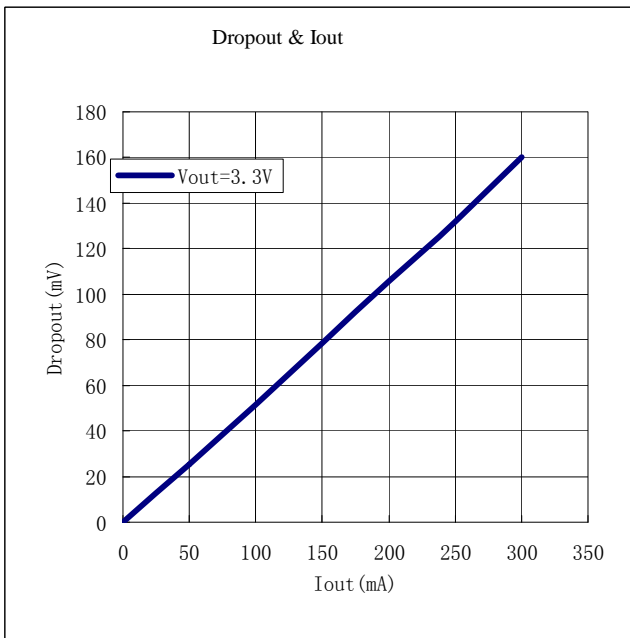
1) Output Voltage vs. Output Current (with output short protection)



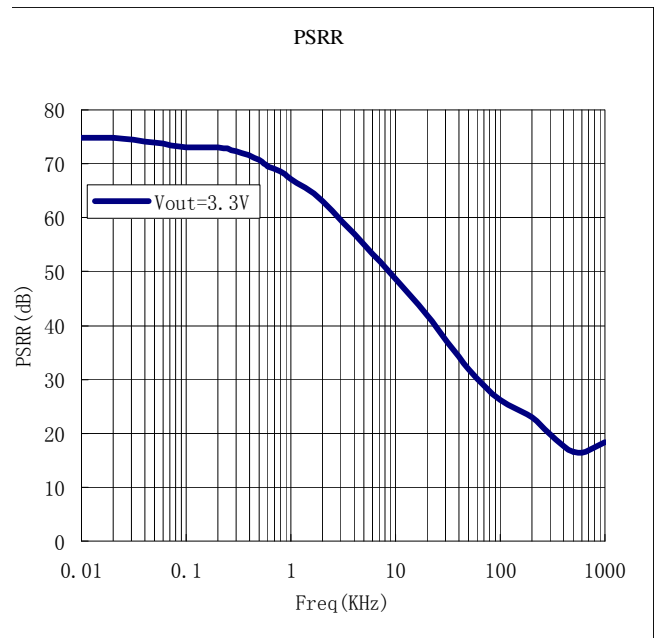
2) Output Voltage vs. Input Voltage



3) Dropout Voltage vs. Output Current

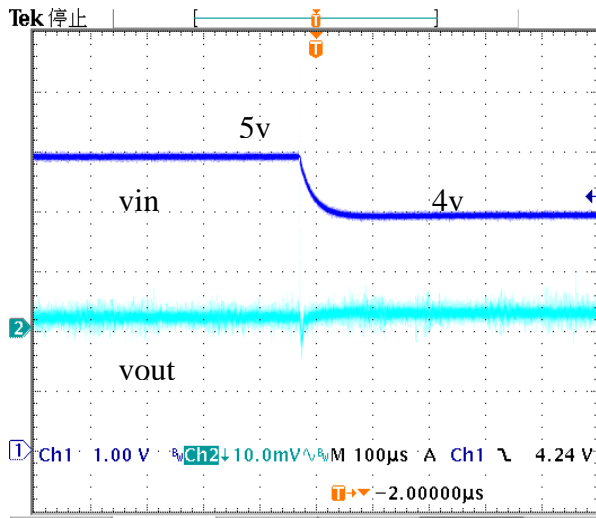
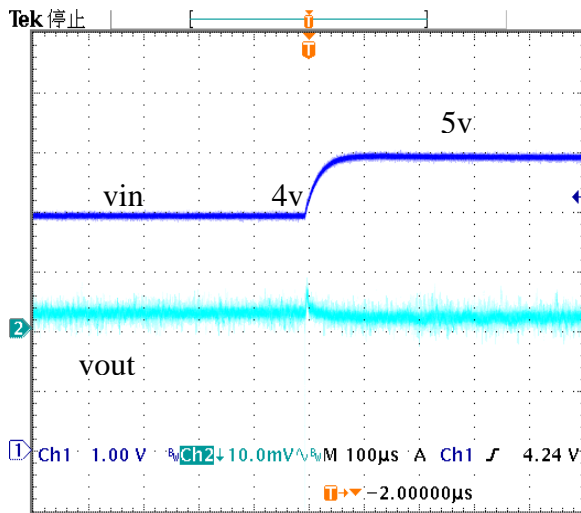


4) Ripple rejection vs. Frequency



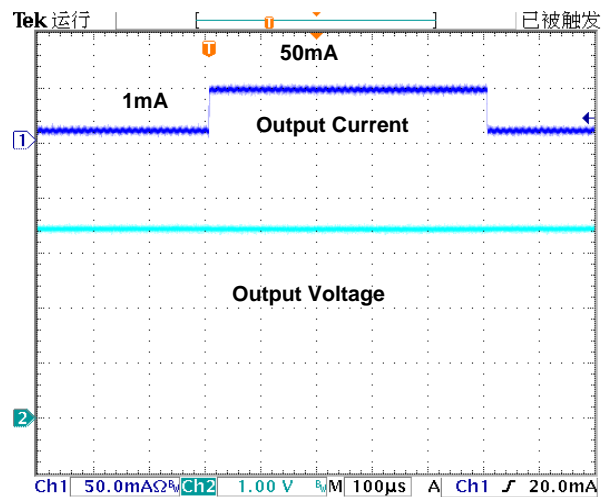
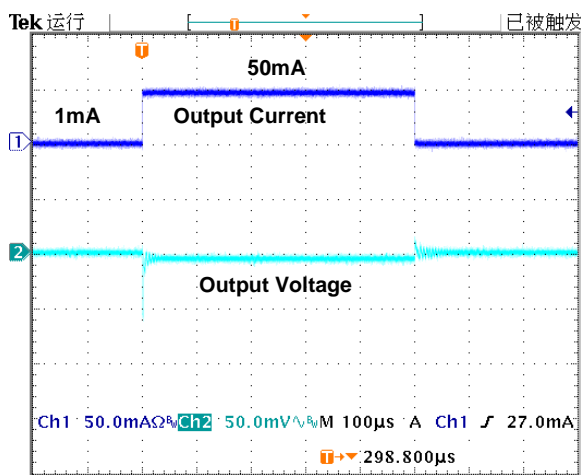
5) Line transient response

$C_{in}=C_{out}=1\mu F$ $I_{out}=25mA$ $V_{out}=3.3V$

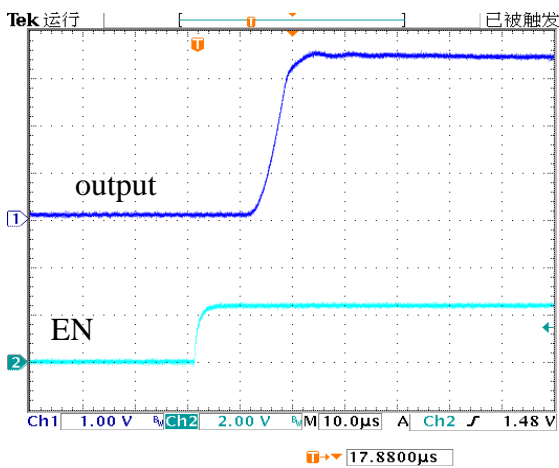


6) Load transient response

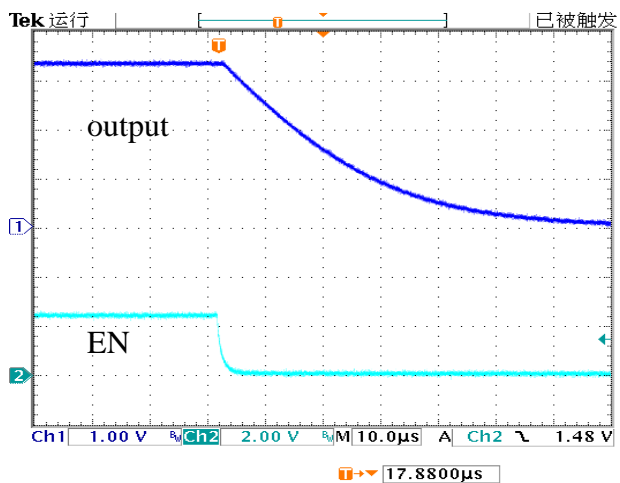
$C_{in}=C_{out}=1\mu F$ $V_{in}=4.5V$ $V_{out}=3.3V$



7) Start up from EN



8) Shut down from EN



PACKAGE OUTLINE

Package	SC70-5	Devices per reel	3000Pcs	Unit	mm
<p>Package dimension:</p>					

Package	SOT-23-5	Devices per reel	3000Pcs	Unit	mm
<p>Package Dimension:</p>					

Package	SOT-23-3	Devices per reel	3000Pcs	Unit	mm
Package Dimension:					
<p>The drawing shows the mechanical specifications of the LC1463 SOT-23-3 package. It includes a top view, a side view, and a perspective view. Key dimensions are as follows:</p> <ul style="list-style-type: none"> Top View: <ul style="list-style-type: none"> Overall width: 2.9 ± 0.2 mm Lead 3 width: 0.4 ± 0.1 mm Lead 1 and 2 spacing: 1.9 ± 0.2 mm Lead 1 and 2 width: 0.95 mm (each) Package body width: 1.6 ± 0.2 mm Side View: <ul style="list-style-type: none"> Maximum height: 1.4 MAX. mm Lead 3 height: 1.1 mm (tolerance: $+0.2$ to -0.1) Lead 3 thickness: 0.8 mm Lead 3 lead-in length: 0 to 0.1 mm Lead 1 and 2 height: 0.2 MIN. mm Lead 1 and 2 thickness: 0.16 mm (tolerance: $+0.1$ to -0.06) 					