



Product Specification

SPECIFICATION FOR APPROVAL

| (|) Preliminary | Specification |
|---|---------------|----------------------|
|---|---------------|----------------------|

(●) Final Specification

Title

| BUYER | | SUPPLIER | LG Display Co., Ltd. |
|-------|--|----------|----------------------|
| | | | |

| BUYER | |
|-------|--|
| MODEL | |

| SUPPLIER | LG Display Co., Ltd. | | |
|----------|----------------------|--|--|
| *MODEL | LC185EXE | | |
| SUFFIX | TEA1 (RoHS Verified) | | |

18.5" HD TFT LCD

^{*}When you obtain standard approval, please use the above model name without suffix

| APPROVED BY | SIGNATURE DATE |
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| Please return 1 copy for your | confirmation with |
| your signature and co | mments. |

| APPROVED BY | SIGNATURE DATE | | | | |
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Record of revisions

| Revision No | Date | Page | Description |
|--------------------|---------------|------|----------------------------|
| Ver 0.0 | OCT. 12, 2011 | | Preliminary Specifications |
| Ver 0.1 | OCT. 26, 2011 | 26 | Update REAR VIEW |
| | | 31 | Update Storage |
| Ver 1.0 | NOV. 24, 2011 | | Final CAS release |
| | | | |

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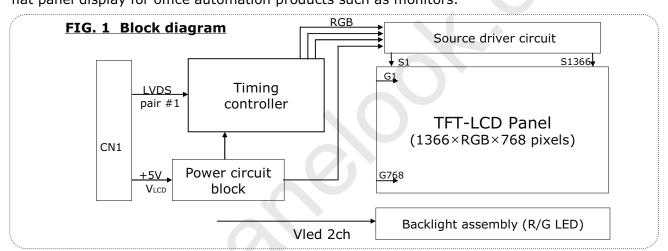




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1. General description

LC185EXE-TEA1 is a Color Active Matrix Liquid Crystal Display with a Light Emitting Diode (White LED) backlight system without LED Driver. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. It has a 18.5 inch diagonally measured active display area with HD resolution (768 vertical by 1366 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M colors with Advanced-FRC(Frame Rate Control). It has been designed to apply the interface method that enables low power, high speed, low EMI. FPD Link or compatible must be used as a LVDS chip. It is intended to support applications where thin thickness, wide viewing angle, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LC185EXE-TEA1 characteristics provide an excellent flat panel display for office automation products such as monitors.



General features

| General realures | | | | | |
|------------------------|--|--|--|--|--|
| Active screen size | 18.51 inches (470.1mm) diagonal | | | | |
| Outline Dimension | 430.4(H) x 254.6(V) x 9.7(D) mm(Typ.) | | | | |
| Pixel Pitch | 0.10*RGB(H)mm x 0.30(V)mm | | | | |
| Pixel Format | 1366 horizontal By 768 vertical Pixels. RGB stripe arrangement | | | | |
| Interface | LVDS 1Port | | | | |
| Color depth | 16.7M colors | | | | |
| Luminance, white | 250 cd/m² (Center 1Point, typ) | | | | |
| Viewing Angle (CR>10) | R/L 170(Typ.), U/D 160(Typ.) | | | | |
| Power Consumption | Total 12.80W(Typ.), (3.60W@VLCD, 9.20W@IBL = 110 mA) | | | | |
| Weight | 1,000g (Typ.) | | | | |
| Display operating mode | Transmissive mode, Normally White | | | | |
| Surface treatments | Hard coating(3H), Anti-glare treatment of the front polarizer | | | | |
| Color Gamut | 72% CIE1931 | | | | |
| | | | | | |

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2. Absolute maximum ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. Absolute maximum ratings

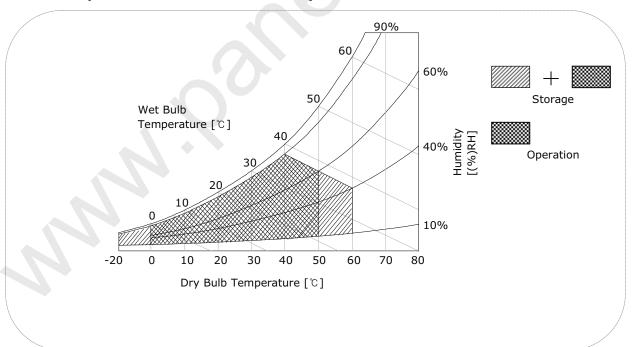
| Dawamatar | Cymphal | Val | ues | Linita | Notes | |
|----------------------------|------------------|------|------|--------|--------|--|
| Parameter | Symbol | Min | Max | Units | | |
| Power Supply Input Voltage | V _{LCD} | -0.3 | +6.0 | Vdc | At 25℃ | |
| Operating Temperature | T _{OP} | 0 | 50 | °C | | |
| Storage Temperature | T _{ST} | -20 | 60 | °C | 1.2 | |
| Operating Ambient Humidity | H _{OP} | 10 | 90 | %RH | 1, 2 | |
| Storage Humidity | H _{ST} | 10 | 90 | %RH | | |

Notes: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.

2. Storage condition is guaranteed under packing condition.

FIG. 2 Temperature and relative humidity







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3. Electrical specifications

3-1. Electrical characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

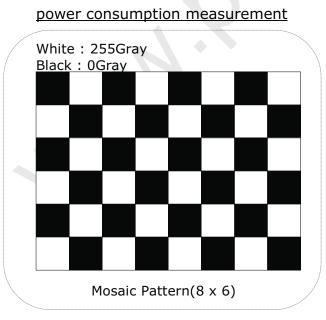
Table 2. Electrical characteristics

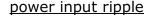
| Parameter | Cumbal | | Values | Unit | Notes | |
|-------------------------------|-------------------------|-----|--------|------|-------|-------|
| Parameter | Symbol | Min | Тур | Max | Unit | Notes |
| MODULE: | | | | | | |
| Power Supply Input Voltage | V _{LCD} | 4.5 | 5.0 | 5.5 | Vdc | |
| Permissive Power Input Ripple | V _{LCD} | - | - | 0.3 | V | 3 |
| Dower Supply Input Current | I _{LCD-MOSAIC} | - | 720 | 940 | mA | 1 |
| Power Supply Input Current | I _{LCD-BLACK} | - | 900 | 1170 | mA | 2 |
| Power Consumption | P _{LCD} | _ | 3.60 | 4.70 | Watt | 1 |
| Inrush current | I _{RUSH} | - | - | 3.0 | А | 4 |

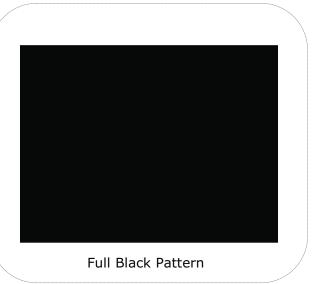
Notes:

- 1. The specified current and power consumption are under the VLCD=5.0V, $25 \pm 2^{\circ}\text{C}$, f_V =60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. Permissive power ripple should be measured under VCC=5.0V, $25^{\circ}C$, f_V (frame frequency)=75Hz condition and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20MHz.
- 4. The duration of rush current is about 5ms and rising time of power Input is 500us \pm 20%.

FIG.3 pattern for Electrical characteristics







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Table 3. LED array ELECTRICAL CHARACTERISTICS

| Parameter | Symbol | Condition | | Unit | Note | | |
|--------------------|----------|-----------|--------|------|-------------|-------|-----|
| Parameter | Syllibol | | Min. | Тур. | Max. | Offic | s |
| LED: | | | | | | | 1 |
| LED String Current | Is | | - | 110 | 120 | mA | 2 |
| LED String Voltage | Vs | | 37.7 | 41.6 | 45.5 | V | 3 |
| Power Consumption | Parray | | - | 9.2 | 10.0 | Watt | 4,6 |
| LED Life Time | LED_LT | | 30,000 | - | C- - | Hrs | 5 |

LED driver design guide

: The design of the LED driver must have specifications for the LED in LCD Assembly. The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.

So all the parameters of an LED driver should be carefully designed and output current should be constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs. When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

Notes:

- 1. Specified values are for a single LED bar.
- 2. The specified current is input LED chip 100% duty current.
- 3. The specified voltage is input LED string voltage at typical 110mA 100% duty current.
- 4. The specified power consumption is input LED bar power consumption at typical 110mA 100% duty current.
- 5. The life time is determined as the time at which luminance of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at $25 \pm 2^{\circ}$ C.
- The LED bar power consumption shown above does not include loss of external driver.The used LED string current is the LED typical current.
 - Typ Power Consumption is calculated with PBar = $Vs(Typ.) \times Is(Typ.) \times Nstring$ Max Power Consumption is calculated with PBar = $Vs(Max.) \times Is(Typ) \times Nstring$





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3-2. Interface connections

LCD Connector(CN1): GT103-30S-HF15-E2500(LSM) or IS100-L300-C23 (UJU)

Mating connector: FI-X30H and FI-X30HL (JAE) or Equivalent

Table 4. Module connector(CN1) pin configuration

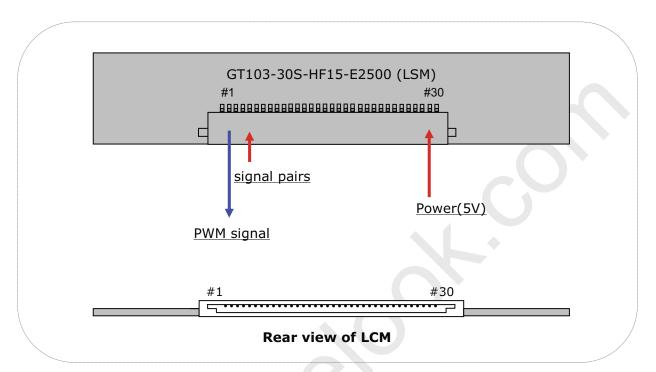
| Pin No | Symbol | Description | | | | | | |
|--------|---------|--|--|--|--|--|--|--|
| 1 | NC | No Connection (For LCD internal use only.) | | | | | | |
| 2 | PWM_OUT | Reference signal for inverter control | | | | | | |
| 3 | NC _ | No Connection (For LCD internal use only.) | | | | | | |
| 4 | GND | Ground | | | | | | |
| 5 | RX0- | Minus signal of channel 0 (LVDS) | | | | | | |
| 6 | RX0+ | Plus signal of channel 0 (LVDS) | | | | | | |
| 7 | GND | Ground | | | | | | |
| 8 | RX1- | Minus signal of channel 1 (LVDS) | | | | | | |
| 9 | RX1+ | Plus signal of channel 1 (LVDS) | | | | | | |
| 10 | GND | Ground | | | | | | |
| 11 | RX2- | Minus signal of channel 2 (LVDS) | | | | | | |
| 12 | RX2+ | Plus signal of channel 2 (LVDS) | | | | | | |
| 13 | GND | Ground | | | | | | |
| 14 | RXCLK- | Minus signal of clock channel (LVDS) | | | | | | |
| 15 | RXCLK+ | Plus signal of clock channel (LVDS) | | | | | | |
| 16 | GND | Ground | | | | | | |
| 17 | RX3- | Minus signal of channel 3 (LVDS) | | | | | | |
| 18 | RX3+ | Plus signal of channel 3 (LVDS) | | | | | | |
| 19 | GND | Ground | | | | | | |
| 20 | NC | No Connection (For LCD internal use only.) | | | | | | |
| 21 | NC | No Connection (For LCD internal use only.) | | | | | | |
| 22 | NC | No Connection (For LCD internal use only.) | | | | | | |
| 23 | GND | Ground | | | | | | |
| 24 | GND | Ground | | | | | | |
| 25 | GND | Ground | | | | | | |
| 26 | VLCD | Power Supply (5.0V) | | | | | | |
| 27 | VLCD | Power Supply (5.0V) | | | | | | |
| 28 | VLCD | Power Supply (5.0V) | | | | | | |
| 29 | VLCD | Power Supply (5.0V) | | | | | | |
| 30 | VLCD | Power Supply (5.0V) | | | | | | |





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FIG. 4 Connector diagram



Notes:

- 1. NC: No Connection.
- 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
- 3. All V_{LCD} (power input) pins should be connected together.
- 4. Input Level of LVDS signal is based on the IEA 664 Standard.
- 5. PWM_OUT is a reference signal for inverter control. This PWM signal is synchronized with vertical frequency. Its frequency is 3 times of vertical frequency, and its duty ratio is 50%. If the system don't use this pin, do not connect.

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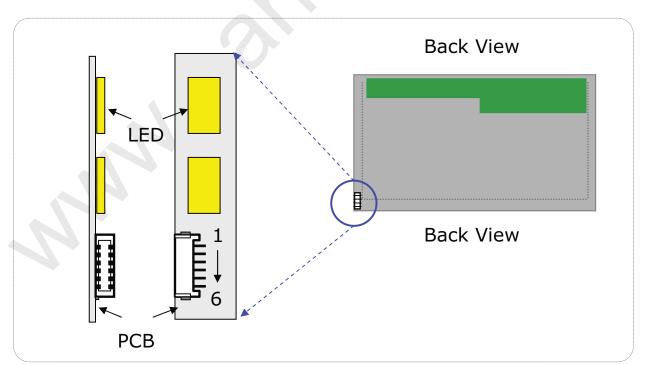
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Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION

The LED interface connector is a model 10019HR-H06B manufactured by Yeonho Electronics. The mating connector is a FFC/FPC specified in LED interface connector specification. The pin configuration for the connector is shown in the table below.

| Pin | Symbol | Description | Notes |
|-----|--------|---------------------------|-------|
| 1 | FB1 | Channel1 Current Feedback | |
| 2 | NC | No connection | |
| 3 | VLED | LED Power Supply | |
| 4 | VLED | LED Power Supply | |
| 5 | NC | No connection | |
| 6 | FB2 | Channel2 Current Feedback | |

FIG. 5 Backlight connector view



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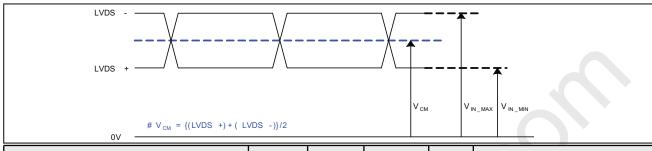




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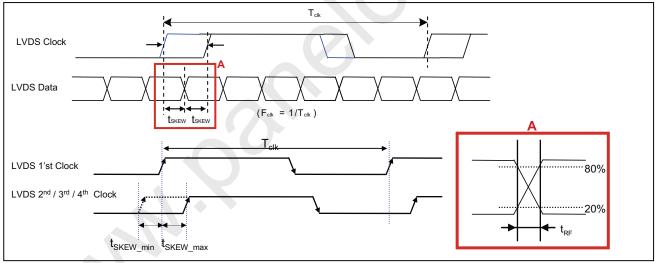
3-3. LVDS characteristics

3-3-1. DC Specification



| Description | Symbol | Min | Max | Unit | Note |
|-------------------------------|-----------------|-----|-----|------|------|
| LVDS Common mode Voltage | V _{CM} | 1.0 | 1.5 | V | - |
| LVDS Input Voltage Range | V _{IN} | 0.7 | 1.8 | ٧ | ÷ - |
| Change in common mode Voltage | △VCM | • | 250 | mV | - |

3-3-2. AC Specification



| Description | Symbol | Min | Max | Unit | Note |
|--|----------------------|------|----------------------------|------|--------------------------------|
| LVDS Differential Voltage | V_{TH} | 100 | 600 | mV | Tested with Differential Probe |
| LVD3 Differential Voltage | V_{TL} | -600 | -100 | mV | 3 |
| LVDS Clock to Data Skew | t _{SKEW} | - | (0.25*T _{clk})/7 | ps | - |
| LVDS Clock/DATA Rising/Falling time | t _{RF} | 260 | (0.3*T _{clk})/7 | ps | 2 |
| Effective time of LVDS | t _{eff} | ±360 | - | ps | - |
| LVDS Clock to Clock Skew (Even to Odd) | t _{SKEW_EO} | - | 1/7* T _{clk} | ps | - |

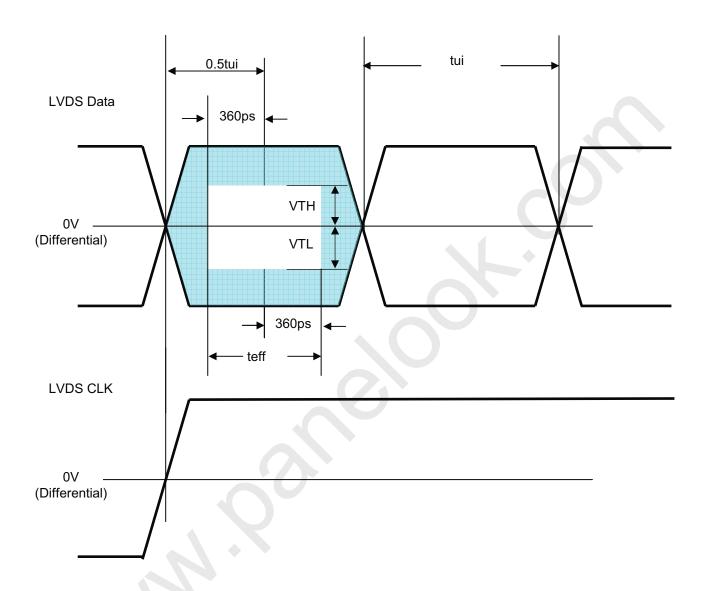
- Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.
 - 2. If \mathbf{t}_{RF} isn't enough, \mathbf{t}_{eff} should be meet the range.
 - 3. LVDS Differential Voltage is defined within t_{eff}

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^{*} This accumulated waveform is tested with differential probe

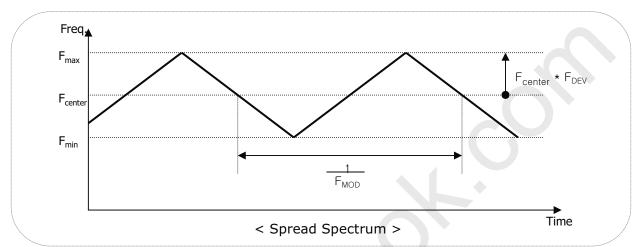




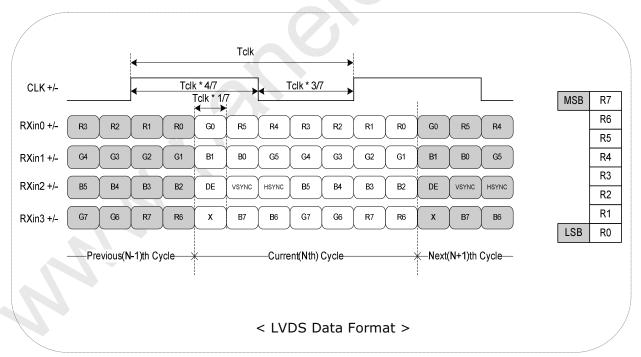
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Note 4:

This SSC specifications are just T-CON operation specification. In case of various system condition, the optimum setting value of SSC can be different. LGD recommend the SI should be adjust the SSC deviation and modulation frequency in order not to happen any kinds of defect phenomenon.



3-3-3. LVDS Data format



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Table 6. Required signal assignment for Flat Link(NS:DS90CF383) transmitter

| Pin # | Pin Name | Require Signal | Pin # | Pin Name | Require Signal |
|-------|----------|----------------------------|-------|-------------------------|--|
| 1 | VCC | Power Supply for TTL Input | 29 | GND | Ground pin for TTL |
| 2 | D5 | TTL Input (R7) | 30 | D26 | TTL Input (DE) |
| 3 | D6 | TTL Input (R5) | 31 | T _X CLKIN | TTL Level clock Input |
| 4 | D7 | TTL Input (G0) | 32 | PWR DWN | Power Down Input |
| 5 | GND | Ground pin for TTL | 33 | PLL GND | Ground pin for PLL |
| 6 | D8 | TTL Input (G1) | 34 | PLL VCC | Power Supply for PLL |
| 7 | D9 | TTL Input (G2) | 35 | PLL GND | Ground pin for PLL |
| 8 | D10 | TTL Input (G6) | 36 | LVDS GND | Ground pin for LVDS |
| 9 | VCC | Power Supply for TTL Input | 37 | TxOUT3+ | Positive LVDS differential data output 3 |
| 10 | D11 | TTL Input (G7) | 38 | TxOUT3- | Negative LVDS differential data output 3 |
| 11 | D12 | TTL Input (G3) | 39 | T _X CLKOUT+ | Positive LVDS differential clock output |
| 12 | D13 | TTL Input (G4) | 40 | T _X CLKOUT – | Negative LVDS differential clock output |
| 13 | GND | Ground pin for TTL | 41 | T _X OUT2+ | Positive LVDS differential data output 2 |
| 14 | D14 | TTL Input (G5) | 42 | T _X OUT2 – | Negative LVDS differential data output 2 |
| 15 | D15 | TTL Input (B0) | 43 | LVDS GND | Ground pin for LVDS |
| 16 | D16 | TTL Input (B6) | 44 | LVDS VCC | Power Supply for LVDS |
| 17 | VCC | Power Supply for TTL Input | 45 | T _X OUT1+ | Positive LVDS differential data output 1 |
| 18 | D17 | TTL Input (B7) | 46 | T _X OUT1 – | Negative LVDS differential data output 1 |
| 19 | D18 | TTL Input (B1) | 47 | T _X OUT0+ | Positive LVDS differential data output 0 |
| 20 | D19 | TTL Input (B2) | 48 | T _X OUT0 – | Negative LVDS differential data output 0 |
| 21 | GND | Ground pin for TTL Input | 49 | LVDS GND | Ground pin for LVDS |
| 22 | D20 | TTL Input (B3) | 50 | D27 | TTL Input (R6) |
| 23 | D21 | TTL Input (B4) | 51 | D0 | TTL Input (R0) |
| 24 | D22 | TTL Input (B5) | 52 | D1 | TTL Input (R1) |
| 25 | D23 | TTL Input (RSVD) | 53 | GND | Ground pin for TTL |
| 26 | VCC | Power Supply for TTL Input | 54 | D2 | TTL Input (R2) |
| 27 | D24 | TTL Input (HSYNC) | 55 | D3 | TTL Input (R3) |
| 28 | D25 | TTL Input (VSYNC) | 56 | D4 | TTL Input (R4) |

Notes: Refer to LVDS Transmitter Data Sheet for detail descriptions.

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3-4. Signal timing specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 7. Timing table (DE Only mode)

| ITE | М | Symbol | Min | Тур | Max | Unit | Note |
|------------|-------------------|--------|------------------|--------------|----------------|------|------|
| | Display Period | tHV | 1 | 1366 | 1 | tclk | |
| Horizontal | Blank | tнв | 90 | 162 | 410 | tclk | |
| | Total | tHP | 1456 | 1528 | 1776 | tclk | |
| | Display Period | tvv | - | 768 | | tHP | |
| Vertical | Blank | t∨в | 20 (126) | 22 (180) | 240 (295) | tHP | 1 |
| | Total | t∨P | 788 (894) | 790 (948) | 1008 (1063) | tHP | |

| | ITE | М | Symbol | Min | Тур | Max | Unit | Note |
|-----|---------|------------|--------|------------|------------|------------|------|--|
| | | DCLK | fclk | 63.0 | 72.4 | 80.0 | MHz | |
| | | Horizontal | fH | 45 | 47.4 | 55 | KHz | 2 |
| Fre | equency | Vertical | fv | 57 (47) | 60 (50) | 63 (53) | Hz | 2 NTSC : 57~63Hz (PAL : 47~53Hz) |

Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

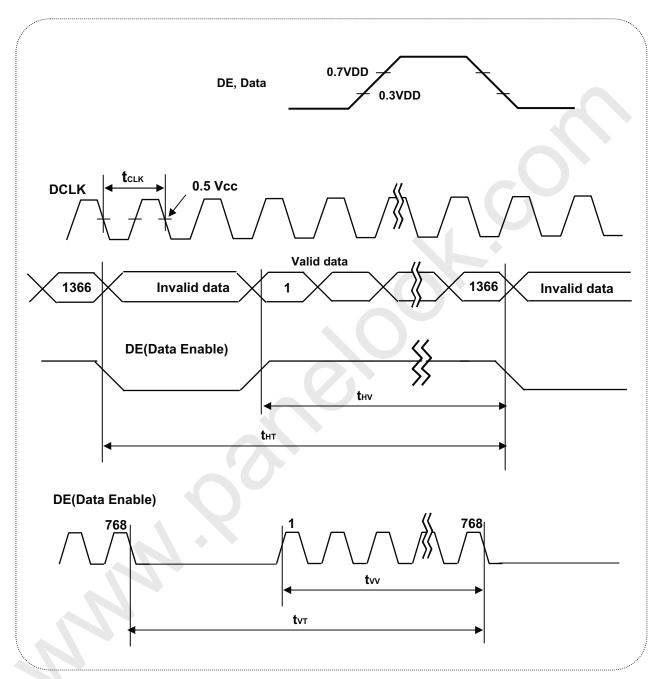
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
- X Timing should be set based on clock frequency.





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3-5. Signal timing waveforms



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3-6. Color input data reference

The brightness of each primary color (red,green and blue) is based on the 8bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. Color data reference

| | | | | | | | | | | | In | out | Сс | lor | Da | ata | | | | | | | | | |
|----------------|---|---------------------------------|---------------------------------|---------------------------------|----------------------------|----------------------------|---------------------------------|----------------------------|---------------------------------|---------------------------------|----------------------------|---------------------------------|----------------------------|----------------------------|----------------------------|---------------------------------|---------------------------------|----------------------------|---------------------------------|---------------------------------|---------------------------------|----------------------------|--------------------------------------|---------------------------------|--------------------------------------|
| | Color | N 4 | CD | | Re | ed | | | , D | N 4 | CD | | Gre | een | | | J, | Blue MSB LSB | | | | | | | |
| | | | SB R6 | R5 | R4 | R3 | R2 | LS R1 | R0 | | SB G6 | G5 | G4 | G3 | G2 | LS G1 | | _ | _ | В5 | В4 | В3 | В2 | _ | в В0 |
| Basic Color | Black Red (255) Green (255) Blue (255) Cyan Magenta Yellow White | 0 1 0 0 1 1 1 | 0 1 0 0 1 1 1 | 0 1 0 0 1 1 1 | 0 1 0 0 1 1 | 0 1 0 0 1 1 | 0 1 0 0 1 1 1 | 0 1 0 0 1 1 | 0 1 0 0 1 1 | 0 0 1 0 1 0 1 | 0 0 1 0 1 1 1 | 0 0 1 0 1 0 1 | 0 0 1 0 1 1 | 0 0 1 0 1 1 | 0 0 1 0 1 1 | 0 0 1 0 1 0 1 | 0 0 1 0 1 0 1 | 0 0 1 1 0 1 | 0 0 1 1 1 0 1 | 0 0 1 1 1 0 1 | 0 0 1 1 1 0 1 | 0 0 1 1 0 1 | 0 0 1 1 1 0 | 0 0 1 1 1 0 1 | 0 0 1 1 1 0 1 |
| Red | Red(000) Dark Red(001) Red(002) Red(253) Red(254) Red(255) Bright | 0 0 0 - 1 1 1 | 0 0 0 - 1 1 1 | 0 0 0 - 1 1 1 | 0 0 0 - 1 1 1 | 000111 | 0001111 | 001-011 | 0 1 0 - 1 0 1 | 00011000 | 00011000 | 000 000 | 00011000 | 00011000 | 00011000 | 00011000 | 00011000 | 000000 | 000000 | 000 000 | 00011000 | 000000 | 0 0 0 - 0 0 0 | 00011000 | 0 0 0 - 0 0 0 |
| Green | Green(000) Dark Green(001) Green(002) Green(253) Green(254) Green(255)Bright | 00011000 | 000 000 | 000 000 | 000 000 | 000 000 | 000 000 | 000 000 | 000 000 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 1 - 0 1 1 | 0 1 0 - 1 0 1 | 0 0 0 0 0 0 | 000 000 | 000 000 | 000 000 | 0 0 0 - 0 0 0 | 0 0 0 - 0 0 | 000 000 | 0 0 0 - 0 0 0 |
| Blue | Blue(000) Dark Blue(001) Blue(002) Blue(253) Blue(254) Blue(255) Bright | 000000 | 000 000 | 000000 | 000 000 | 000000 | 000 000 | 000 000 | 000 000 | 000 000 | 000 000 | 0 0 0 0 0 0 | 000 000 | 000 000 | 000000 | 000000 | 000 000 | 0 0 - - 1 1 | 0 0 0 - 1 1 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 - - 1 1 | 0 0 0 - - 1 1 1 | 0 1 - 0 1 1 | 0 1 0 - - 1 0 1 |

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3-7. Power sequence

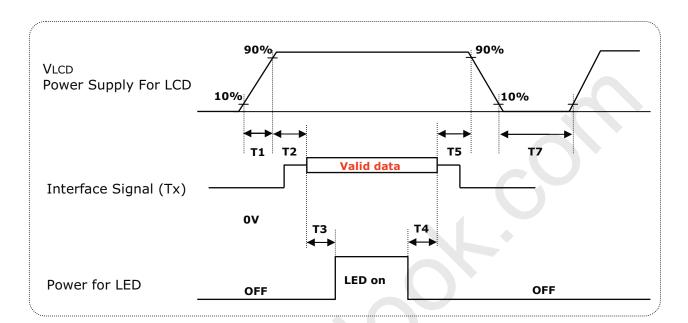


Table 9. Power sequence

| Parameter | | Values | | | | | | | | | |
|-----------|------|--------|-----|---------|--|--|--|--|--|--|--|
| Parameter | Min | Тур | Max | - Units | | | | | | | |
| T1 | 0.5 | - | 10 | ms | | | | | | | |
| T2 | 0.01 | - | 50 | ms | | | | | | | |
| Т3 | 500 | - | - | ms | | | | | | | |
| T4 | 200 | - | - | ms | | | | | | | |
| T5 | 0.01 | - | 50 | ms | | | | | | | |
| T7 | 1 | - | - | S | | | | | | | |

Notes

- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD $\rm V_{LCD}$ to $\rm 0V$.
- 3. Lamp power must be turn on after power supply for LCD an interface signal are valid.

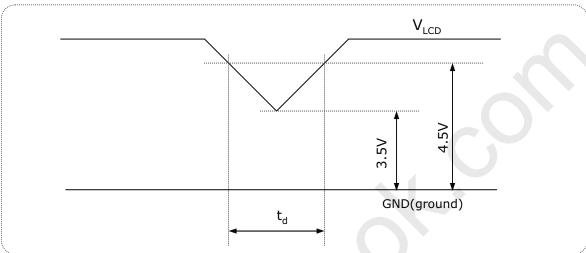




Product Specification

3-8. V_{LCD} Power dip condition

FIG. 6 Power dip condition



1) Dip condition

$$3.5V \le V_{LCD} < 4.5V$$
 , $t_d \le 20ms$

2)
$$V_{LCD}$$
< 3.5V

 $V_{\text{LCD}}\text{-dip}$ conditions should also follow the Power On/Off conditions for supply voltage.





Product Specification

4. Optical specification

Optical characteristics are determined after the unit has been 'ON' for 30 minutes in a dark environment at $25\,^{\circ}$ C.

Table 10. Optical characteristics

Ta= 25°C, V_{LCD} =5.0V, fV=60Hz f_{CLK} = 77.0MHz, Is=110mA

| Table 10. Optic | ai Cilai actei | ISUCS | Ta= 25°C, √ | $t_{LCD} = 5.0 \text{V}$, fV | =60Hz f _{CLK} = | 77.0MHz, I | s =110mA | |
|---------------------|---------------------|-----------------------|-----------------|-------------------------------|--------------------------|---------------|-------------------|---------------|
| Dawawaa | . | Cymahal | | Values | | Units | Natas | |
| Parame | ter | Symbol | Min | Тур | Max | Units | Notes | |
| Contrast Ratio | CR | 600 | 1000 | - | | 1 (PR-880) | | |
| Surface Luminance | e, white | L _{WH} | L _{WH} | 200 | 250 | - | cd/m ² | 2 (PR-880) |
| Luminance Variation | on | δ _{WHITE} 9P | 75 | | | % | 3 (PR-880) | |
| Docnanca Timo | Rise Time | Tr _R | - | 1.1 | 2.6 | ms | 4 | |
| Response Time | Decay Time | Tr _D | - | 3.9 | 7.4 | ms | (RD80S) | |
| | DED | Rx | | 0.648 | | | | |
| | RED | Ry | | 0.329 | | | | |
| | GREEN | Gx | | 0.305 | | | | |
| Color Coordinates | GREEN | Gy | Тур | 0.631 | Тур | | (PR-650) | |
| [CIE1931] | BLUE | Bx | -0.03 | 0.154 | +0.03 | | - (FK-030) | |
| | BLOL | Ву | | 0.062 | | | | |
| | WHITE | Wx | | 0.313 | | | | |
| | VVIIII | Wy | | 0.329 | | | | |
| Viewing Angle (CR | >5) | | | | | | | |
| x axis, ri | ght(φ=0°) | θr | 75 | 88 | | Degree | | |
| x axis, le | ft (ϕ =180°) | θΙ | 75 | 88 | | | | |
| y axis, u | ρ (φ=90°) | θu | 70 | 85 | | | | |
| y axis, d | lown (φ=270°) | θd | 70 | 85 | | | 5 | |
| Viewing Angle (CR | >10) | | | | | | (PR-880) | |
| x axis, ri | x axis, right(φ=0°) | | 70 | 85 | | Degree | | |
| x axis, le | ft (ϕ =180°) | θΙ | 70 | 85 | | | | |
| y axis, u | y axis, up (φ=90°) | | 60 | 75 | | | | |
| y axis, do | own (φ=270°) | θd | 70 | 85 | | | | |
| Crosstalk | | | | | 1.5 | % | (PR880) | |

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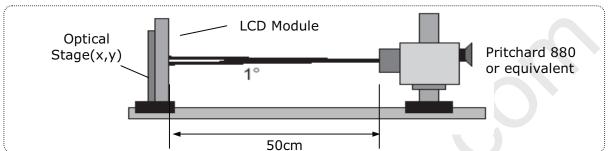


Product Specification

The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 $^{\circ}.$

FIG. 7 presents additional information concerning the measurement equipment and method.

FIG. 7 Optical characteristic measurement equipment and method



Notes:

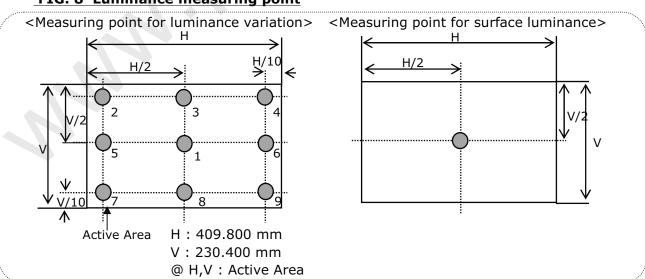
 $1. \ Contrast \ ratio (CR) \ is \ defined \ mathematically \ as : It \ is \ measured \ at \ center \ point (1)$

- 2. Surface luminance is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 8.
- 3. The variation in surface luminance , δ $_{\text{WHITE}}$ is defined as

$$\delta_{\text{WHITE}} = \frac{\text{Minimum (P1,P2P9)}}{\text{Maximum (P1,P2P9)}}$$

For more information see Figure 8.

FIG. 8 Luminance measuring point



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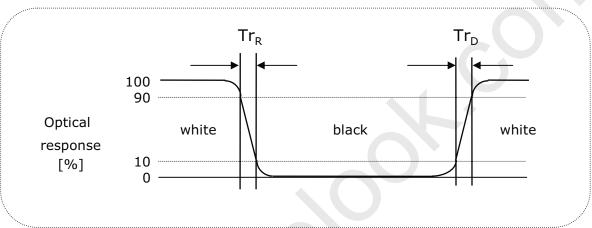
Notes:

4. Response time is the time required for the display to transition from black to white (Decay Time, Tr_D) and from white to black (Rise Time, Tr_R)

The sampling rate is 2,500 sample/sec. For additional information see FIG. 9.

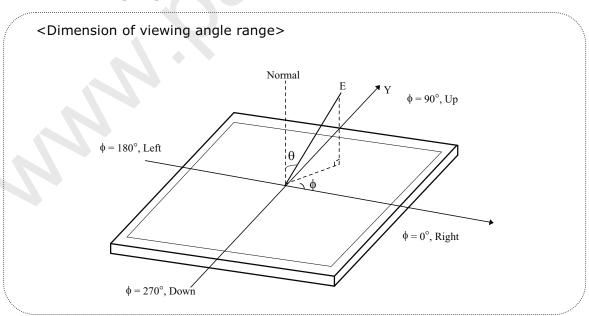
The response time is defined as the following figure and shall be measured by switching the input signal for each gray to gray.

FIG. 9 Response time



5. Viewing angle is the angle at which the contrast ratio is greater than 10 or 5. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG. 10 .

FIG. 10 Viewing angle



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Product Specification

Notes:

6. Gray scale specification

Table 11. Gray scale

| Gray level | Luminance [%] (Typ) | | | | | |
|------------|---------------------|--|--|--|--|--|
| LO | 0.10 | | | | | |
| L31 | 0.97 | | | | | |
| L63 | 4.43 | | | | | |
| L95 | 11.06 | | | | | |
| L127 | 21.13 | | | | | |
| L159 | 34.88 | | | | | |
| L191 | 54.53 | | | | | |
| L223 | 77.00 | | | | | |
| L255 | 100 | | | | | |





Product Specification

5. Mechanical characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Table 12. Mechanical characteristics

| | Horizontal | 430.4 mm | | | | |
|-----------------------|--|------------|--|--|--|--|
| Outline dimension | Vertical | 254.6 mm | | | | |
| | Depth | 9.70 mm | | | | |
| Donal aven | Horizontal | 413.4 mm | | | | |
| Bezel area | Vertical | 234.0 mm | | | | |
| A ativo diantava avaa | Horizontal | 409.800 mm | | | | |
| Active display area | Vertical | 230.400 mm | | | | |
| Weight | 1,000g (Typ.) 1,050g (Max) | | | | | |
| Surface treatment | Hard coating(3H) Anti-glare treatment of the front polarizer | | | | | |

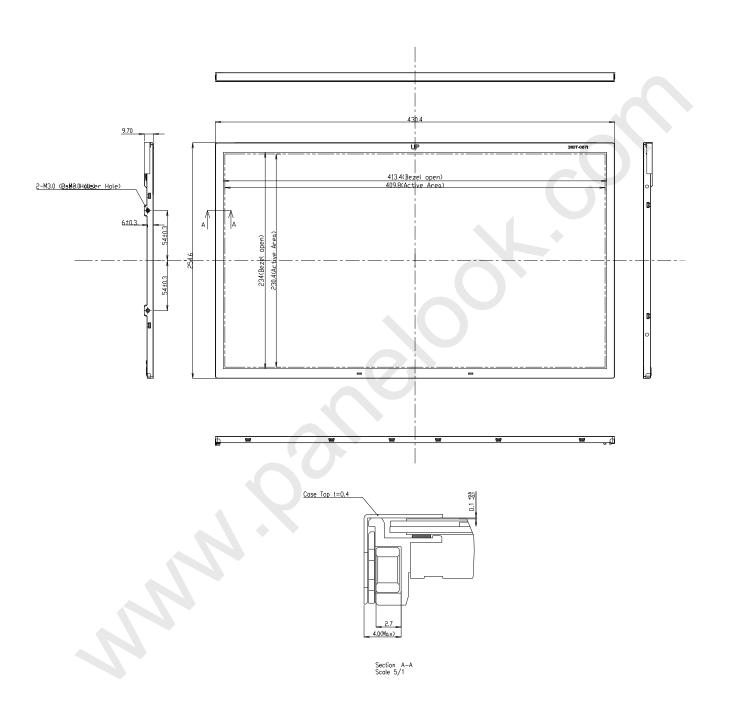
Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.





Product Specification

<FRONT VIEW>



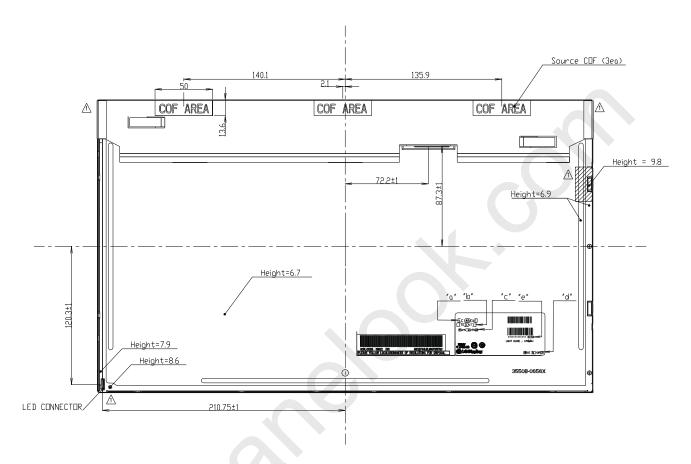
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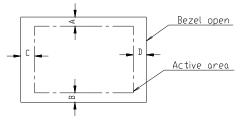


Product Specification

<REAR VIEW>







6. Unspecified tolerances to be $\pm 0.5 mm$ 7. The CDF area is weak & sensitive, So, please don't press the CDF area.

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Product Specification

6. Reliability

Table 13. Environment test conditions

| No | Test Item | Condition | | | | | |
|----|---------------------------------------|--|--|--|--|--|--|
| 1 | High temperature storage test | Ta= 60°C 240hrs | | | | | |
| 2 | Low temperature storage test | Ta= -20°C 240hrs | | | | | |
| 3 | High temperature operation test | Ta= 50°C 50%RH 240hrs | | | | | |
| 4 | Low temperature operation test | Ta= 0°C 240hrs | | | | | |
| 5 | Vibration test (non-operating) | Wave form: random Vibration level: 1.0GRMS Bandwidth: 10-300Hz Duration: X,Y,Z, 30 min One time each direction | | | | | |
| 6 | Shock test (non-operating) | Shock level: 120G Waveform: half sine wave, 2msec Direction: $\pm X$, $\pm Y$, $\pm Z$ One time each direction | | | | | |
| 7 | Altitude operating storage / shipment | 0 - 10,000 feet(3,048m) 0 - 40,000 feet(12,192m) | | | | | |

[{] Result evaluation criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.





Product Specification

7. International standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1:2006 + A11:2009,
 European Committee for Electrotechnical Standardization(CENELEC).
 Information Technology Equipment Safety Part 1: General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1: 2001 Embedded LED Power (Class 1M)

2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open. Do not open while operating.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement."

 International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003





Product Specification

8. Packing

8-1. Designation of lot mark

a) Lot mark

| А | В | С | D | Е | F | G | Н | I | J | К | L | М | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|--|
|---|---|---|---|---|---|---|---|---|---|---|---|---|--|

A,B,C : Size (Inch)

E: Month

D: Year

F ~ M : Serial No.

Note:

1. Year

| Year | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 |
|------|------|------|------|------|------|------|------|------|------|------|
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 |

2. Month

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | Α | В | С |

b) Location of lot mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing form

a) Package quantity in one box: 16 pcs

b) Box size : 365 mm X 315 mm X 492 mm





Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the Module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 mV$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw (if not, it causes metal foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.







Global LCD Panel Exchange Center

LC185EXE Liquid Crystal Display

Product Specification

9-3. Electrostatic discharge control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for strong light exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.

9-6. Handling precautions for protection film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.