# SPECIFICATION FOR APPROVAL

(	)	Preliminary	Specification
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( ● ) Final Specification

This Product must be used for a TV Application This is not designed for the public display.

Title 32.0" WXGA TFT LCD	
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BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC320WXN
SUFFIX	SAA1 (RoHS Verified)

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE						
Please return 1 conv for your confirmation with							
	Please return 1 copy for your confirmation with your signature and comments.						

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## **RECORD OF REVISIONS**

Revision No.	Revision Date	Page	Description
1.0	Jan,07,2008	-	Final Specification

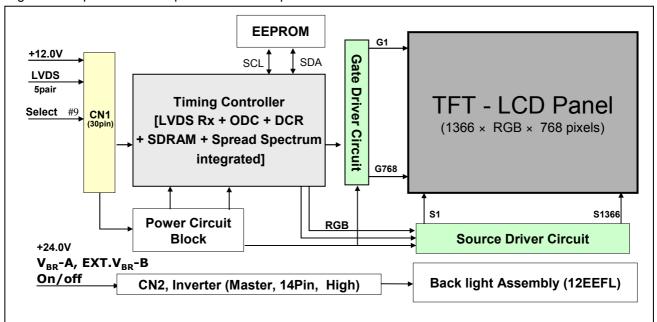
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#### 1. General Description

The LC320WXN is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 31.51 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 1-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



#### **General Features**

Active Screen Size	31.51 inches(800.4mm) diagonal
Outline Dimension	760.0 mm(H) x 450.0 mm(V) x 48.0 mm(D) (Typ.)
Pixel Pitch	170.25/ <sup>∞</sup> x 510.75/ <sup>∞</sup> x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8bit, 16,7 M colors
Luminance, White	500 cd/m² (Center 1 point) (Typ.)
Viewing Angle (CR>10)	Viewing angle free ( R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 87.6Watt (Typ.) (Logic=3.3 W, Lamp= 84W [VBR-A=1.65V] )
Weight	6,150g(Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), anti-glare treatment of the front polarizer (Haze 13%)

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### 2. Absolute Maximum Ratings

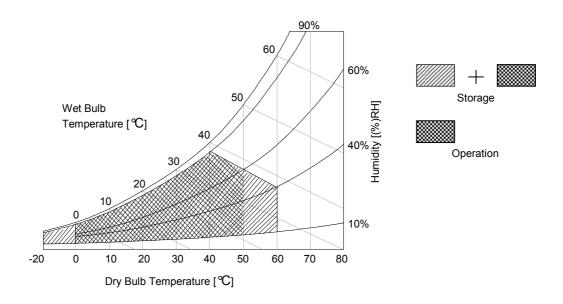
The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

**Table 1. ABSOLUTE MAXIMUM RATINGS** 

Doromotor		Cymhol	Symbol Value Min Max		Lloit	Remark	
Га	Parameter				Unit		
Power Input	LCM	VLCD	-0.3	+14.0	VDC	at 25 ± 2 ℃	
Voltage	Backlight inverter	VBL	-0.3	+27.0	VDC		
ON/OFF Conf	ON/OFF Control Voltage		-0.3	+5.5	VDC		
Brightness Co	ontrol Voltage	VBR	0	+5.0	VDC		
Operating Ter	mperature	Тор	0	+50	$^{\circ}$		
Storage Tem	Storage Temperature		-20	+60	℃	Note 1.2	
Operating Ambient Humidity		Нор	10	90	%RH	Note 1,2	
Storage Humidity		Hst	10	90	%RH		

Notes: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 ℃ Max. and no condensation of water.

2. Gravity mura can be guaranteed under 40 °C condition.



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### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit.

The other Is used for the EEFL backlight and inverter circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note	
rarameter	Min		Тур	Max		Offic
Circuit :						
Power Input Voltage	V <sub>LCD</sub>	11.4	12.0	12.6	V <sub>DC</sub>	
Power Input Current	I <sub>LCD</sub>	-	270	350	mA	1
Power Input Current		-	350	450	mA	2
Power Consumption	P <sub>LCD</sub>	-	3.3	4.2	Watt	1
Rush current	I <sub>RUSH</sub>	-	-	3.0	Α	3

Notes : 1. The specified current and power consumption are under the  $V_{LCD}$ =12.0V, 25  $\pm$  2 °C,  $f_V$ =60Hz condition whereas mosaic pattern(8 x 6) is displayed and  $f_V$  is the frame frequency.

2. The current is specified at full white pattern.

White: 255 Gray

3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).

Black : 0 Gray



White: 255 Gray

Mosaic Pattern(8 x 6)

Full White pattern

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Table 3. ELECTRICAL CHARACTERISTICS

Parameter			Cymbol	Symbol Values			Unit	Notes
			Syllibol	Min	Тур	Max	Offic	Notes
Inverter:								
Power Supply Input Voltage			VBL	22.8	24.0	25.2	Vdc	1
Power Supply Inpu	t Voltage Ripp	le		-	-	0.5	Vp-p	1
	After Aging		IBL A	-	3.5	3.85	Α	V <sub>BR-A</sub> = 1.65V 1
Power Supply	Aiter Aging		IDL_A	-	3.6	4.0	Α	V <sub>BR-A</sub> = 3.3V 1
Input Current	Before Agir	na	IBL_B	-	4.0	4.5	Α	V <sub>BR-A</sub> = 1.65V 2
	Belore Agii	ig	IDL_D	-	4.5	5.0	Α	V <sub>BR-A</sub> = 3.3V 2
Power Supply Input Current (In-Rush)			Irush	-	-	6.3	А	V <sub>BL</sub> = 22.8V Ext V <sub>BR-B</sub> = 100% V <sub>BR-A</sub> = 1.65V
Power Consumptio	n		PBL	-	84	92.4	W	V <sub>BR-A</sub> = 1.65V 1
	Brightness Adjust		VBR-A	0.0	1.65	3.3	Vdc	
	On/Off	On	V on	2.5	-	5.0	Vdc	
	On/On	Off	V off	-0.3	0.0	8.0	Vdc	
Input Voltage for Control System	Brightness A	Adjust	Ext. VBR-B	30	-	100	%	On Duty
Signals	PWM Frequency for NTSC & PAL		NTSC/PAL		100/120		Hz	5
	Pulse Duty Level(PWM) (Burst mode)		High Level	2.5	-	5.0	Vdc	HIGH: Lamp on
			Low Level	0.0	-	0.8	Vdc	LOW:Lamp off
Lamp:								
Discharge Stabilization Time			Ts			3	min	3
Life Time		_		50,000			Hrs	4

#### Notes:

- 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25± 2 °C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (VBR-A : 1.65V & ExtVBR-B : 100%), it is total power consumption.
  - The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is  $24.0V \pm 5\%$ .
- 2. Electrical characteristics are determined within 30 minutes at 25± 2℃. The specified currents are under the typical supply Input voltage 24V.
- 3. The brightness of the lamp after lighted for 5minutes is defined as 100%.
  - TS is the time required for the brightness of the center of the lamp to be not less than 95% at typical current. The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
- 4. Specified Values are for a single lamp which is aligned horizontally.
  - The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (VBR-A: 1.65V & ExtVBR-B:100%), on condition of continuous operating at 25± 2°C
- 5. LPL recommend that the PWM freq. is synchronized with Two times harmonic of Vsync signal of system.
- 6. The duration of rush current is about 20ms.

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#### 3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 30-pin connector is used for the module electronics and One connectors(14-pin) are used for the integral backlight system.

#### 3-2-1. LCD Module

- LCD Connector(CN1): FI-X30SSL-HF (Manufactured by JAE) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
7	GND	Ground	
9	Select	Select LVDS Data format	1
10	NC	NC	
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	NC	NC	
28	NC	NC	
29	GND	Ground	
30	GND	Ground	2

- Notes: 1. The pin no 9 is an option pin for DISM or LG format. ( VESA Format = "GND" / JEIDA Format = "VCC")
  Please refer to Appendix for further details.
  - 2. The pin no 30 is LCD Test option.

    LCM operates "AGP" (Auto Generation Pattern) or "NSB" (No Signal Black) is case that LVDS signals are out of frequency or abnormal condition in spite of 12 yell power supply

are out of frequency or abnormal condition in spite of 12 volt power supply. LPL recommends "NSB". ( AGP : "VCC" or "OPEN" / NSB : "GND" )

- 3. All GND (ground) pins should be connected together, which should be also connected to the LCD module's metal frame.
- 4. All VLCD (power input) pins should be connected together.
- 5. Input Level of LVDS signal is based on the IEA 664 Standard.

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#### 3-2-2. Backlight Inverter

Master

-Inverter Connector: 20022WR-14B1

(manufactured by Yeon-Ho) or Equivalent

- Mating Connector: PHR-14 or Equivalent

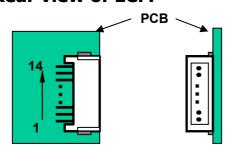
**Table 5. INVERTER CONNECTOR PIN CONFIGULATION** 

Pin No	Symbol	Description	Master	Note	
1	VBL	Power Supply +24.0V	VBL		
2	VBL	Power Supply +24.0V	VBL		
3	VBL	Power Supply +24.0V	VBL		
4	VBL	Power Supply +24.0V	VBL		
5	VBL	Power Supply +24.0V	VBL		
6	GND	Backlight Ground	GND		
7	GND	Backlight Ground	GND		
8	GND	Backlight Ground	GND	1	
9	GND	Backlight Ground	GND		
10	GND	Backlight Ground	GND		
11	VBR-A	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	VBR-A	2,3	
12	Von/off	0.0V ~ 5.0V	On/Off	3, Open/High for B/L on as default	
13	EXT. VBR-B	Burst Dimming Control PWM signal input	External PWM	4	
14	GND	POWER GND	GND	5	

Notes: 1. GND should be connected to the LCD module's metal frame.

- 2. Minimum Brightness: VBR-A = 0.0V Maximum Brightness: VBR-A = 3.3V "OPEN": VBR-A = 1.65V
- 3. Rising Edge: Lamp "ON" / Falling Edge: Lamp "OFF"
- 4. Pin#13 can be opened. (if Pin #13 is open, Ext VBR-B is 100%)
- 5. Pin#14 can be opened. (GND or NC)
- 6. Each impedance of pin #11,12 and 13 is 147[ $k\Omega$ ], 30[ $k\Omega$ ], 55[ $k\Omega$ ]

### **♦** Rear view of LCM



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#### 3-3. Signal Timing Specifications

Table 6-1 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6-1. TIMING TABLE for NTSC (DE Only

ı	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	tHV	-	1366	-	tclk	
Horizontal	Blank	tHB	thp-thv	162	thp-thv	tclk	
	Total	tHP	1456	1528	1920	tclk	
	Display Period	tVV	-	768	-	Lines	
Vertical	Blank	tVB	tvp-tvv	22	tvp-tvv	Lines	
	Total	tVP	776	790	1063	Lines	

TI	ITEM		Min	Тур	Max	Unit	Note
	DCLK	fCLK	63	72.4	80	MHz	
Frequency	Horizontal	fH	45	47.4	50	KHz	
	Vertical	fV	57	60	63	Hz	

Table 6-2 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6-2. TIMING TABLE for PAL (DE Only Mode)

ı	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	tHV	-	1366	-	tclk	
Horizontal	Blank	tHB	thp-thv	162	thp-thv	tclk	
	Total	tHP	1456	1528	1920	tclk	
	Display Period	tVV	-	768	-	Lines	
Vertical	Blank	tVB	tvp-tvv	180	tvp-tvv	Lines	
	Total	tVP	894	948	1008	Lines	

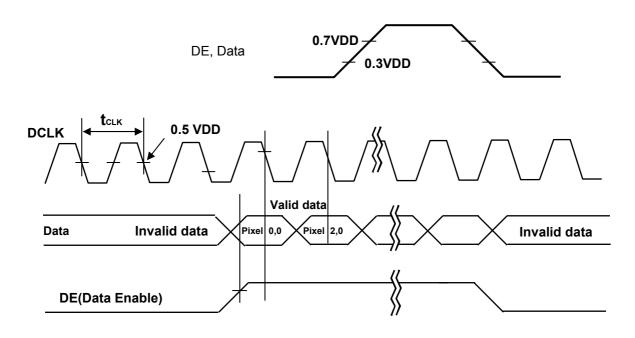
דו	ЕМ	Symbol	Min	Тур	Max	Unit	Note
	DCLK	fCLK	63	72.4	80	MHz	
Frequency	Horizontal	fH	45	47.4	50	KHz	
	Vertical	fV	47	50	53	Hz	

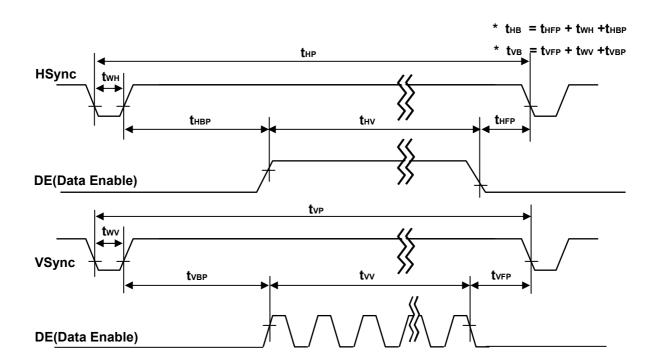
Note: The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode).

The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

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### 3-4. Signal Timing Waveforms





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#### 3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

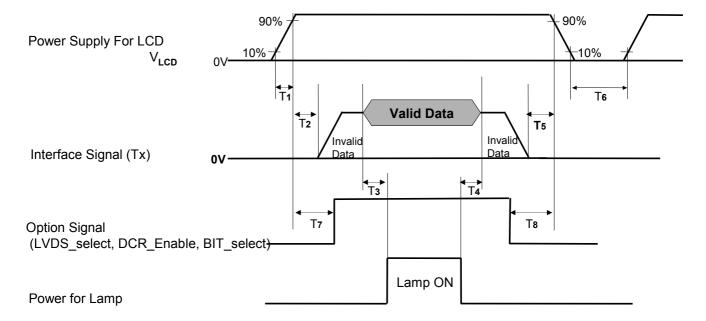
Table 7. COLOR DATA REFERENCE

													Inpu	ıt Co	olor	Data	а									
	Color					RE	D							GRE	ΕN	l						BL	UE			
			MS							$\dashv$	MS								MS							SB
									R1 F	₹0							G1	G0						B2		B0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																										
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) [	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN										Î																
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) D	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE										ĺ																
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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#### 3-6. Power Sequence

#### 3-6-1. LCD Driving circuit



**Table 8. POWER SEQUENCE** 

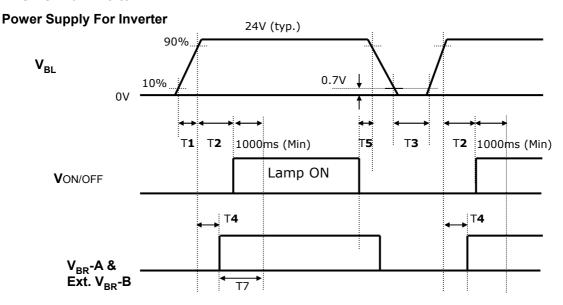
Devementes		Unit	Notes			
Parameter	Min	Тур	Max	Unit	Notes	
T1	0.5	-	20	ms		
T2	0	-	-	ms	4	
Т3	200	-	-	ms	3	
T4	200	-	-	ms	3	
T5	0	-	-	ms		
Т6	2.0	-	-	s	5	
T7	0	-	T2	ms	4	
Т8	0	-	-	ms	4	

Note: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply  $V_{LCD}$  to 0V.
- 3. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V<sub>LCD</sub>), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
- 5. T6 should be measured after the Module has been fully discharged between power off and on period.

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#### 3-6-2. On/Off for Inverter



#### 3-6-3. Deep condition for Inverter

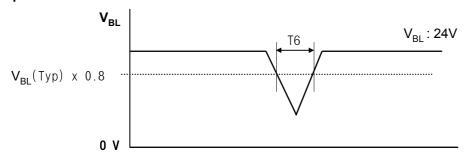


Table 9. Power Sequence for Inverter

Dawanaatau		Values		Lleite	Domarko		
Parameter	Min	Тур	Max	Units	Remarks		
T1	20	-	-	ms	1		
T2	500	-	-	ms			
T3	200	-	-	ms	2		
T4	0		-	ms			
T5	10	-	-	ms			
T6	-	-	10	ms	V <sub>BL</sub> (Typ) x 0.8		
T7	1000	-	-	ms	3		

Note: 1. T1 describes rising time of 0V to 24V and is not applied at restarting time.

- 2. T4(max) is less than T2.
- 3. For 1 second after lamp on (T7),  $V_{BR}$ -A should be typical 1.65V and Ext.  $V_{BR}$ -B should be 100%

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### 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and for 30 minutes in a dark environment at  $25\pm2\,^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

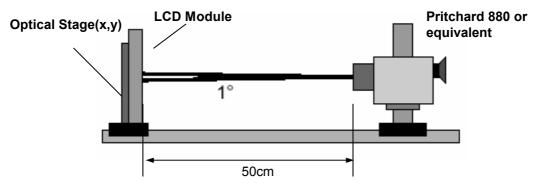


FIG. 1 Optical Characteristic Measurement Equipment and Method

#### **Table 10. OPTICAL CHARACTERISTICS**

Ta= 25± 2 °C, V<sub>LCD</sub>=12.0V, fv=60Hz, Dclk=72.4MHz, VBR-A =1.65V, Ext. VBR-B =100%

				Value			
Par	ameter	Symbol	Min	Тур	Max	Unit	Note
Contrast Ratio		CR	800	1100	-		1
Surface Lumina	nce, white	L <sub>WH</sub>	400	500		cd/m <sup>2</sup>	2
Luminance Vari	ation	δ <sub>WHITE</sub> 5P	-	-	1.3		3
Response Time		G to G	-	8	12	ms	4
	DED	Rx		0.636			
	RED	Ry		0.335			
	GREEN	Gx		0.291	Typ +0.03		
Color Coordinate		Gy	Тур	0.613			
[CIE1931]	BLUE	Bx	-0.03	0.146			
	BLUE	Ву		0.061			
	WHITE	Wx		0.279			
	VVIIIE	Wy		0.292			
Viewing Angle (	CR>10)						
x a	axis, right(φ=0°)	θr	89	-	-		
ха	axis, left (φ=180°)	θΙ	89	-	-		_
у а	y axis, up (φ=90°)		89	-	-	degree	5
y axis, down (φ=270°)		θd	89	-	-		
Gray Scale				2.2			6

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Notes: 1. Contrast Ratio(CR) is defined mathematically as:

CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)

Surface Luminance at position n with all white pixels

Surface Luminance at position n with all black pixels

n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.

- 2. Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at 25± 2℃. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance ,  $\delta$  WHITE is defined as :  $\delta \, \text{WHITE(5P)} = \text{Maximum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L$

Where  $L_{on1}$  to  $L_{on5}$  are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time,  $Tr_R$ ) and from G(M) to G(N) (Decay Time,  $Tr_D$ ). For additional information see the FIG. 3. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 6. Gray scale specification
  Gamma Value is approximately 2.2. For more information, see the Table 11.

**Table 11. GRAY SCALE SPECIFICATION** 

Gray Level	Luminance [%] (Typ.)
LO	0.10
L15	0.32
L31	1.10
L47	2.60
L63	4.90
L79	8.10
L95	12.1
L111	16.7
L127	21.6
L143	28.0
L159	35.4
L175	43.9
L191	53.3
L207	64.1
L223	75.8
L239	88.0
L255	100

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Measuring point for surface luminance & measuring point for luminance variation.

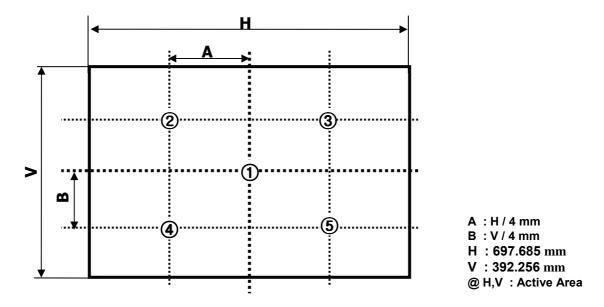


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

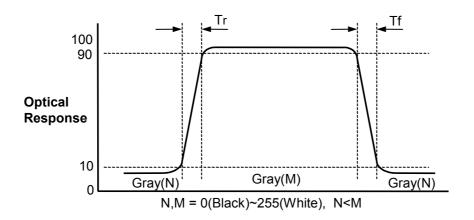


FIG. 3 Response Time

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### Dimension of viewing angle range

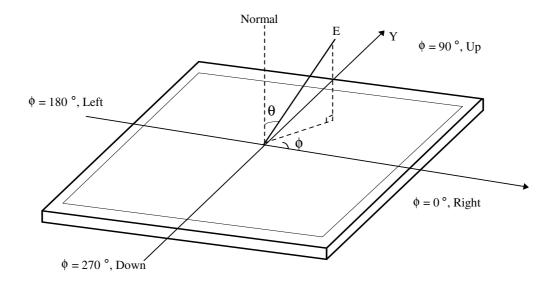


FIG. 4 Viewing Angle

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#### 5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

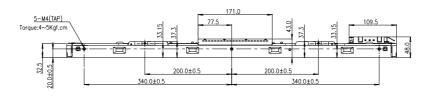
**Table 12. MECHANICAL CHARACTERISTICS** 

Item		Value			
	Horizontal	760.0mm			
Outline Dimension	Vertical	450.0 mm			
	Depth	48.0 mm			
Down Area	Horizontal	703.8mm			
Bezel Area	Vertical	398.4mm			
Active Dieplay Area	Horizontal	697.685mm			
Active Display Area	Vertical	392.256mm			
Weight	6,150 g(Typ.), 6,770g(Max)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer(13%)				

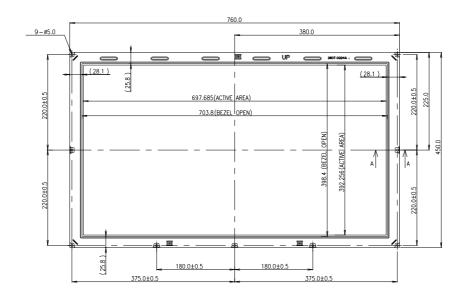
Note: 1.Please refer to a mechanic drawing in terms of tolerance at the next page.

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#### <FRONT VIEW>

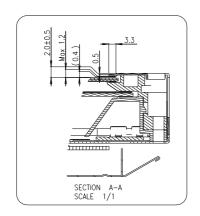




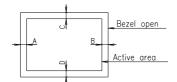






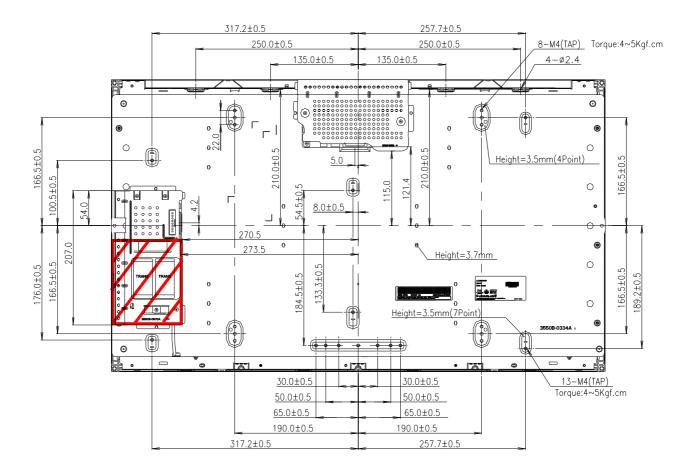


- NOTES
  1. Unspecified tolerances are to be ±1.0mm.
  2. Tilt and partial disposition tolerance of display area are as following.
  (1) X-Direction: IA-BI ≤ 1.5mm
  (2) Y-Direction: IC-DI ≤ 1.5mm



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#### <REAR VIEW>



Notes: It should be recommended that any exterior materials do not go passing up the red area slanted. (For example, electrical cable, system board, etc.). Otherwise, it could cause that abnormal display happens.

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# 6. Reliability

**Table 13. ENVIRONMENT TEST CONDITION** 

No.	Test Item	Condition		
1	High temperature storage test	Ta= 60 ℃ 75%RH 240h		
2	Low temperature storage test	Ta= -20℃ 240h		
3	High temperature operation test	Ta= 50℃ 60%RH 240h		
4	Low temperature operation test	Ta= 0 ℃ 240h		
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min One time each direction		
6	Shock test (non-operating)	Shock level : $100G$ Waveform : half sine wave, $2ms$ Direction : $\pm X$ , $\pm Y$ , $\pm Z$ One time each direction		
7	ESD test	Condition : 150pF, 330 ohm Case , air Evaluation : ± 15kV		
8	Humidity condition Operation Ta= 40 ℃ 90%RH 240h			
9	Altitude operating storage / shipment	0 - 14,000 feet(4267.2m) 0 - 40,000 feet(12192m)		

Note: Before and after Reliability test, LCM should be operated with normal function.

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#### 7. International Standards

#### 7-1. Safety

a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc.,

Standard for Audio, Video and Similar Electronic Apparatus.

b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association,

Standard for Audio, Video and Similar Electronic Apparatus.

c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002,

Safety requirements for Audio, Video and Similar Electronic Apparatus..

#### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
  - CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
  - EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

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### 8. Packing

### 8-1. Designation of Lot Mark

a) Lot Mark

АВС	D E	F G	н	J K	L	М
-----	-----	-----	---	-----	---	---

A,B,C: SIZE(INCH) D: YEAR

E: MONTH  $F \sim M$ : SERIAL NO.

#### Note

#### 1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

#### b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

### 8-2. Packing Form

a) LCM quantity in one pallet: 12 pcs

b) Pallet Size: 1030mm X 870mm X 740mm

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#### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

#### 9-1. Mounting Precautions

- (1)You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
  Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer. \* Panel push force can be guaranteed under 5kgf / \$\phi\$10mm
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

  And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.
- 11) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic.

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#### 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

#### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

#### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5 ℃ and 35 ℃ at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.

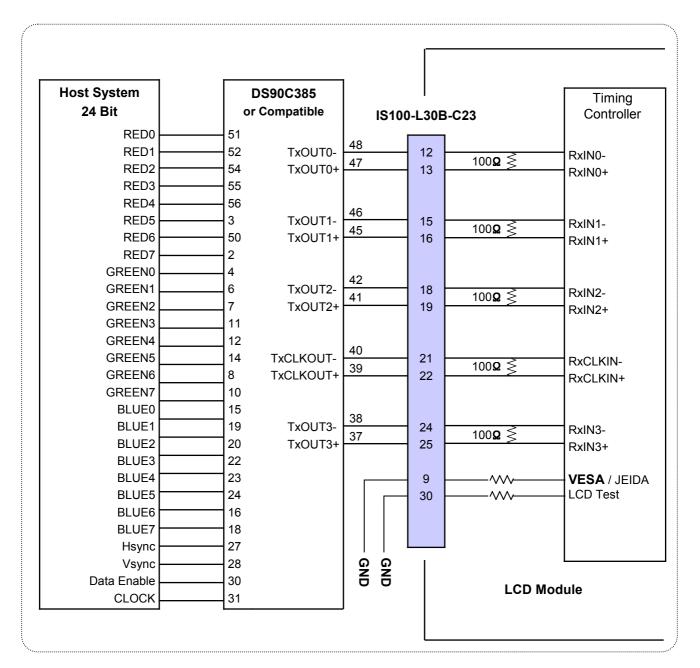
#### 9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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#### # APPENDIX-I-1

■ Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="L")



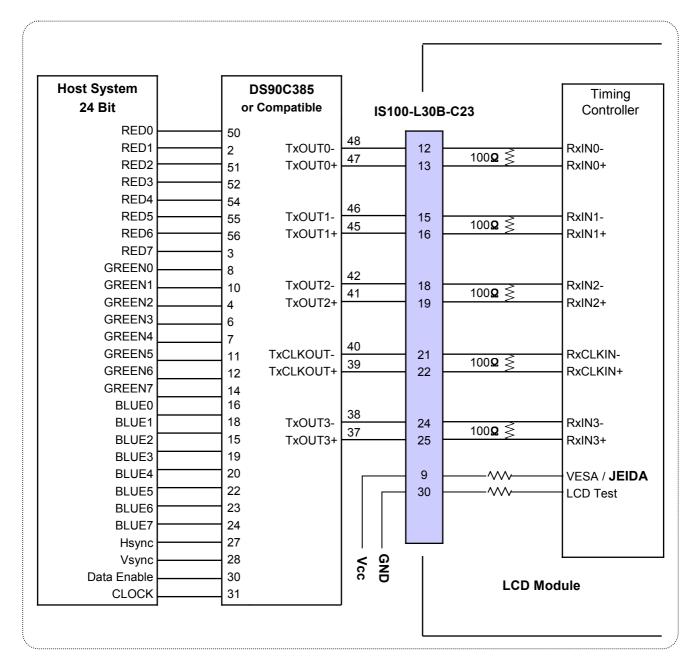
#### Notes:

- 1. The LCD module uses a 100 Ohm( $\Omega$ ) resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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#### # APPENDIX-I-2

■ Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="H")



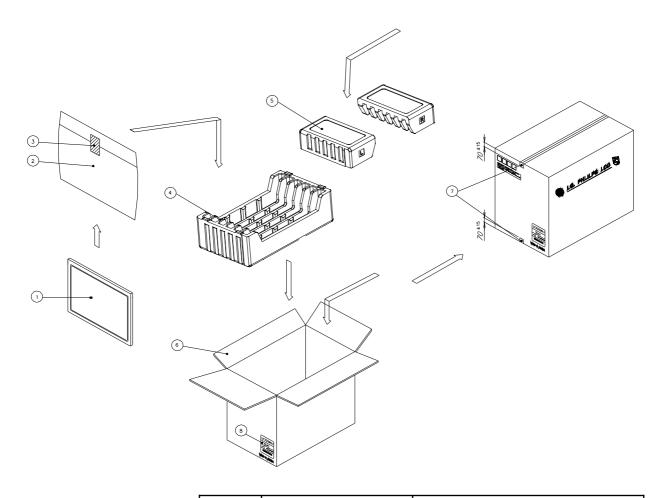
#### Notes:

- 1. The LCD module uses a 100 Ohm( $\Omega$ ) resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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### # APPENDIX- II-1

# ■ LC320WXN—SAA1 Packing Ass'y

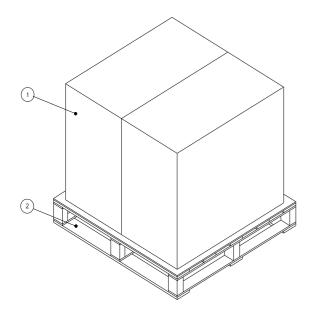


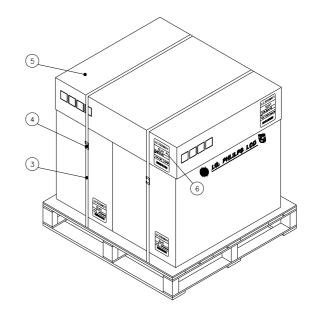
NO.	DESCRIPTION	MATERIAL
1	LCD MODULE	
2	BAG	AL
3	TAPE	MASKING 20MM X 50M
4	PACKING, BOTTOM	EPS
5	PACKING, TOP R_L	EPS
6	BOX	PAPER_DW3
7	TAPE	OPP 70MMX300M
8	LABEL	YUPO PAPER 100X100

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### # APPENDIX- II-2

### ■ LC320WXN-SAA1 Pallet Ass'y





Box quantity per pallet: 2ea

Pallet size: L1030 x W870 x H740

Pallet gross weight: 113.0kg

NO.	DESCRIPTION	MATERIAL
1	PACKING ASS'Y	
2	PALLET	Plywood
3	BAND	PP
4	CLIP, BAND	STEEL
5	ANGLE, Cover	PAPER (DW3)
6	LABEL	PAPER

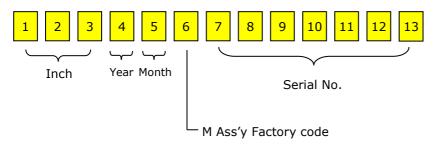
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#### # APPENDIX- III

#### ■ LCM Label



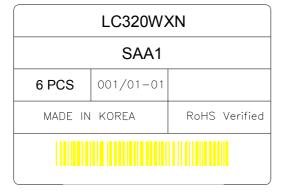
### ■ Serial No. (See CAS 26page for more information)



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#### # APPENDIX- IV

### ■ Box Label



### ■ Pallet Label

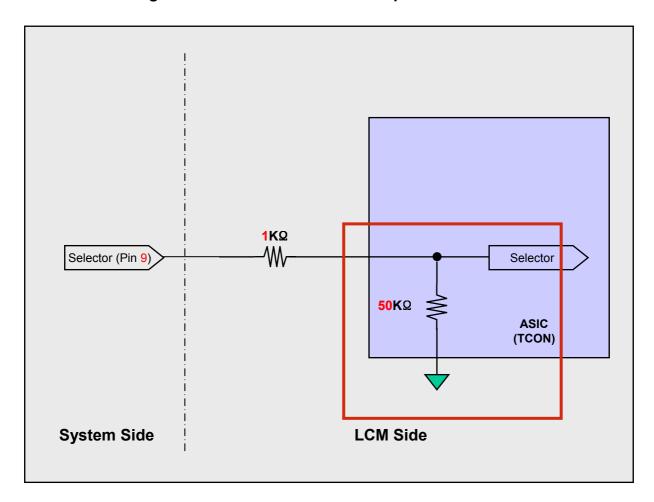


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#### # APPENDIX- V

■ Option Pin Circuit Block Diagram

### Circuit Block Diagram of LVDS Format Selection pin



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#### # APPENDIX- VI

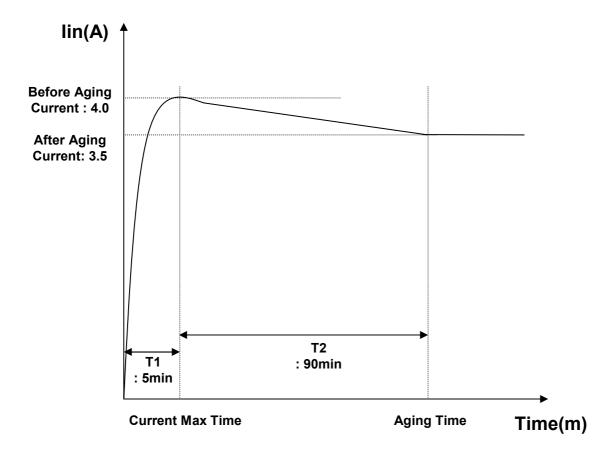
#### ■ Inverter Input Current(Design for Power Supply)

This is only the reference data of Inverter input current for LC320WXN-SAA1 model.

1. Model: LC320WXN-SAA1

2. Test condition : Vin =24V , Ext.  $V_{BR\_B}~:100\%$  ,  $V_{BR\_A}$  : 1.65V, At 25  $^{\circ}\mathrm{C}$ 

3. Equipment : Oscilloscope (Tektronix : TDS5054) , AC/DC Current Probe(TCP312)



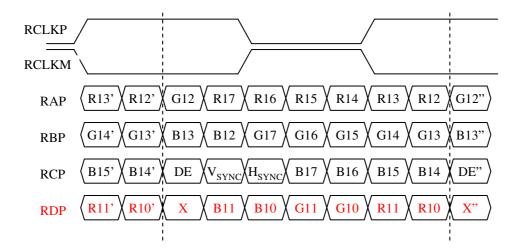
\* Initial Current Boost Function is not used at LC320WXN-SAA1

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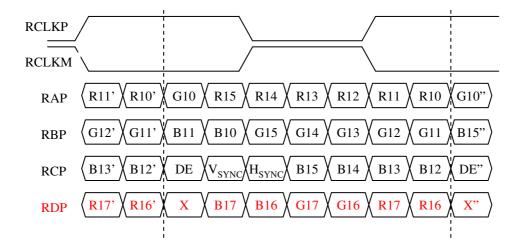
#### # APPENDIX- VII

## LVDS Data-Mapping info. (8bit)

### ■ LVDS Select: "H" Data-Mapping (JEIDA format)



### ■ LVDS Select: "L" Data-Mapping (VESA format)

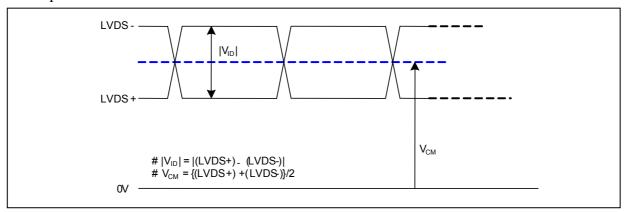


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#### # APPENDIX- VIII

# **LVDS Input characteristics**

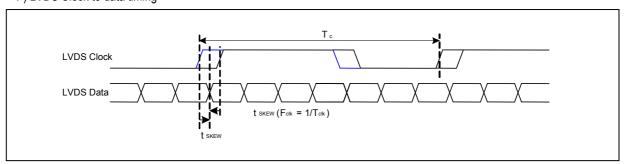
### 1. DC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	V <sub>ID</sub>	200	500	mV	-
LVDS Common mode Voltage	V <sub>CM</sub>	1.0	1.5	V	-

### 2. AC Specification

#### 1) LVDS Clock to data timing

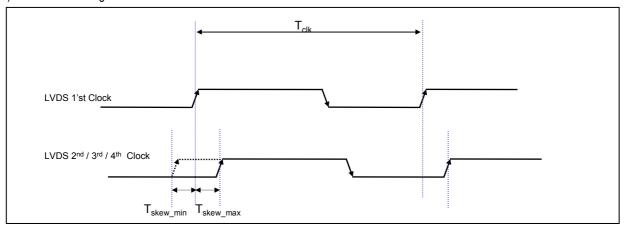


Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skew Margin	t <sub>SKEW</sub>	- (Tc / 7)* 0.2	+(Tc / 7)* 0.2	ps	Note 1

Note> 1. If Tc=13.8ns ,  $t_{\rm SKEW}$  Min= - 394ps Max= + 394ps

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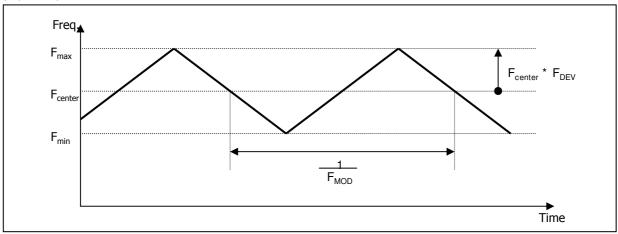
### 2) LVDS Clock timing



< LVDS inter-port Clock timing >

Description	Symbol	Min	Max	Unit	Notes
LVDS inter-port Clock Skew	T <sub>skew</sub>	-2.5	2.5	ns	-

### 3) Spread Spectrum



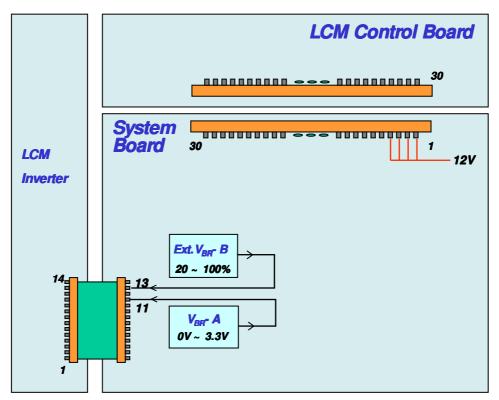
< Spread Spectrum >

Description	Symbol	Min	Max	Unit	Notes
Maximum deviation of input clock frequency during SSC	F <sub>DEV</sub>	-	± 2	%	-
Maximum modulation frequency of input clock during SSC	F <sub>MOD</sub>	-	300	KHz	-

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#### # APPENDIX-IX

#### ■ System DCR (Dynamic Contrast Ratio)- Max 11,000:1 (Reference)



EXTV <sub>BR</sub> -B	20% ~ 100%	100%
V <sub>BR</sub> -A	0V ~ <b>1.65V</b>	1.65V
DCR Level	(Bare CR) X 10	1100 : 1

Vвк-а,в Voltage	Function
EXTV <sub>BR</sub> -B : 100% V <sub>BR</sub> -A : 1.65V	Maximum Brightness (100%) (DCR Condition)
EXTV <sub>BR</sub> -B : 20% V <sub>BR</sub> -A : 0V	Minimum Brightness (10%) (DCR Condition)

Note : 1. To make DCR Max (Bare CR) X 10,  $V_{BR}$ -A and  $V_{BR}$ -B must be given by system.

2. DCR Max (Bare CR) X 10 is defined mathematically as :

DCR = Maximum DCRn (n=1, 2, 3, 4, 5)

DCRn = Surface Luminance at position n with all white pixels (EXTV<sub>BR-B</sub>= 100%, V<sub>BR-A</sub>=1.65V)

Surface Luminance at position n with all black pixels (EXTVBR-B=20%, VBR-A=0V)

n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.

- 3. Measurement Sequence (aging time 10 min each pattern):
  - 1 Turn On LCM
  - 2 Measure Black Luminance (EXTVBR-B=20%, VBR-A=0V)
  - 3 Measure White Luminance (EXTVBR-B=100%, VBR-A=1.65V)
- 4. In case the lowest minimum brightness is continued,

it can affect the lamp reliability and appear the partial darkness.

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#### # APPENDIX- X

## **Gray to Gray Response Time Uniformity**

This is only the reference data of G to G and uniformity for LC320WXN-SAA1 model.

#### 1. G to G Response Time:

Response time is defined as Figure 3 and shall be measured by switching the input signal for "Gray (N)" and "Gray(M)".(32Gray Step at 8bit)

#### 2. G to G Uniformity

The variation of G to G Uniformity ,  $\delta$  G to G is defined as :

G to G Uniformity = 
$$\frac{Maximum(GtoG) - Typical(GtoG)}{Typical(GtoG)} \le 1$$

\*Maximum (GtoG) means maximum value of measured time (N, M = 0 (Black) ~ 255(White), 32 gray step).

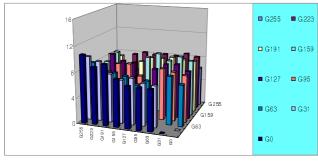
	0Gray	32Gray	64Gray	•••	223Gray	255Gray
0Gray		TrR:0G→32G	TrR:0G→64G		TrR:0G→223G	TrR:0G→225G
32Gray	TrD:32G→0G		TrR:32G→64G		TrR:32G→223G	TrR:32G→255G
64Gray	TrD:64G→0G	TrD:64G→32G			TrR:64G→223G	TrR:64G→255G
223Gray	TrD:223G→0G	TrD:223G→32G	TrD:223G→64G			TrR:223G→255G
255Gray	TrD:255G→0G	TrD:255G→32G	TrD:255G→64G		TrD:255G→223G	

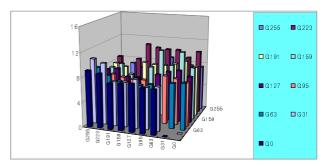
- 3. Sampling Size: 2 pcs
- 4. Measurement Method: Follow the same rule as optical characteristics measurement.

#### 5. Current Status

Below table is actual data of production on Oct.24, 2007 (LPL RV Event Sample)

	G to G Respo	Uniformity		
	Min.	Max.	Offiliofffility	
# 1	5.4	11.2	0.40	
# 2	5.6	11.4	0.43	





<#1> <#2>

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