



SPECIFICATION FOR APPROVAL

(●) Preliminary Specification

() Final Specification

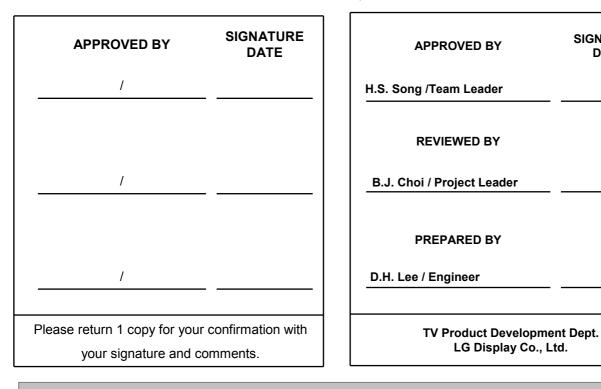
Titlo	
Title	

37.0" WUXGA TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC370WUN
SUFFIX	SCA1(RoHS Verified)

*When you obtain standard approval, please use the above model name without suffix



1 /41

SIGNATURE

DATE

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RECORD OF REVISIONS

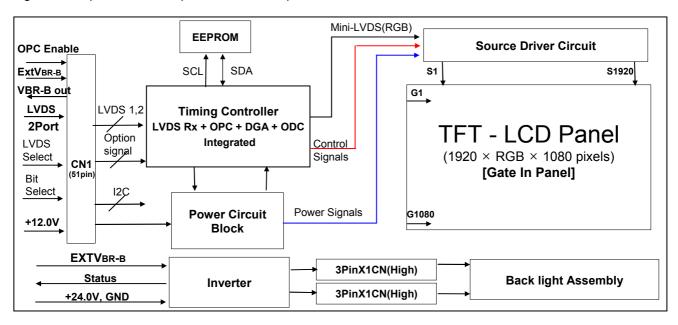
Revision No.	Revision Date	Page	Description
0.1	May. 03, 2010	-	Preliminary Specification(First Draft)

1. General Description

The LC370WUN is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 37.0 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.06B (true) colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	37 inches(940.091mm) diagonal
Outline Dimension	877(H) x 516.8 (V) x 55.5 mm (D) (Typ.)
Pixel Pitch	0.4845 mm x 0.4845 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10-bit(D), 1.06 B colors
Luminance, White	500 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Typ.), U/D 178 (Typ.))
Power Consumption	Total 126.68W (Typ.) (Logic= 7.68 W, Inverter= 115 W)
Weight	7,300 g (Тур.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)

2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

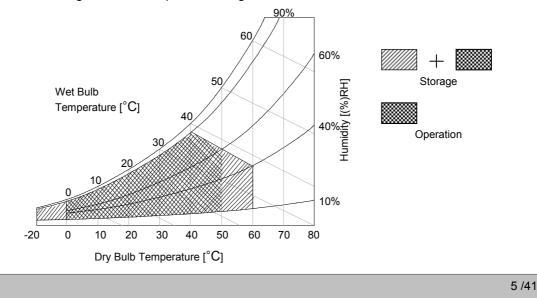
Parameter		Symbol Value		lue	Unit	Note
Faiai	Faraineter		Min	Max	Onit	Note
Rower Input Voltago	LCD Circuit	VLCD	-0.3	+14.0	VDC	
Power Input Voltage	Inverter	VBL	-0.3	+ 27.0	VDC	
Invertor Control Voltage	ON/OFF	Voff / Von	-0.3	+5.5	VDC	1
Inverter Control Voltage	Brightness	VBR	0.0	+5.0	VDC	
T-Con Option Selection	/oltage	VLOGIC	-0.3	+4.0	VDC	
Operating Temperature		Тор	0	+50	°C	2,3
Storage Temperature	Storage Temperature		-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	0.0
Storage Humidity		Hs⊤	10	90	%RH	2,3

Note1. Ambient temperature condition (Ta = 25 \pm 2 $^\circ\text{C}$)

2. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be Max 39°C, and no condensation of water.

- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may degraded in case of improper thermal management in final product design.



3. Electrical Specifications

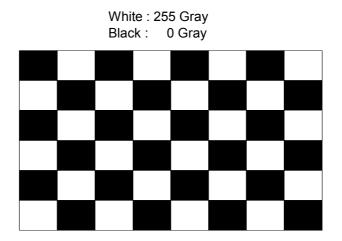
3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the EEFL backlight and inverter circuit.

Table 2.	ELECTRICAL	CHARACTERISTICS
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Parameter	Symbol Value				Unit	Note		
Falameter	Symbol	Min	Тур	Max	Onit	NOLE		
Circuit :	Circuit :							
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC			
Dower Input Current	h.a.	-	640	835	mA	1		
Power Input Current	ILCD	-	1010	1315	mA	2		
Power Consumption	PLCD		7.68	9.98	Watt	1		
Rush current	Irush	-	-	5.0	A	3		

- Note 1. The specified current and power consumption are under the V_{LCD}=12.0V, Ta=25 \pm 2°C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
 - 2. The current is specified at the maximum current pattern.
 - 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).



Mosaic Pattern(8 x 6)

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol		Values		Unit	Note		
Falameter			Symbol	Min	Тур	Мах		Note	
Inverter :									
Power Supply Input	t Voltage		VBL	22.8	24.0	25.2	VDC	1	
Power Supply	After Aging		IBL_A	-	4.8	5.1	A	1	
Input Current	Before Agir	ng	IBL_B	-	5.6	5.9	A	2	
Power Supply Input Current (In-Rush)			IRUSH	-	-	10	А	VBL = 22.8V EXTVBR-B = 100% 6	
Power Consumptio	n		PBL	-	115	122	W	1	
	On/Off	On	Von	2.5	-	5.0	VDC		
		Off	Voff	-0.3	0.0	0.8	VDC		
Input Voltage for	Brightness	Adjust	EXTVBR-B	20	-	100	%	On Duty 7	
Control System Signals	PWM Freq		PAL		100		Hz	5	
	NTSC & P	AL	NTSC		120		Hz	5	
	,	Pulse Duty		2.5	-	5.0	VDC	High: Lamp on	
	Level (PWM) (Burst mode)		Low Level	0.0	-	0.8	VDC	Low : Lamp off	
Lamp:									
Discharge Stabiliz	ation Time		Ts			3	min	3	
Life Time				50,000	60,000		Hrs	4	

- Note 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (EXTVBR-B: 100%), it is total power consumption.
 - 2. Electrical characteristics are determined within 30 minutes at 25±2°C. The specified currents are under the typical supply Input voltage 24V.
 - The brightness of the lamp after lighted for 5minutes is defined as 100%.
 TS is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
 The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
 - 4. Specified Values are for a single lamp which is aligned horizontally. The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (EXTVBR-B :100%), on condition of continuous operating at 25± 2°C
 - 5. LGD recommend that the PWM freq. is synchronized with Two times harmonic of Vsync signal of system.
 - 6. The duration of rush current is about 10ms.
 - 7. **EXTV**BR-B is based on input PWM duty of the inverter.

3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector : FI-R51S-HF(manufactured by JAE) or KN25-51P-0.5SH(manufactured by Hirose) (CN1) Refer to below table
- Mating Connector : FI-R51HL(JAE) or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	Reverse	'H' = Enable , 'L' or NC = Disable	27	Bit Select	'H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Reserved for LGD)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Reserved for LGD)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Reserved for LGD)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	EXTVBR-B	External VBR (From System)	34	GND	Ground
9	VBR-B out	OPC output (From LCM)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	OPC Enable	'H' = Enable , 'L' or NC = Disable	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC	No Connection
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC	No Connection
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC	No Connection	-	-	-

Note 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the **EIA 644** Standard.
- 4. Specific pins(pin No. **#2~#6**) are used for internal data process of the LCD module. These pins should be no connection.

Specific pins(pin No. # 8~#10) are used for OPC function of the LCD module.
 If not used, these pins are no connection. (Please see the Appendix III-4 for more information.)

- 6. LVDS pin (pin No. **#24,25,40,41**) are used for 10Bit(D) of the LCD module. If used for 8Bit(R), these pins are no connection.
- Specific pin No. #44 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-2-2. Backlight Module

[Master]

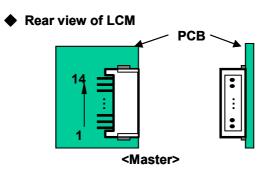
-Inverter Connector : 20022WR-14B1(Yeonho) or Equivalent - Mating Connector : 20022HS-14 or Equivalent

Table 5. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Note
1	VBL	Power Supply +24.0V	
2	VBL	Power Supply +24.0V	
3	VBL	Power Supply +24.0V	
4	VBL	Power Supply +24.0V	
5	VBL	Power Supply +24.0V	
6	GND	Backlight Ground	
7	GND	Backlight Ground	
8	GND	Backlight Ground	1
9	GND	Backlight Ground	
10	GND	Backlight Ground	
11	NC	No Connection	
12	VON/OFF	Backlight ON/OFF control	
13	EXTVBR-B	External PWM	
14	Status	Lamp Status	2

Note 1. GND should be connected to the LCD module's metal frame.

- 2. Normal : Low (under 0.7V) / Abnormal : High (upper 3.0V) Please see **Appendix IV-1** for more information.
- 3. The impedance of pin #12 is over $50[K\Omega]$ & the impedance of Pin #13 is over $100[K\Omega]$.



3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

		<u> </u>		, , , , , , , , , , , , , , , , , , ,			
I	TEM	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tн∨	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	t∨v	-	1080	-	tHP	
Vertical	Blank	tvв	11	45	69	tHP	
	Total	tvp	1091	1125	1149	tHP	
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fн	65	67.5	70	KHz	
	Vertical	f∨	57	60	63	Hz	

Table 6-1. TIMING TABLE for NTSC (DE Only Mode)

Table 6-2. TIMING TABLE for PAL (DE Only Mode)

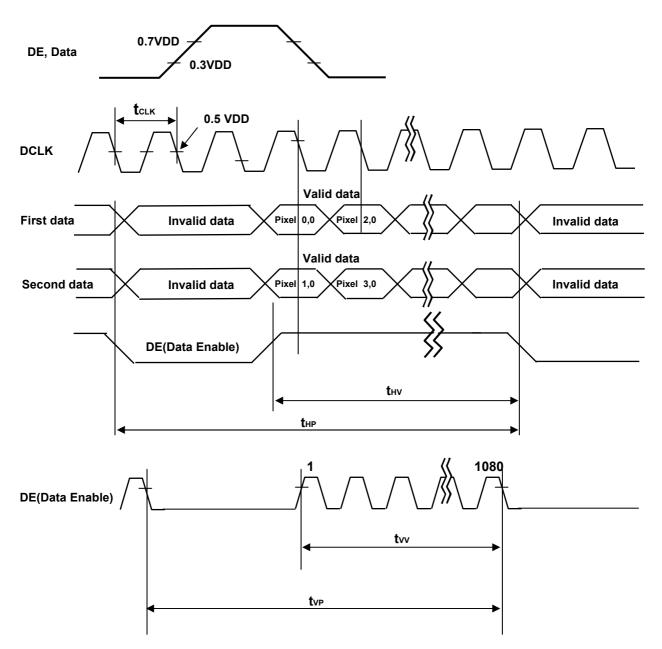
I	TEM	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tн∨	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tvv	-	1080	-	tHP	
Vertical	Blank	tvв	228	270	300	tHP	
	Total	tvp	1308	1350	1380	thp	
	DCLK	fCLK	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fн	65	67.5	70	KHz	
	Vertical	f∨	47	50	53	Hz	

Note The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

Ver. 0.1

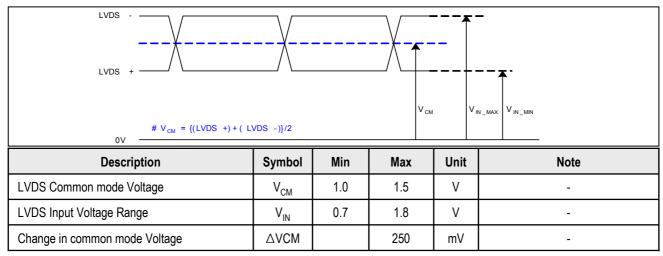
3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram

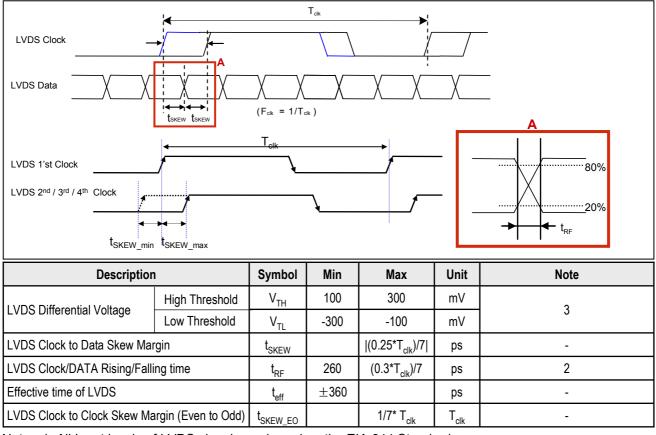


3-4-2. LVDS Input Signal Characteristics

1) DC Specification



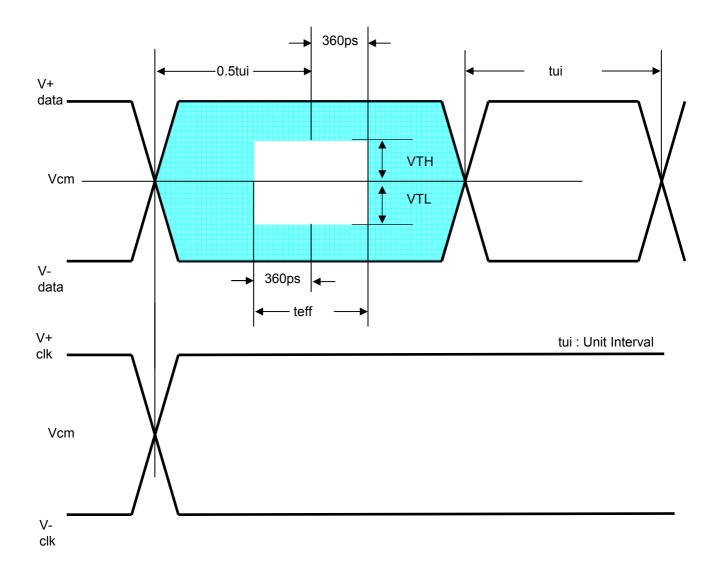
2) AC Specification



Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range.
- 3. LVDS Differential Voltage is defined within ${\rm t_{eff}}$

Ver. 0.1



3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

														Inp	out	Co	lor	Da	ta												
	Color	MS	SB			R	ΞD			L	.SB	M	SB		G	SRI	EEI	N		L	SB	M	SB			BL	UE			L	SB
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	GO	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED						-																									
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
GREEN																															
	GREEN (1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										-					
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

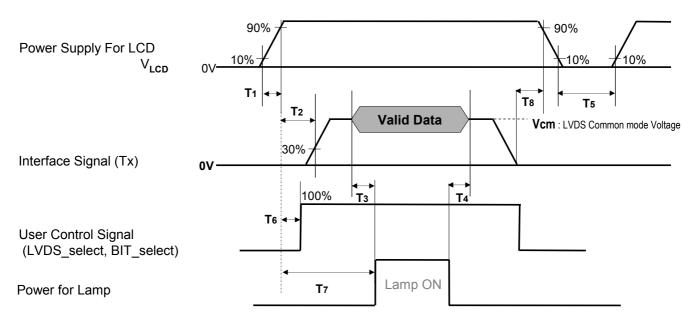
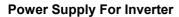


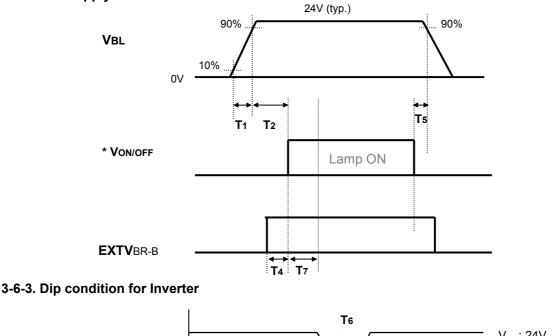
Table 8. POWER SEQUENCE

Deverseter	Value	Value		l la it	Natas
Parameter	Min	Тур	Max	– Unit	Notes
T1	0.5	-	20	ms	
T2	0	-	-	ms	4
Т3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	s	5
T6	-	-	T2	ms	4
T 7	0.5	-	-	S	
Т8	100	_	-	ms	6

- Note : 1. Please avoid floating state of interface signal at invalid period.
 - 2. When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.
 - 3. The **T3** / **T4** is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 - 4. If the on time of signals(Interface signal and user control signals) precedes the on time of Power(V_{LCD}), it will be happened abnormal display. When **T6** is NC status, **T6** doesn't need to be measured.
 - 5. **T5** should be measured after the Module has been fully discharged between power off and on period.
 - 6. It is recommendation specification that **T8** has to be 100ms as a minimum value.

3-6-2. Sequence for Inverter





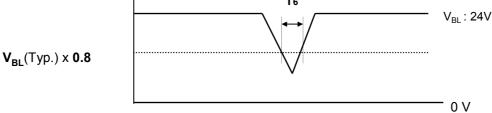


Table 9. Power Sequence for Inverter

Deremeter		Values		Unite	Note
Parameter	Min	Тур	Max	Units	Note
T1	20	-	-	ms	1
T2	500	-	-	ms	
T4	0		-	ms	2
T5	10	-	-	ms	
T6	-	-	10	ms	V_{BL} (Typ) x 0.8
T 7	1000	-	-	ms	3

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time. 2. T4(max) is less than T2.

3. It is the recommendation to input Max Duty to Inverter** for EXTVBR-B during T7 period.

- **When OPC Function is applied, the Max Duty is input to T-Con.
- * The recommendation of Von/off rising time is under 10ms.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

It is presented additional information concerning the measurement equipment and method in FIG. 1.

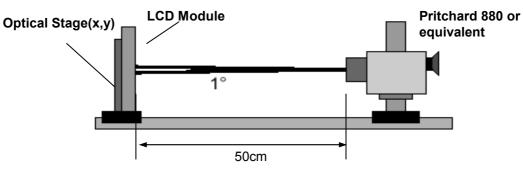


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= 25 \pm 2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz, **EXTV**BR-B =100%

Damana		O much al		Value		11	Nata
Paramet	er	Symbol	Min	Тур	Max	Unit	Note
Contrast Ratio		CR	1100	1500	-		1
Surface Luminance, v	vhite	L _{WH}	400	500		cd/m ²	2
Luminance Variation		δ _{WHITE} 5P			1.3		3
Deenenee Time	nse Time Gray-to-Gray Uniformity RED		-	5	8	ms	4
Response nine			-	-	1	ms	5
	r Coordinates			0.639			
				0.334			
				0.289			
Color Coordinates	GREEN	Gy	Тур	0.606	Тур		
[CIE1931]	DUUE	Bx	-0.03	0.145	+0.03		
	BLUE	Ву		0.065			
		Wx		0.279			
	VVIIIE	Wy		0.292			
Color Tempe	rature			10,000		К	
Color Gar	nut			72		%	
Viewing Angle	Color Gamut Viewing Angle (CR>10)						
x axis	s, right(φ=0°)	θr	89	-	-		
x axis	, left (φ=180°)	θΙ	89	-	-	dograa	6
y axis, up (φ=90°) y axis, down (φ=270°		θu	89	-	-	degree	U
		θd	89	-	-		
Gray Sca	le		-	-	-		7
Ver. 0.1							17 /41

Note : 1. Contrast Ratio(CR) is defined mathematically as :

Contrast Ratio = $\frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$ It is measured at center 1-point.

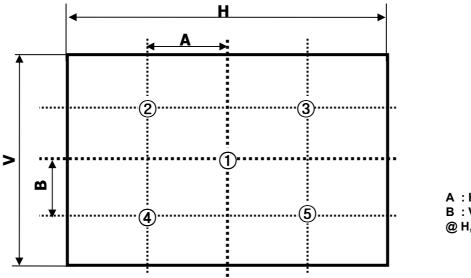
- Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as :

 δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transit from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
 ※ G to G Spec stands for average value of all measured points. Photo Detector : RD-80S / Field : 2°
- 5. Gray to Gray Response time uniformity is Reference data. Please see Appendix V.
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.

Gray Level	Luminance [%] (Typ)
LO	0.067
L15	0.24
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100
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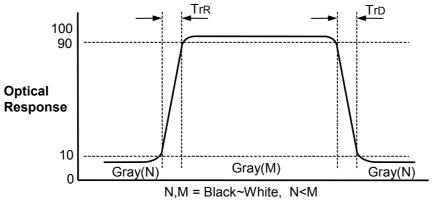
Measuring point for surface luminance & luminance variation



A : H / 4 mm B : V / 4 mm @ H,V : Active Area

FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".





Dimension of viewing angle range

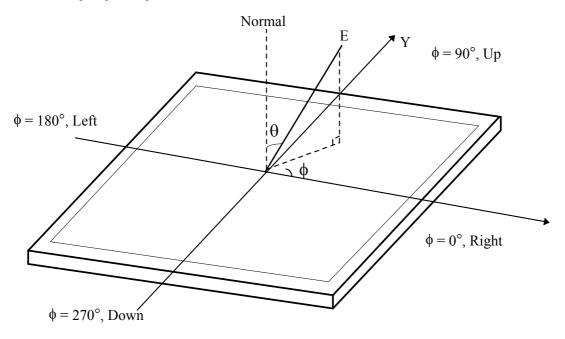


FIG. 4 Viewing Angle

5. Mechanical Characteristics

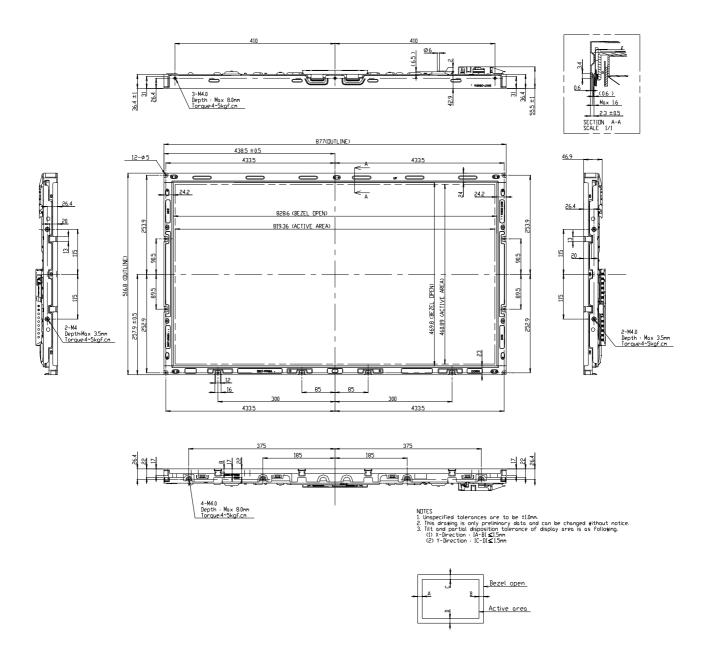
Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

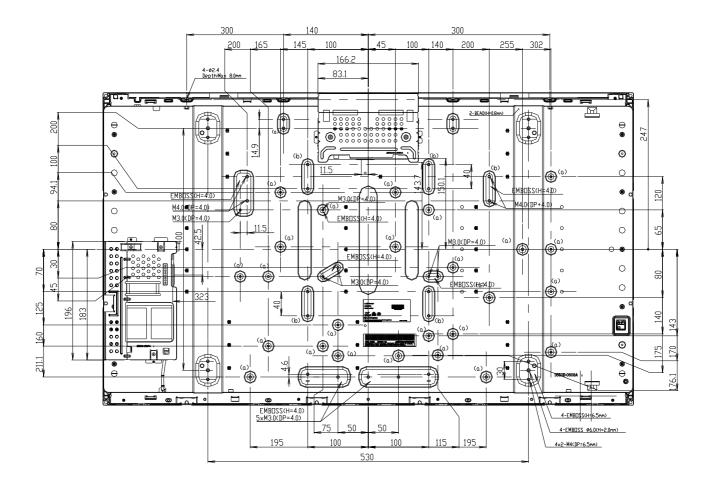
Item	Va	lue
	Horizontal	877.0 mm
Outline Dimension	Vertical	516.8 mm
	Depth	55.5 mm
Deed Area	Horizontal	828.6 mm
Bezel Area	Vertical	469.8 mm
Active Display Area	Horizontal	819.36 mm
Active Display Area	Vertical	460.89 mm
Weight	7,300(typ) , 7,600(Max)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

[FRONT VIEW]



[REAR VIEW]



6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min Each direction per 10 min
6	Shock test (non-operating)	Shock level : 100Grms Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

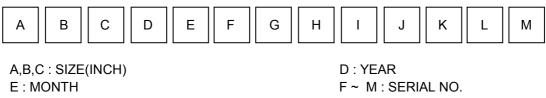
7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

8. Packing

8-1. Information of LCM Label

a) Lot Mark



Note

I. IEAR										
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 4 pcs
- b) Box Size : 968 mm X 366 mm X 595 mm.

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer.
- Transparent protective plate should have sufficient strength in order to the resist external force. (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental)
- to the polarizer.)(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 mV(Over and under shoot voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from transformers to prevent abnormal display, sound noise and temperature rising.
- (11) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic.

(12) Partial darkness may happen under the long-term operation of any dimming without power on/off. This phenomenon which disappears naturally after 5 minutes is not a problem about reliability but LCD characteristics.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

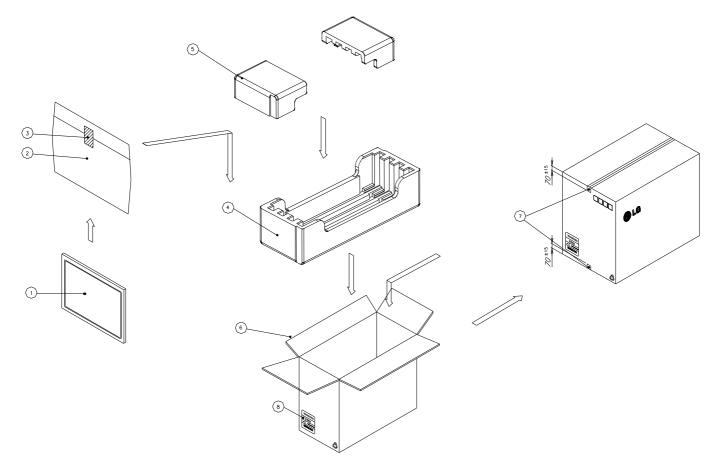
- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

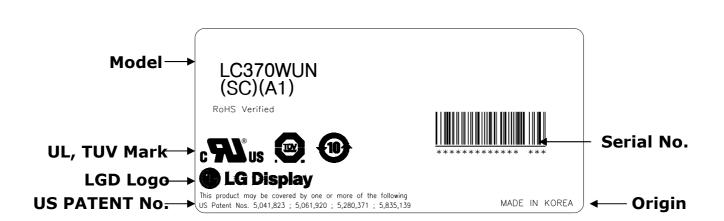
APPENDIX-I

■ LC370WUN-SCA1 – Pallet Ass'y



NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	AL
3	TAPE	MASKING 20MMX50M
4	Packing	EPS
5	Packing	EPS
6	BOX	PAPER_DW3
7	TAPE	OPP 70MMX300M
8	Label	ART 100X70

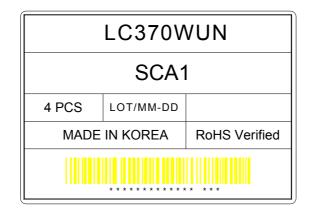
■ LC370WUN-SCA1-LCM Label



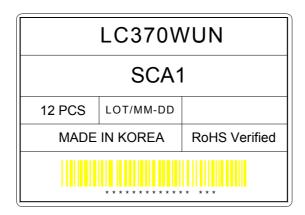
Ver. 0.1

■ LC370WUN-SCA1-Label

Box Label



Pallet Label



■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L")

Host System		THC63LVD103					
30 Bit		or Compatible					Timing
RED0		33			•		Controller
RED1		34		FI-RE51S-HF			
RED2		35					
RED3		36		31			
RED4		37 T/	A-		12	<u>100</u> Ω >	RO0N
RED5		38 TA	4+	30	13	1002 5	RO0P
RED6		59					
RED7		61 -	B-	29	14		RO1N
RED8		4		28		1 00 Ω ≷	
RED9		5 TE	3+		15		RO1P
GREEN0		40		05			
GREEN1		41 T(C-	25	16	>	RO2N
GREEN2		42 тс	C+	24	17	100Ω ≶	RO2P
GREEN3		44					_
GREEN4		45		23	40		DOOLIKN
GREEN5		46 TCLI		22	19	1 00 Ω <	ROCLKN
GREEN6		62 TCLM	< +		20	10000	ROCLKP
GREEN7		63		04			
GREEN8		6 TI	D-	21	22	>	RO3N
GREEN9		8 тс)+	20	23	<u>100</u> Ω 🗧	RO3P
BLUE0		48					
BLUE1		49	-	19	0.4		DOM
BLUE2		1 50	E-	18	24	100Ω ≷	RO4N
BLUE3		52 TE	-+	10	25	10000	RO4P
BLUE4		53					
BLUE5		54			7		VESA/ JEIDA
BLUE6		64					
BLUE7		1				l	
BLUE8		9			1		
BLUE9		11]
Hsync		55		G		LCM Module	
Vsync		57		GND			
Data Enable		58		J			
CLOCK	<u>}</u>	12					

Note: 1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '9' means MSB and '0' means LSB at R,G,B pixel data.

Ver. 0.1

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")

Host System	тно	C63LVD103				
30 Bit	or C	Compatible				Timing
RED0	4					Controller
RED1	5		FI-RE51S-HF			
RED2	59					
RED3	61		31			
RED4	33	TA-		12	100Ω ≷	RO0N
RED5	34	TA+	30	13	10025	RO0P
RED6	35					
RED7	36	TB-	29	14		RO1N
RED8	37		28		100Ω ≶	
RED9	38	TB+		15		RO1P
GREEN0	6		25			
GREEN1	8	TC-		16		RO2N
GREEN2	62	TC+	24	17	<u>100</u> Ω	RO2P
GREEN3	63					
GREEN4	40	TCLK-	23	19		ROCLKN
GREEN5	41		22		<u>100</u> Ω 🗧	
GREEN6	42	TCLK+		20		ROCLKP
GREEN7	44		21			
GREEN8	45	TD-		22	2	RO3N
GREEN9	46	TD+	20	23	<u>100</u> Ω	RO3P
BLUE0	9					
BLUE1	11	TE-	19	24		RO4N
BLUE2	64		18		<u>100</u> Ω <	
BLUE3	1	TE+	_	25		RO4P
BLUE4	48					
BLUE5	49			7		VESA / JEIDA
BLUE6	50					
BLUE7	52				I	
BLUE8	53			1		
BLUE9	54					
Hsync	55		<		LCM Module	
Vsync	57		VCC			
Data Enable	58					
CLOCK	12]			

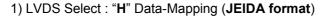
Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

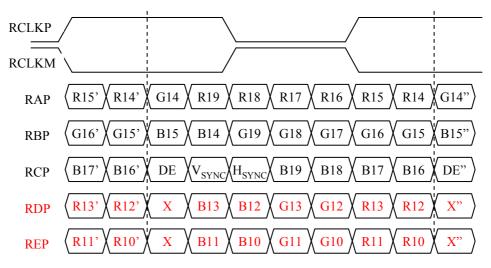
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '9' means MSB and '0' means LSB at R,G,B pixel data.

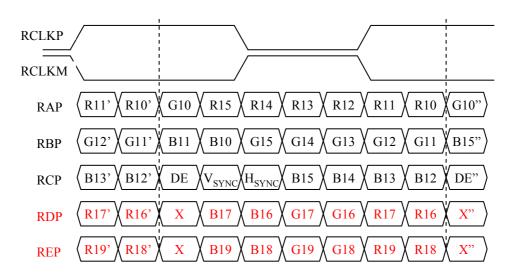
Ver. 0.1

LVDS Data-Mapping Information (10 Bit)



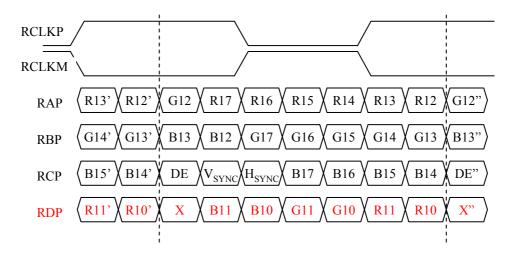


2) LVDS Select : "L" Data-Mapping (VESA format)

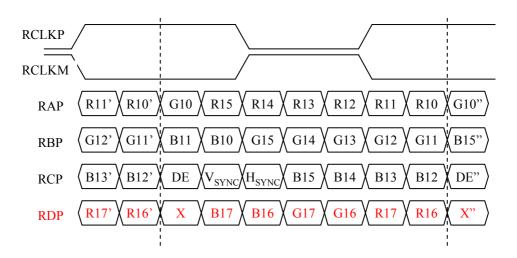


LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)

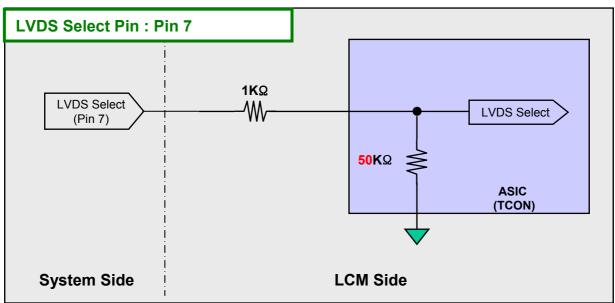


2) LVDS Select : "L" Data-Mapping (VESA format)

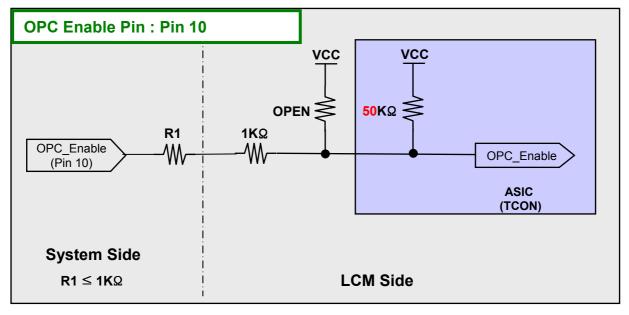


Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin

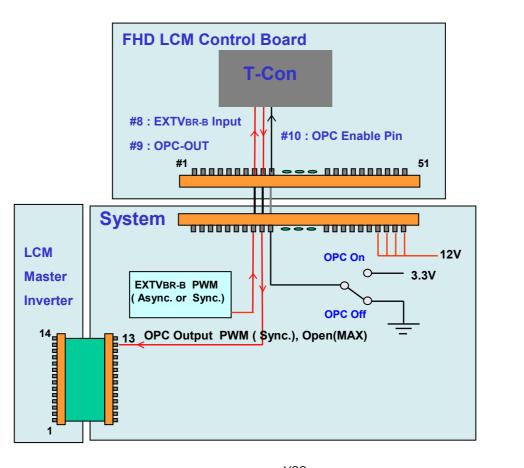


2) Circuit Block Diagram of OPC Enable Selection pin

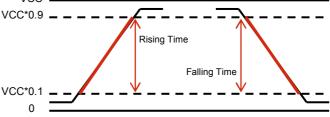


EXTVBR-B & **OPC** Design Guide

- 1) When OPC Enable is "L", OPC Output = System Dimming.
- 2) OPC Output(PWM Signal) is synchronized with V-Sync Freq. of System in T-Con Board.
- 3) Regardless of OPC, System should always give dimming Signal (EXTVBR-B) to T-con.
- 4) PWM Specification (VCC = 3.3V) @ OPC
 - a) PWM High Voltage Range : 2.5 V ~ 3.6 V
 - b) PWM Low Voltage Range $: 0.0 \text{ V} \sim 0.8 \text{ V}$



Input Frequency	MAX 1Khz (Recommendation: 50~300Hz)	VCC VCC*0.9
Rising Time	MAX 10.0 µs	
Falling Time	MAX 10.0 µs	VCC*0.1

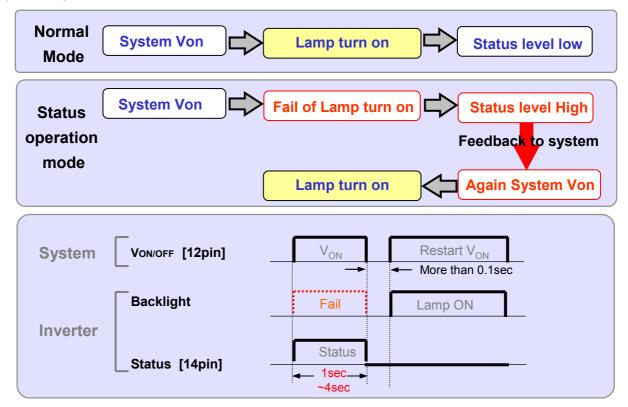


Ver. 0.1

■ Inverter 14th Pin (Status) Design Guide

- 1) Function of Status pin
- Purpose : Preventing of backlight off by restarting the inverter technically
- How to : When inverter is abnormal operation, TV system inputs the Von signal in the inverter once more to turn on the lamp safely
- Attention : Restart system's Von signal when status pin is high for some time (min:1sec , max:4sec). (The turn on time of lamp can be late such as the low temperature or the storage time)

2) Status operation modes in TV set



3) Inverter pin map

Pin No	Symbol	Description	Inv.
11	NC	No Connection	NC
12	Von/off	0.0V ~ 5.0V	On/Off
13	EXTVBR-B	EXTVBR-B Burst Dimming Control PWM signal input	
14	Status	Normal : Under 0.7V / Abnormal : Upper 3.0V	status

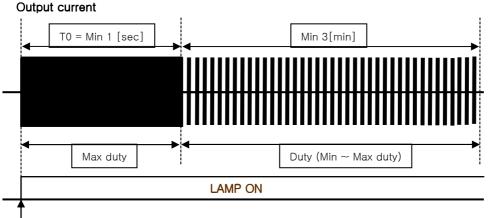
Ver. 0.1

Mega DCR Using Condition (1)

The Deep Dimming means using the input PWM duty less than Min duty. The input PWM duty (Min & Max duty) refer to the table 3 on the page 7.

The Deep Dimming must be used very carefully due to limitation of lamp characteristics and specification.

1) For stable lamp on, its duty condition should follow below the condition. After Inverter ON signal, T0 duration should be sustained.

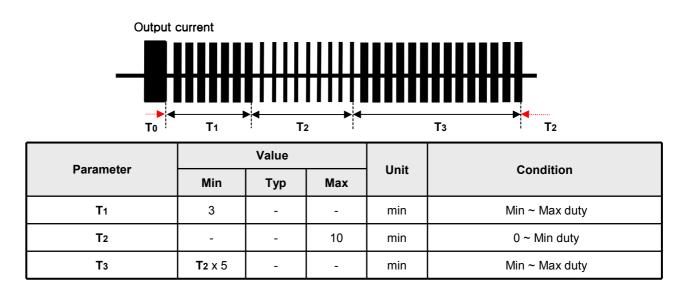


Inverter ON signal

2) B/L may not satisfy some of LCM specification at the Deep Dimming.

- Duration : The Deep Dimming must be limited within 10 minutes.
- Ratio : The operation time of the Deep Dimming must be less than 1/5 time of the Normal Duty (Min ~ Max duty) operation in a certain period to prevent unwanted operation.
- FOS : Partial darkness or darkness of center area during the Deep Dimming might be happened due to insufficient lamp current.
- Warm up : The Normal Duty (Min ~ Max duty) must be used 3 min after the lamps "ON". In case of low temperature, more warm up time may be needed.

■ Mega DCR Using Condition (2)



3) Following the recommended conditions as aforementioned, there is no difference of lamp lifetime between conventional method and new one.

APPENDIX- V

Gray to Gray Response Time Uniformity (δ_{GTOG})

This is only the reference data of G to G and uniformity for LC370WUN-SCC1 model.

1. G to G Response Time :

Response time is defined as Figure3 and shall be measured by switching the input signal for "Gray (N)" and "Gray(M)".(32Gray Step at 8bit)

2. G to G Uniformity

The variation of G to G Uniformity , δ ${\tt G}$ to ${\tt G}$ is defined as :

G to G Uniformity = $\frac{Maximum(GtoG) - Typical(GtoG)}{Typical(GtoG)} \leq 1$

*Maximum (GtoG) means maximum value of measured time (N, M = 0 (Black) ~ 255(White), 32 gray step).

	0Gray	32Gray	64Gray		223Gray	255Gray
0Gray		TrR:0G→32G	TrR:0G→64G		TrR:0G → 223G	TrR:0G→255G
32Gray	TrD:32G→0G		TrR:32G→64G		TrR:32G→223G	TrR:32G→255G
64Gray	TrD:64G→0G	TrD:64G→32G			TrR:64G→223G	TrR:64G→255G
				/		
223Gray	TrD:223G→0G	TrD:223G→32G	TrD:223G→64G			TrR:223G→255G
255Gray	TrD:255G→0G	TrD:255G→32G	TrD:255G→64G		TrD:255G→223G	

- 3. Sampling Size : 2 pcs
- 4. Measurement Method : Follow the same rule as optical characteristics measurement.
- 5. Current Status

Below table is actual data of production on RV (LGD RV Event Sample)

Comple	G to G Respo	l luciformation	
Sample	Min.	Max.	Uniformity
# 1	3.5	7.6	0.93
# 2	3.5	7.7	0.96

