SPECIFICATION
FOR
APPROVAL

(   )   Preliminary Specification

(●)   Final Specification

Title	37.0" WXGA TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC370WXN
SUFFIX	SAA1(RoHS Verified)

*When you obtain standard approval,
  please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
/	
/	
/	

Please return 1 copy for your confirmation with
your signature and comments.

APPROVED BY	SIGNATURE DATE
J.H. Lee / Senior Manager	
REVIEWED BY	
J.Y.Lee / Manager	
PREPARED BY	
S.Y. Choi / Engineer	

TV Product Development Dept.
LG. Philips LCD Co., Ltd

Contents

Record of Revisions

| Revision No.	Revision Date	Page	Description
1.0	Jan. 08, 2007	-	Final Specification

1. General Description

LC370WXN is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 37.02 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.
It has been designed to apply the 8-bit 1 port LVDS interface.
It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.
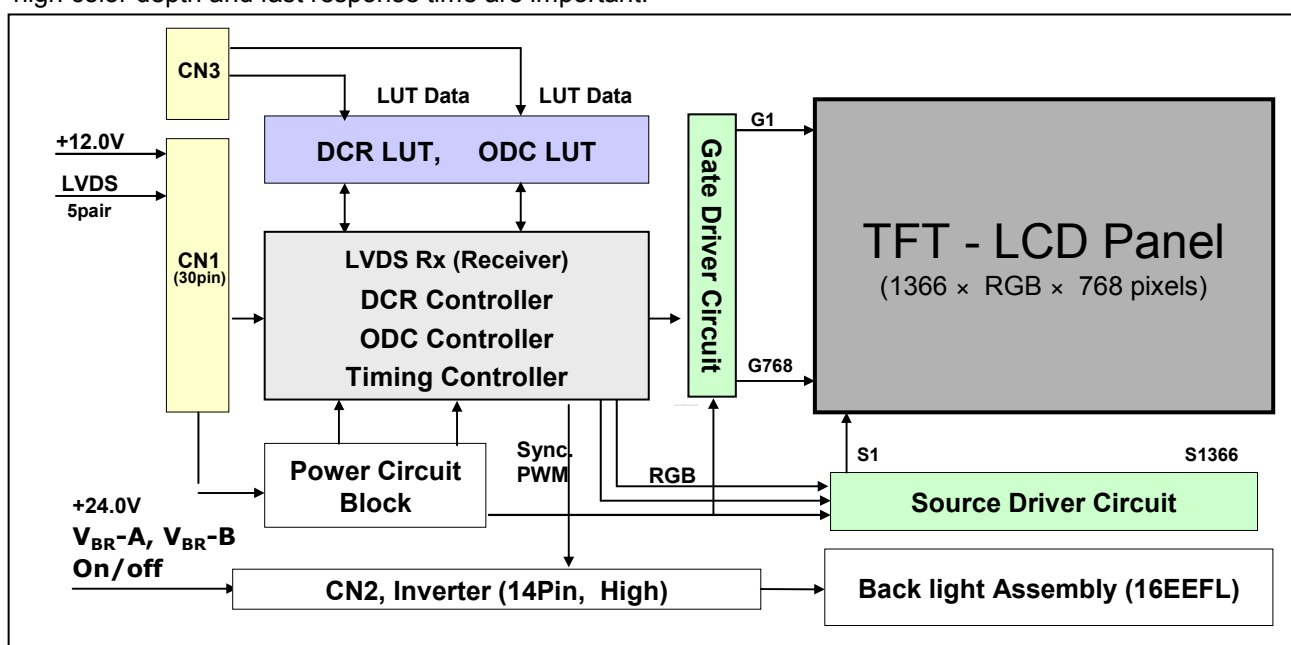
General Features

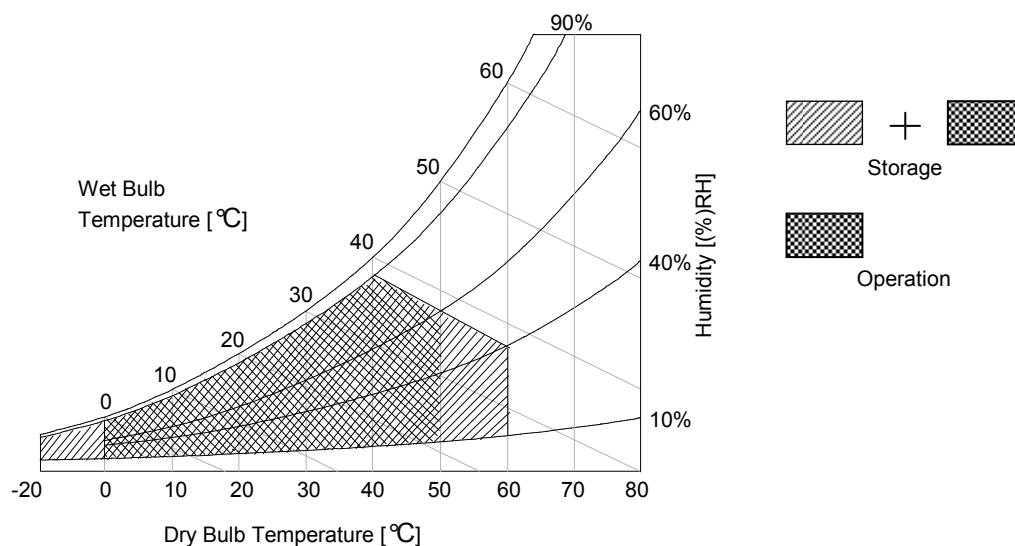|	
Active Screen Size	37.02 inches(940.3mm) diagonal
Outline Dimension	877.0mm(H) x 516.8mm(V) x 55.5mm(D) (Typ.)
Pixel Pitch	0.200mm x 0.600mm x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	500 cd/m^2 (Center 1 point Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 123.9 Watt (Typ.) (Logic= 3.9 W, B/L= 120 W [VBR-A=1.65V])
Weight	9000 g (Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 13%)

2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Remark
			Min	Max		
Power Input Voltage	LCM	VLCD	+8.0	+14.0	VDC	at 25 ± 2 ℃
	Backlight inverter	VBL	+22.5	+27.0	VDC	
ON/OFF Control Voltage		VON/OFF	-0.3	+5.5	VDC	
Brightness Control Voltage		VBR	0	+5.0	VDC	
Operating Temperature		TOP	0	+50	℃	Note 1,2
Storage Temperature		TST	-20	+60	℃	
Operating Ambient Humidity		HOP	10	90	%RH	
Storage Humidity		HST	10	90	%RH	

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 ℃ Max. and no condensation of water.
2. Gravity mura can be guaranteed under 40℃ condition.

3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the EEFL/Backlight is to power inverter.
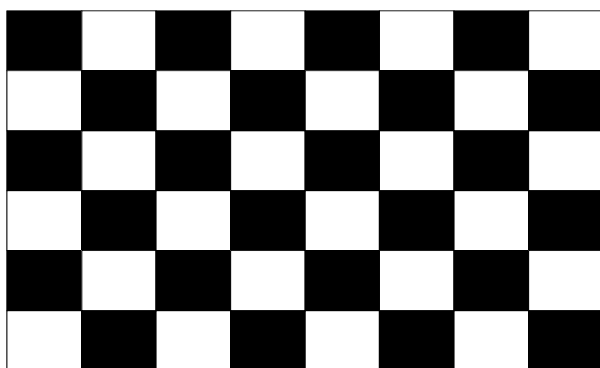
Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
		Min	Typ	Max		
MODULE :						
Power Input Voltage	V_{LCD}	11.4	12.0	12.6	V_{DC}	
Power Input Current	I_{LCD}	-	326	424	mA	1
		-	420	559	mA	2
Power Consumption	P_{LCD}	-	3.9	5.1	Watt	1
Rush current	I_{RUSH}	-	-	3.5	A	3

Note :
1. The specified current and power consumption are under the V_{LCD}=12.0V, 25 $\pm$ 2℃, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
2. The current is specified at the maximum current pattern[full white pattern].
3. The duration of rush current is about 2ms and rising time of power Input is 0.5ms(min.).

White : 255Gray
Black : 0Gray

White : 255 Gray

Mosaic Pattern(8 x 6)

Full White pattern

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter			Symbol	Values			Unit	Notes
				Min	Typ	Max		
Inverter :								
Power Supply Input Voltage			VBL	22.8	24.0	25.2	Vdc	1
Power Supply Input Voltage Ripple				-	-	0.5	Vp-p	1
Power Supply Input Current	After Aging		IBL_A	-	5	5.5	A	V_{BR-A} = 1.65V … 1
				-	5.5	6.0	A	V_{BR-A} = 3.3V … 1
	Before Aging		IBL_B	-	5.5	6.0	A	V_{BR-A} = 1.65V … 2
				-	6	6.5	A	V_{BR-A} = 3.3V … 2
Power Supply Input Current(In-Rush)			Irush	-	-	8	A	V_{BL} = 22.8V Ext V_{BR-B} = 100% V_{BR-A} = 1.65V
Power Consumption			PBL	-	120	132	W	V_{BR-A} = 1.65V … 1
Input signal for Inverter control	Brightness Adjust		V_{BR-A}	0.0	1.65	3.3	Vdc	
	On/Off	On	V on	2.5	-	5.0	Vdc	
		Off	V off	-0.3	0.0	0.8	Vdc	
	Brightness Adjust		EXTV$_{BR-B}$	30		100	%	On duty
PWM Frequency for NTSC & PAL			NTSC/PAL		100/120		Hz	5
Pulse Duty Level(PWM) (Burst mode)	High Level			2.5	-	5.0	Vdc	HIGH: Lamp on LOW:Lamp off
	Low Level			0.0	-	0.8	Vdc	
Lamp :								
Discharge Stabilization Time			Ts			3	min	3
Life Time				50,000			Hrs	4

Notes :
1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120
   minutes at 25± 2℃. The specified current and power consumption are under the typical supply Input voltage
   24Vand VBR (V$_{BR-A}$: 1.65V & ExtV$_{BR-B}$: 100%), it is total power consumption.
     The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is
   24.0V ± 5%.
2. Electrical characteristics are determined within 30 minutes at 25± 2℃.
     The specified currents are under the typical supply Input voltage 24V.
3. The brightness of the lamp after lighted for 5minutes is defined as 100%.
     TS is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
      The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
4. Specified Values are for a single lamp which is aligned horizontally.
     The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value
   at the typical lamp current (V$_{BR-A}$: 1.65V & ExtV$_{BR-B}$:100%), on condition of continuous operating at 25± 2℃
5. LPL recommend that the PWM freq. is synchronized with One or Two times harmonic of Vsync signal of system.
6. The duration of rush current is about 10ms.

Product Specification

3-2. Interface Connections

This LCD employs two kinds of interface connection, a 30-pin connector is used for the module electronics, 14 Connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1) : FI-X30SSL-HF (Manufactured by JAE) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	Select	Select LVDS Data format	1
10	NC	No connection	
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	NC	No connection	
28	NC	No connection	
29	GND	Ground	
30	GND	Ground	2

Note: 1. If the pin no. 9 is Ground, Interface format is "LG", and if the pin no. 9 is Vcc(3.3V), Interface format is "DISM". See page 27 and 28.
    2. The pin no. 30 is necessary for LCD test.
      When LVDS signals are abnormal operation more than 3-Vsync times and power 12V is supplied,
      'Open' or 'Vcc' : LCD operate itself some test patterns.(AGP – Auto Generation Pattern)
      'Ground' : LCD operate itself a black pattern. (NSB – No Signal Black)
      LPL recommend 'Ground' for NSB.
    3. All GND (ground) pins should be connected together, which should be also connected to the LCD module's metal frame.
    4. All V$_{LCD}$ (power input) pins should be connected together.
    5. Input Levels of LVDS signals are based on the IEA 664 Standard.

3-2-2. Backlight Inverter

Inverter Connector : S14B-PH-SMC
          (manufactured by YeonHo) or Equivalent
- Mating Connector : PHR-14 or Equivalent

Table 5. INVERTER CONNECTOR PIN CONFIGULATION

| Pin No	Symbol	Description	Master	Note
1	V_{BL}	Power Supply +24.0V	V_{BL}	
2	V_{BL}	Power Supply +24.0V	V_{BL}	
3	V_{BL}	Power Supply +24.0V	V_{BL}	
4	V_{BL}	Power Supply +24.0V	V_{BL}	
5	V_{BL}	Power Supply +24.0V	V_{BL}	
6	GND	Backlight Ground	GND	
7	GND	Backlight Ground	GND	
8	GND	Backlight Ground	GND	1
9	GND	Backlight Ground	GND	
10	GND	Backlight Ground	GND	
11	V_{BR-A}	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	V_{BR-A}	2, 3
12	$V_{ON/OFF}$	0.0V ~ 5.0V	On/Off	3, Open/High for B/L on as default
13	$ExtV_{BR-B}$	Burst Dimming Control PWM signal input	External PWM	4
14	GND	POWER GND	GND	5

Notes : 1. GND should be connected to the LCD module's metal frame.
     2. Minimum Brightness : V_{BR-A} = 0.0V Maximum Brightness : V_{BR-A} = 3.3V "OPEN" : V_{BR-A} = 1.65V
     3. Rising Edge : Lamp "ON" / Falling Edge : Lamp "OFF"
     4. Pin#13 can be opened. (if Pin #13 is open , Ext V_{BR-B} is 100%)
     5. Pin#14 can be opened. (GND or NC)
     6. Each impedance of pin #11,12 and 13 is 170[KΩ],30[KΩ],55[KΩ]

◆ **Rear view of LCM**
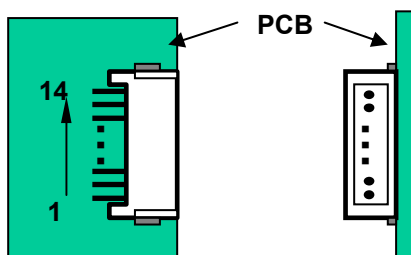
3-3. Signal Timing Specifications

Table 6-1 and Table 6-2 show the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6-1. TIMING TABLE for NTSC (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	tHV	-	1366	-	tclk	
	Blank	tHB	t_{HP}- t_{HV}	162	t_{HP}- t_{HV}	tclk	
	Total	tHP	1456	1528	1920	tclk	
Vertical	Display Period	tVV	-	768	-	Lines	
	Blank	tVB	t_{VP}- t_{VV}	22	t_{VP}- t_{VV}	Lines	
	Total	tVP	776	790	1063	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	fCLK	63	72.4	80	MHz	
	Horizontal	fH	45	47.4	50	KHz	
	Vertical	fV	57	60	63	Hz	

Table 6-2 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.
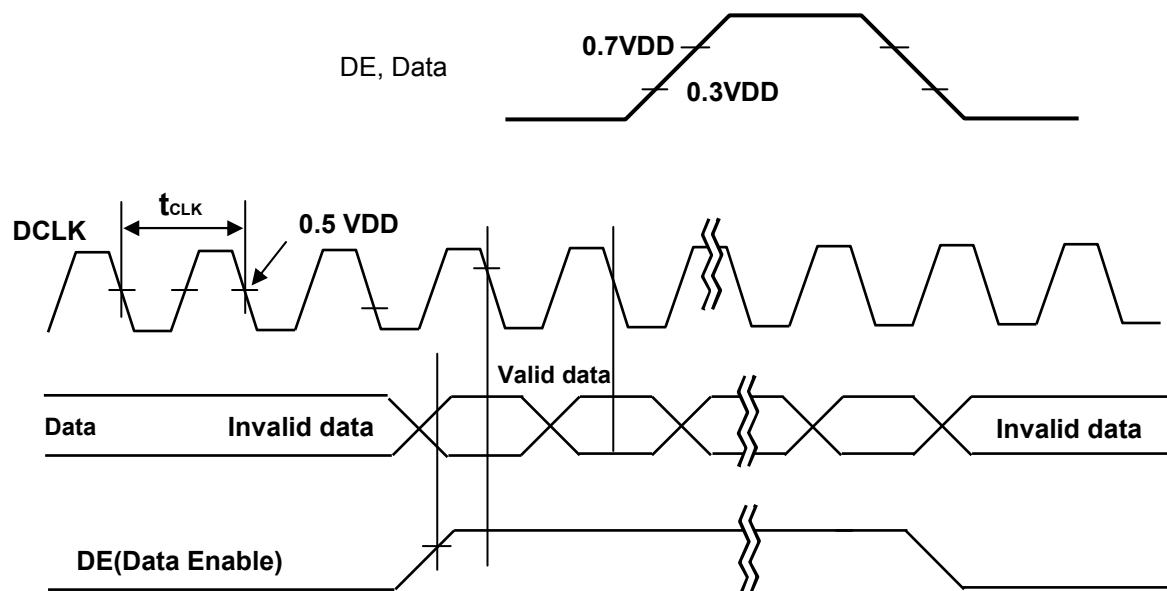
Table 6-2. TIMING TABLE for PAL (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	tHV	-	1366	-	tclk	
	Blank	tHB	t_{HP}- t_{HV}	162	t_{HP}- t_{HV}	tclk	
	Total	tHP	1456	1528	1920	tclk	
Vertical	Display Period	tVV	-	768	-	Lines	
	Blank	tVB	t_{VP}- t_{VV}	180	t_{VP}- t_{VV}	Lines	
	Total	tVP	894	948	1008	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	fCLK	63	72.4	80	MHz	
	Horizontal	fH	45	47.4	50	KHz	
	Vertical	fV	47	50	53	Hz	

Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode).
The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

3-4. Signal Timing Waveforms
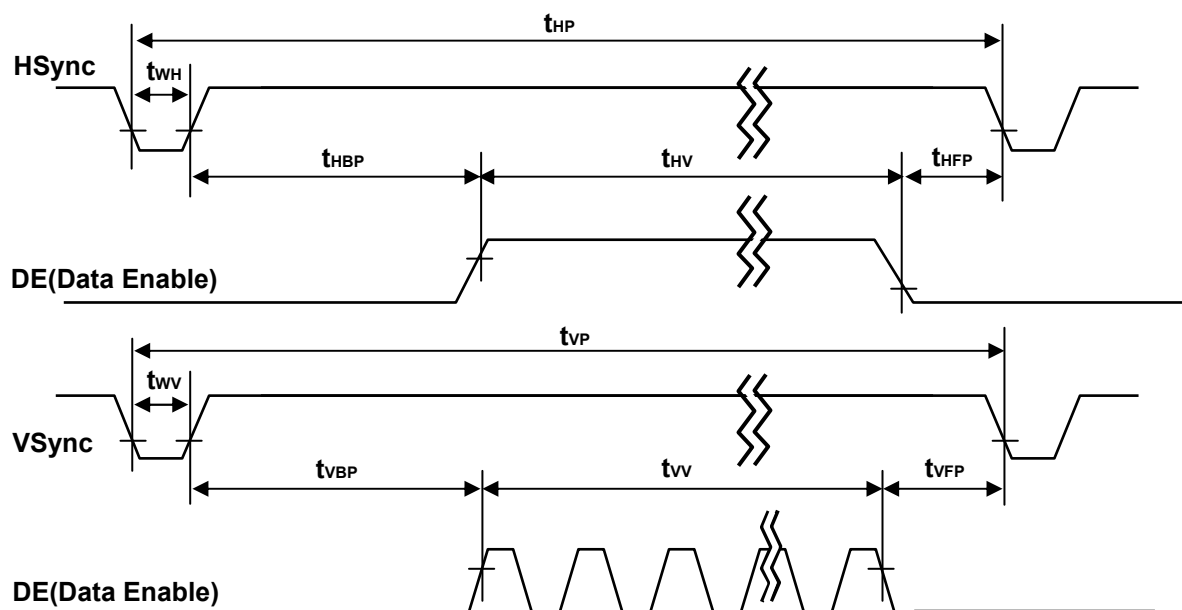
DE, Data

0.7VDD
0.3VDD

DCLK t_{CLK} 0.5 VDD

Data   Invalid data   Valid data   Invalid data

DE(Data Enable)

* Reference : Sync. Relation

* t_{HB} = t_{HFP} + t_{WH} + t_{HBP}

* t_{VB} = t_{VFP} + t_{WV} + t_{VBP}

HSync t_{WH} t_{HP}

t_{HBP} t_{HV} t_{HFP}

DE(Data Enable)

VSync t_{WV} t_{VP}

t_{VBP} t_{VV} t_{VFP}

DE(Data Enable)

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data		
		RED MSB → LSB R9 R8 R7 R6 R5 R4 R3 R2 R1 R0	GREEN MSB → LSB G9 G8 G7 G6 G5 G4 G3 G2 G1 G0	BLUE MSB → LSB B9 B8 B7 B6 B5 B4 B3 B2 B1 B0
Basic Color	Black	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	Red (1023)	1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	Green (1023)	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0
	Blue (1023)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 1
	Cyan	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1
	Magenta	1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 1
	Yellow	1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0
	White	1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1
RED	RED (000)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	RED (001)	0 0 0 0 0 0 0 0 0 1	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	...	...	...	...
	RED (1022)	1 1 1 1 1 1 1 1 1 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	RED (1023)	1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
GREEN	GREEN (000)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	GREEN (001)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 1	0 0 0 0 0 0 0 0 0 0
	...	...	...	...
	GREEN (1022)	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 0	0 0 0 0 0 0 0 0 0 0
	GREEN (1023)	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0
BLUE	BLUE (000)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
	BLUE (001)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 1
	...	...	...	...
	BLUE (1022)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 0
	BLUE (1023)	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1 1

3-6. Power Sequence

3-6-1. LCD Driving circuit
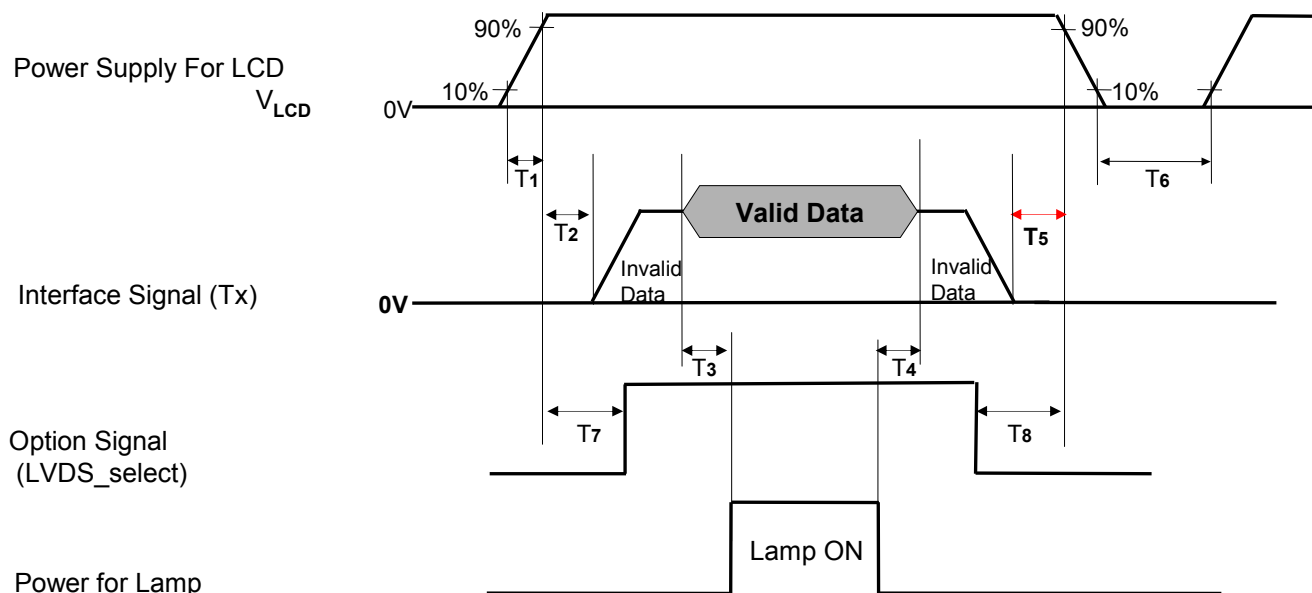
Table 8. POWER SEQUENCE

Parameter	Value			Unit	Notes
	Min	Typ	Max		
T1	0.5	-	20	ms	
T2	0	-	-	ms	4
T3	$3 \times (1/f_V)$	-	-	ms	3
T4	200	-	-	ms	3
T5	0	-	-	ms	
T6	2.0	-	-	s	5
T7	0	-	T2	ms	4
T8	0	-	-	ms	4

Note : 1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
3. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
4. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V_{LCD}), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
5. T6 should be measured after the Module has been fully discharged between power off and on period.

3-6-2. Sequence for Inverter

Power Supply For Inverter
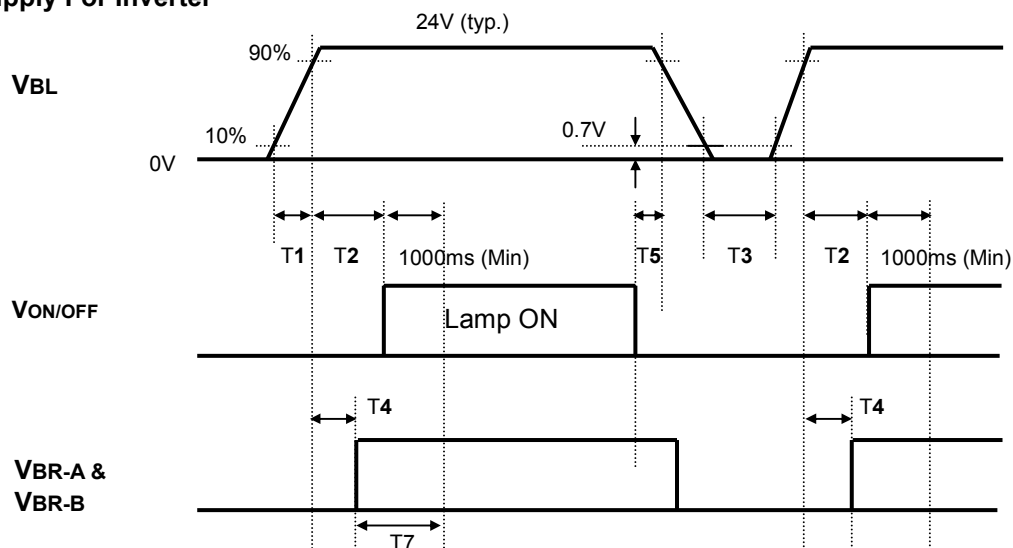
3-6-3. Deep condition for Inverter

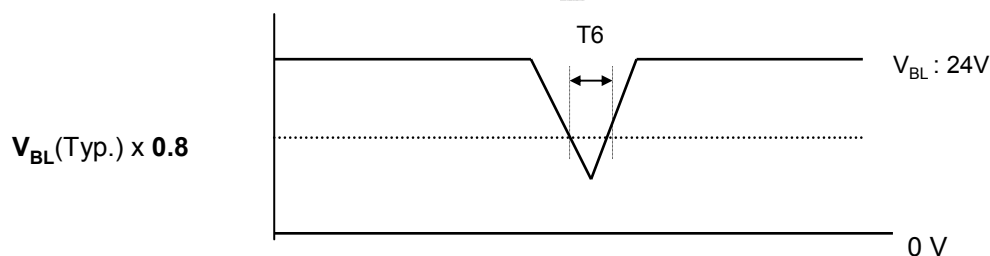### Table 9. Power Sequence for Inverter

Parameter	Values			Units	Remarks
	Min	Typ	Max		
T1	20	-	-	ms	1
T2	500	-	-	ms	
T3	200	-	-	ms	
T4	0		-	ms	2
T5	10	-	-	ms	
T6	-	-	10	ms	V_{BL}(Typ) x 0.8
T7	1000	-	-	ms	3

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.
2. T4(max) is less than T2.
3. In T7 section, VBR-A should be more than 1.65V and ExtVbr-B should be 100%.

Product Specification

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' for 30min in a dark environment at 25± 2℃. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

It is presented additional information concerning the measurement equipment and method in FIG. 1.
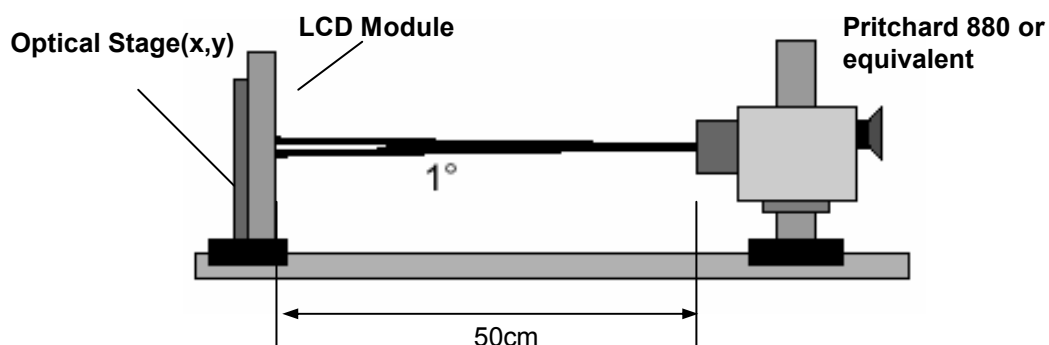

FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS   Ta= 25± 2℃, V_{LCD}=12.0V, fv=60Hz, Dclk=72MHz, V_{BR_A} =1.65V, V_{BR_B} =3.3V

Parameter		Symbol		Value			Unit	Note
				Min	Typ	Max		
Contrast Ratio		CR		800	1100			1
Surface Luminance, white		L_{WH}		400	500		cd/m^2	2
Luminance Variation		δ_{WHITE}	5P			1.3		3
Response Time	G to G			-	5	10	ms	4
Color Coordinates [CIE1931]	RED	Rx			0.636			
		Ry			0.335			
	GREEN	Gx			0.284			
		Gy		Typ -0.03	0.610	Typ +0.03		
	BLUE	Bx			0.144			
		By			0.063			
	WHITE	Wx			0.279			
		Wy			0.292			
Viewing Angle (CR>10)								
	x axis, right(φ=0°)	θr		89	-	-	degree	5
	x axis, left (φ=180°)	θl		89	-	-		
	y axis, up (φ=90°)	θu		89	-	-		
	y axis, down (φ=270°)	θd		89	-	-		
Gray Scale								6

Notes 1. Contrast Ratio(CR) is defined mathematically as :

      CR (Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)
      DCR (Dynamic CR) = Maximum CRn (n=1, 2, 3, 4, 5)

$$CRn = \frac{\text{Surface Luminance at position n with all white pixels}}{\text{Surface Luminance at position n with all black pixels}}$$

n = the Position number(1, 2, 3, 4, 5), For more information, see FIG 2.

  2. Surface luminance is luminance value at the center point across the LCD surface 50cm from the surface with all pixels displaying white.
     For more information, see FIG 2.

  3. The variation in surface luminance , δ WHITE is defined as :
     δ WHITE(5P) = Maximum($L_{on1}, L_{on2}, L_{on3}, ... , L_{on5}$) / Minimum($L_{on1}, L_{on2}, L_{on3}, ... , L_{on5}$)
     Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .
     For more information, see FIG 2.

  4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)

  5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information, see FIG 4.

  6. Gray scale specification
     Gamma Value is approximately 2.2. For more information, see Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
L0	0.09
L15	0.32
L31	1.10
L47	2.60
L63	4.90
L79	8.10
L95	12.1
L111	16.7
L127	21.6
L143	28.0
L159	35.4
L175	43.9
L191	53.3
L207	64.1
L223	75.8
L239	88.0
L255	100

Measuring point for surface luminance & measuring point for luminance variation

A : H / 4 mm
B : V / 4 mm
H : 819.6 mm
V : 460.8 mm
@ H,V : Active Area

FIG. 2 Measure Point for Luminance
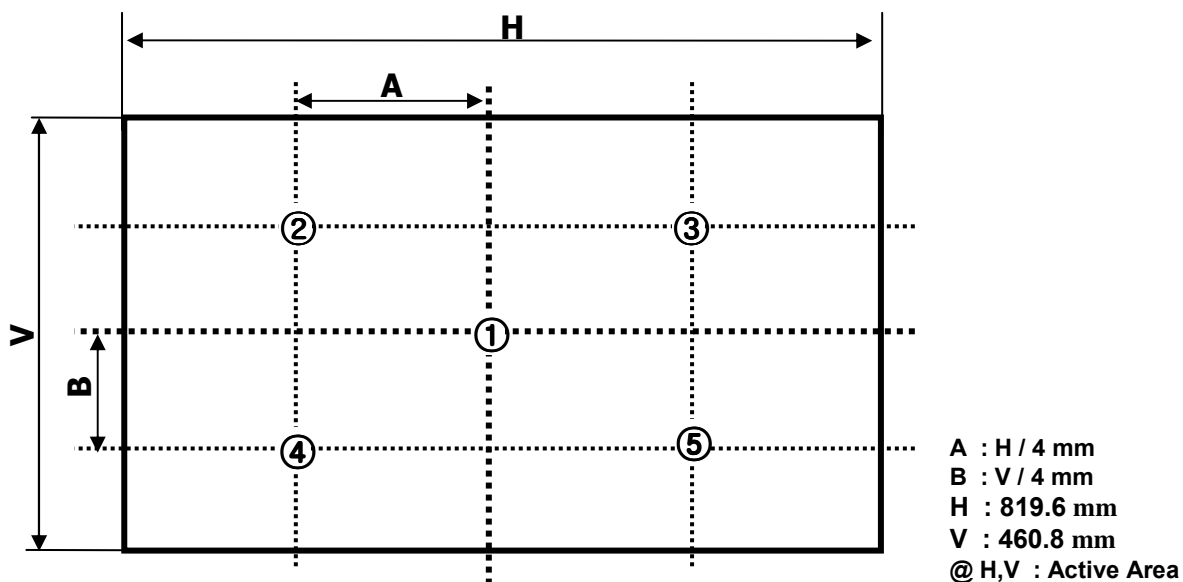
Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

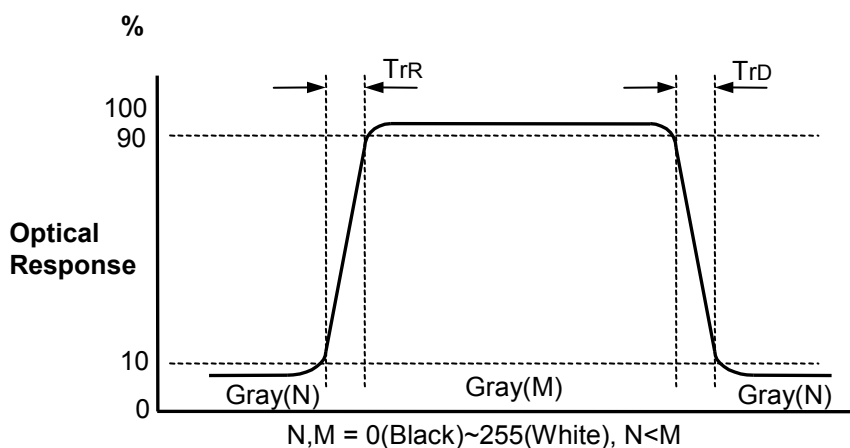N,M = 0(Black)~255(White), N<M

FIG. 3 Response Time

Product Specification

Dimension of viewing angle range

Normal

E

Y

$\phi = 90$ °, Up

$\phi = 180$ °, Left

θ

ϕ

$\phi = 0$ °, Right

$\phi = 270$ °, Down
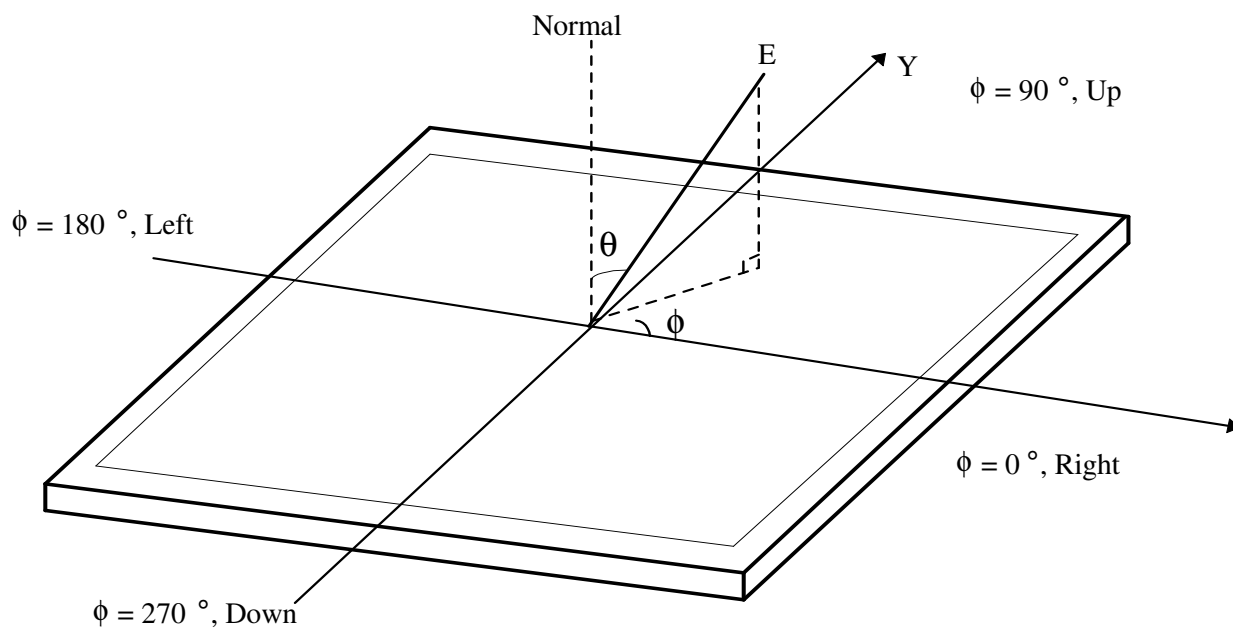
FIG. 4 Viewing angle

Product Specification

5. Mechanical Characteristics

The following items provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD module.

Table 12. MECHANICAL CHARACTERISTICS

Item		Value
Outline Dimension	Horizontal	877.0 mm
	Vertical	516.8 mm
	Depth	55.5 mm
Bezel Area	Horizontal	828.6mm
	Vertical	469.8mm
Active Display Area	Horizontal	819.6mm
	Vertical	460.8mm
Weight	9,000g(Typ.)/9,300(Max)	

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

Product Specification

<FRONT VIEW>
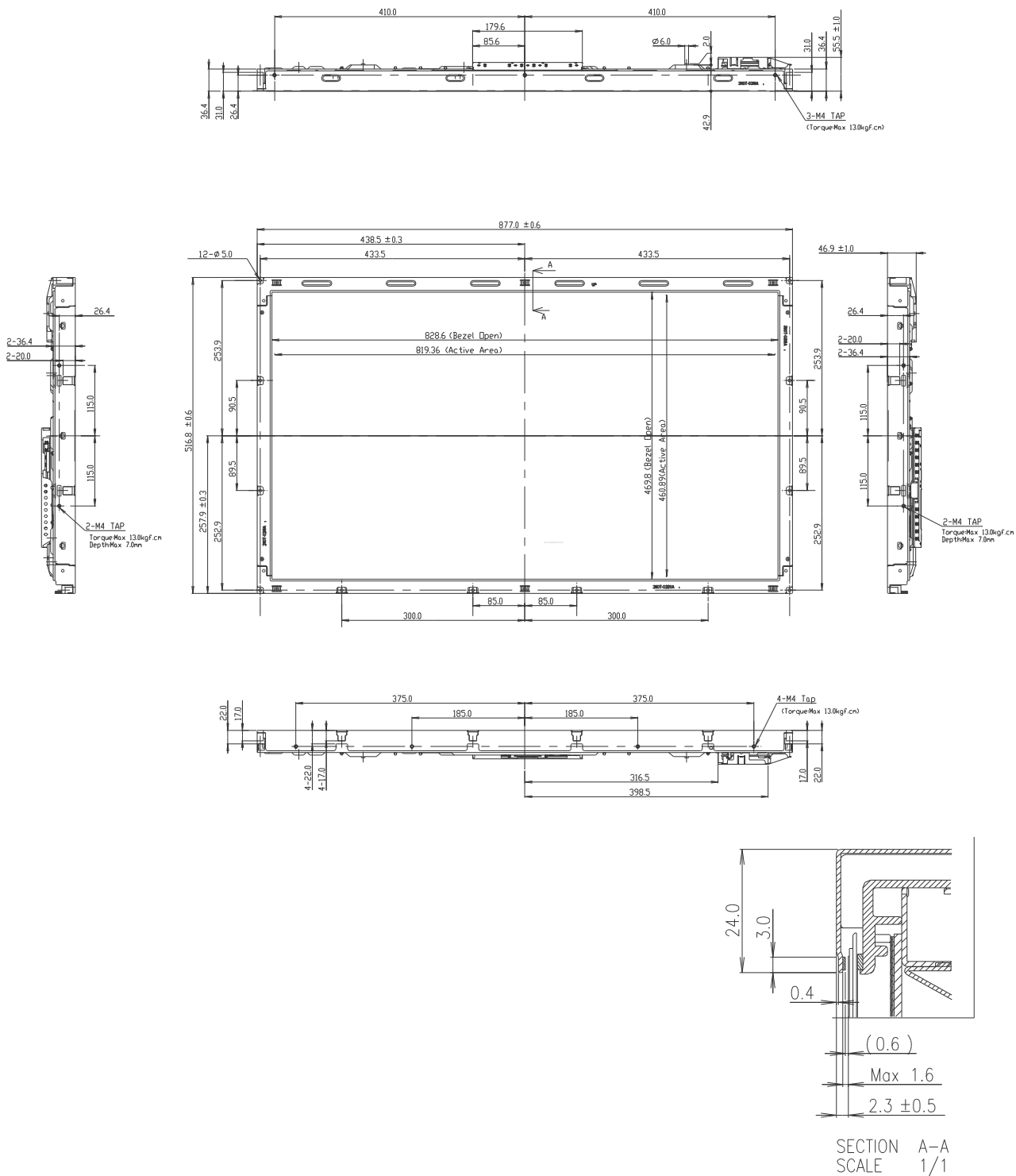
Product Specification

<REAR VIEW>
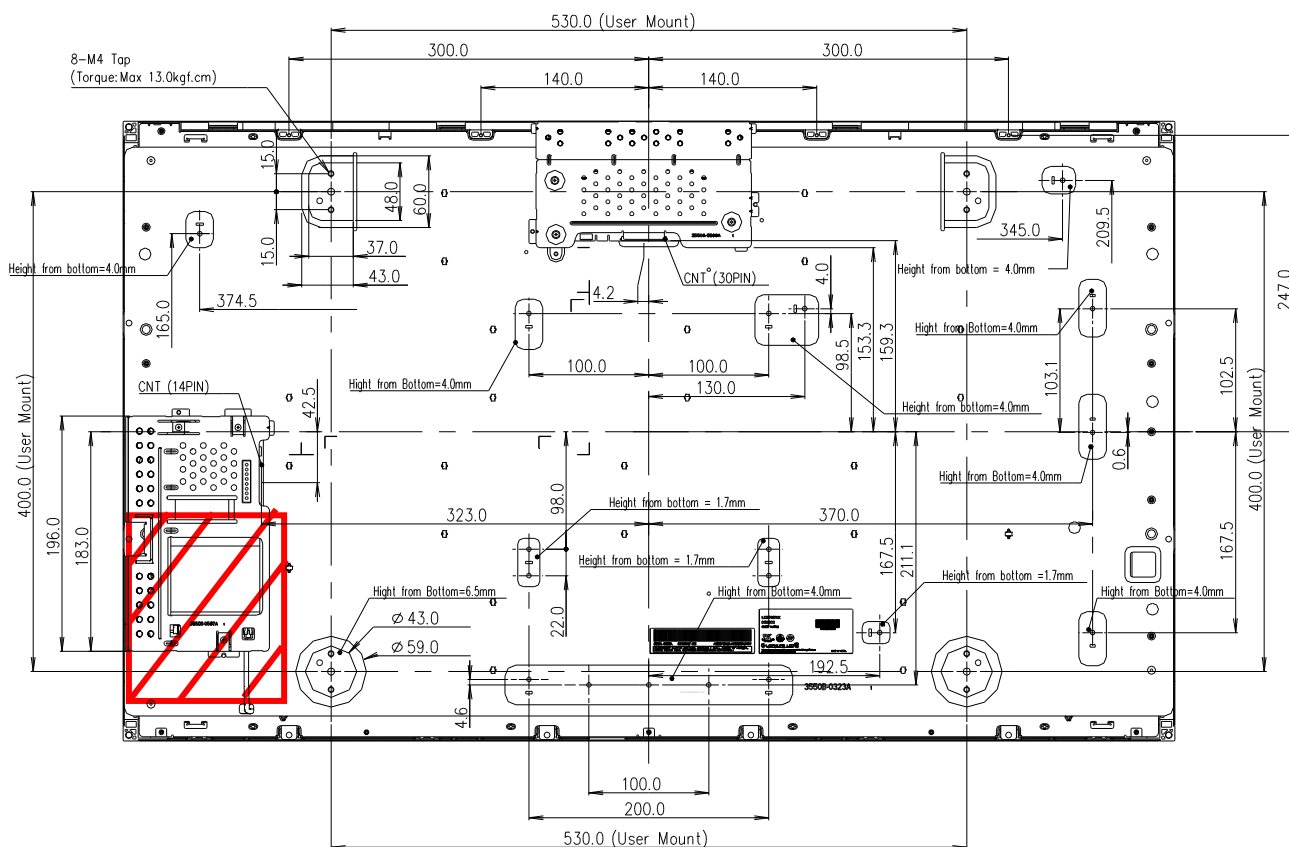
NOTES
1. Unspecified tolerances are to be ±0.5mm.
2. This drawing is only preliminary data and can be changed without notice.
3. Tilt and partial disposition tolerance of display area is as following.
   (1) X-Direction : IA-BI ≤ 1.5mm
   (2) Y-Direction : IC-DI ≤ 1.5mm
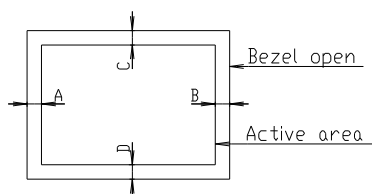
Notes : It should be recommended that any exterior materials do not go passing up the red area slanted.
(For example, electrical cable, system board , etc). Otherwise, it could cause that abnormal
display happens.

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60℃ 240h
2	Low temperature storage test	Ta= -20℃ 240h
3	High temperature operation test	Ta= 50℃ 50%RH 240h
4	Low temperature operation test	Ta= 0℃ 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min 　　　　One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : ±X, ±Y, ±Z 　　　　One time each direction
7	Humidity condition Operation	Ta= 40 ℃, 90%RH
8	Altitude　　operating 　　　　storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

Product Specification

7. International Standards

7-1. Safety

a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc.,
Standard for Audio, Video and Similar Electronic Apparatus.
b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association,
Standard for Audio, Video and Similar Electronic Apparatus.
c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002,
Safety requirements for Audio, Video and Similar Electronic Apparatus..

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and
Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI),
1992
b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound
and Television broadcast receivers and associated equipment"
CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information
Technology Equipment" International Special Committee on Radio Interference.
c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and
Television broadcast receivers and associated equipment"
EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information
Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC),
1988(Including A1:2000)

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

| A | B | C | D | E | F | G | H | I | J | K | L | M |

A,B,C : SIZE(INCH)   D : YEAR
E : MONTH   F ~ M : SERIAL NO.

Note
1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 4 pcs

b) Box Size : 968mm X 366mm X 595mm

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

(1) You must mount a module using specified mounting holes (Details refer to the drawings).

(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(4) You should adopt radiation structure to satisfy the temperature specification.

(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.

(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.

(9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
$V=\pm 200mV$(Over and under shoot voltage)

(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)

(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer

(4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.

(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

(7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.

(8) A screw which is fastened up the steels should be a machine screw.
(if not, it can causes conductive particles and deal LCM a fatal blow)

(9) Please do not set LCD on its edge.

(10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
    It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

(1) The protection film is attached to the bezel with a small masking tape.
    When the protection film is peeled off, static electricity is generated between the film and polarizer.
    This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
(3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Appendix 1-1.

■ **REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (Pin9="L")**

Host System 24 Bit		DS90C385 or Compatible		FI-X30SSL-HF		Timing Controller
RED0	51					
RED1	52	TxOUT0-	48	12		RxIN0-
RED2	54	TxOUT0+	47	13	100Ω	RxIN0+
RED3	55					
RED4	56					
RED5	3	TxOUT1-	46	15		RxIN1-
RED6	50	TxOUT1+	45	16	100Ω	RxIN1+
RED7	2					
GREEN0	4					
GREEN1	6	TxOUT2-	42	18		RxIN2-
GREEN2	7	TxOUT2+	41	19	100Ω	RxIN2+
GREEN3	11					
GREEN4	12					
GREEN5	14	TxCLKOUT-	40	21		RxCLKIN-
GREEN6	8	TxCLKOUT+	39	22	100Ω	RxCLKIN+
GREEN7	10					
BLUE0	15					
BLUE1	19	TxOUT3-	38	24		RxIN3-
BLUE2	20	TxOUT3+	37	25	100Ω	RxIN3+
BLUE3	22					
BLUE4	23			9	1KΩ	**LG** / DISM
BLUE5	24			30		LCD Test
BLUE6	16				150Ω	
BLUE7	18					
Hsync	27					
Vsync	28					
Data Enable	30			GND	GND	**LCD Module**
CLOCK	31					

Note: 1. The LCD Module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (DS90C385 or Compatible)
3. '7' means MSB and '0' means LSB at R,G,B pixel data.

Product Specification

Appendix 1-2.

■ **REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (Pin9="H")**

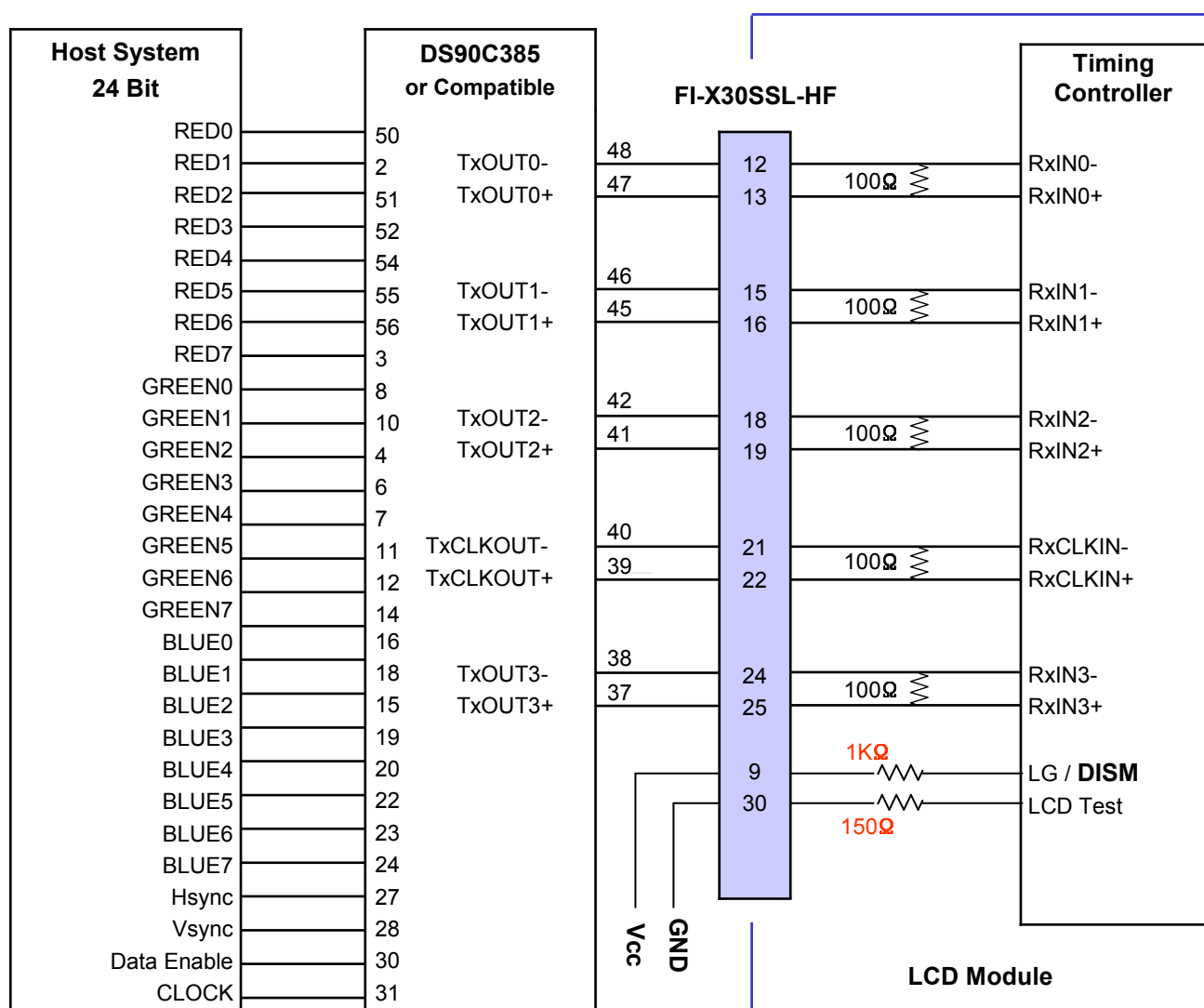| Host System 24 Bit	DS90C385 or Compatible	FI-X30SSL-HF		Timing Controller
RED0	50			
RED1	2	TxOUT0-	48 / 12	RxIN0-
RED2	51	TxOUT0+	47 / 13	100Ω	RxIN0+
RED3	52			
RED4	54			
RED5	55	TxOUT1-	46 / 15	RxIN1-
RED6	56	TxOUT1+	45 / 16	100Ω	RxIN1+
RED7	3			
GREEN0	8			
GREEN1	10	TxOUT2-	42 / 18	RxIN2-
GREEN2	4	TxOUT2+	41 / 19	100Ω	RxIN2+
GREEN3	6			
GREEN4	7			
GREEN5	11	TxCLKOUT-	40 / 21	RxCLKIN-
GREEN6	12	TxCLKOUT+	39 / 22	100Ω	RxCLKIN+
GREEN7	14			
BLUE0	16			
BLUE1	18	TxOUT3-	38 / 24	RxIN3-
BLUE2	15	TxOUT3+	37 / 25	100Ω	RxIN3+
BLUE3	19			
BLUE4	20		9	1KΩ	LG / **DISM**
BLUE5	22		30	LCD Test
BLUE6	23			150Ω
BLUE7	24			
Hsync	27			
Vsync	28			
Data Enable	30		Vcc	GND
CLOCK	31			**LCD Module**

Note: 1. The LCD Module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (DS90C385 or Compatible)
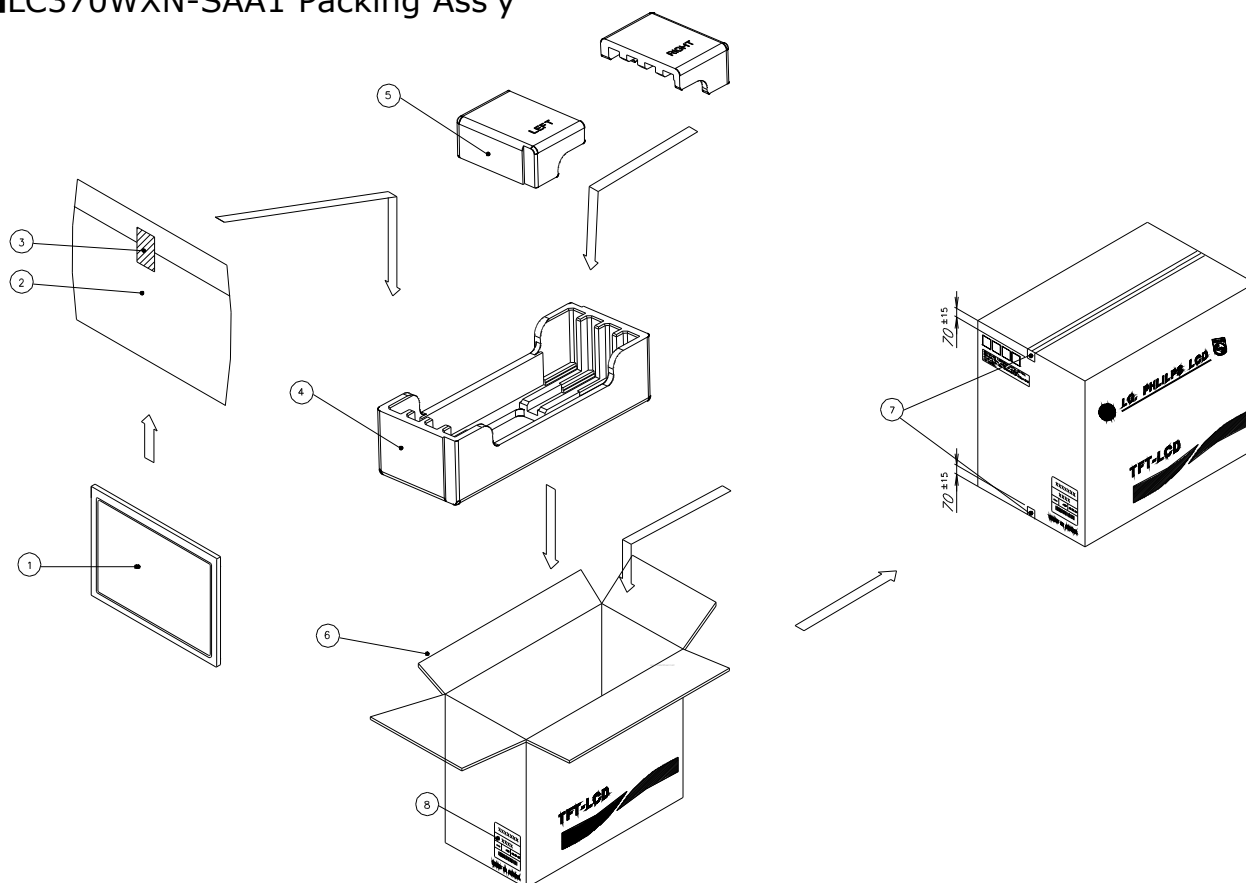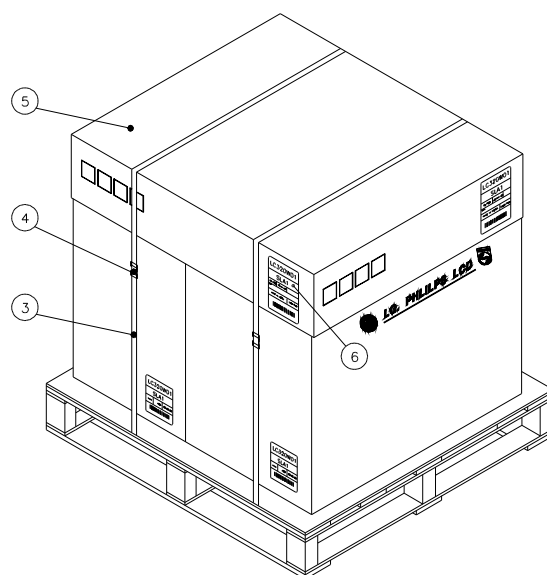3. '7' means MSB and '0' means LSB at R,G,B pixel data.

Appendix 2-1.

■LC370WXN-SAA1 Packing Ass'y

NO.	DESCRIPTION	MATERIAL
1	LCD MODULE	
2	BAG	AL
3	TAPE	MASKING 20MM X 50M
4	PACKING, BOTTOM	EPS
5	PACKING, TOP R_L	EPS
6	BOX	PAPER_DW3
7	TAPE	OPP 70MMX300M
8	LABEL	YUPO PAPER 100X100

Appendix 2-2.

■ LC370WXN-SAA1 Pallet Ass'y
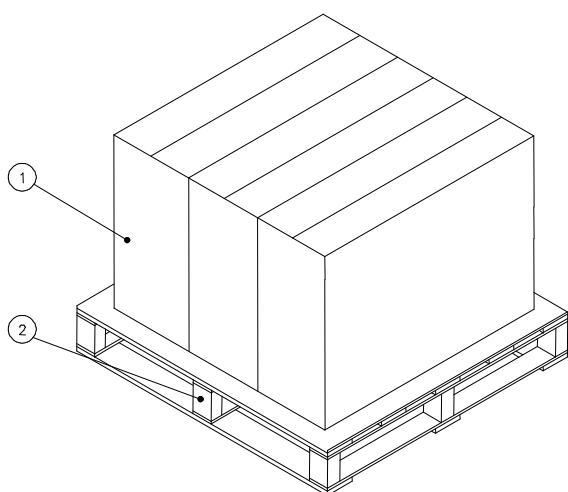
NO.	DESCRIPTION	MATERIAL
1	PACKING ASS'Y	
2	PALLET	Paper_1140X990X117.5
3	ANGLE, PACKING	SWR4
4	LABEL	YUPO PAPER
5	TAPE	OPP
6	BAND	PP
7	BAND, CLIP	CLIP 18MM

Appendix 2-3.

■ LCM Label

Model →	LC370WXN
	(SA)(A1)
	RoHS Verified
UL, TUV Mark →	
LPL Logo →	LG.PHILIPS LCD
US PATENT No. →	This product may be covered by one or more of the following US Patent Nos. 5,041,823 ; 5,061,920 ; 5,280,371 ; 5,835,139

Serial No. ←

Origin ← MADE IN KOREA
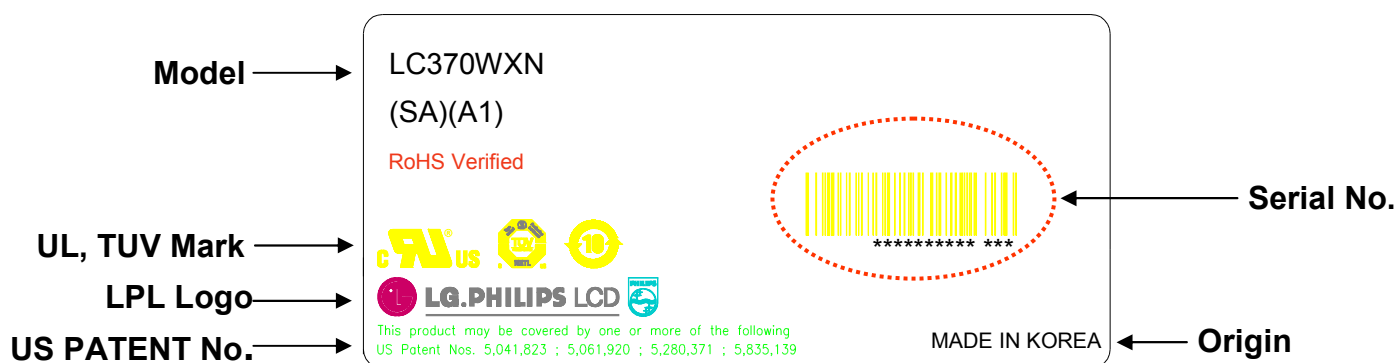
■ **Serial No. (See CAS 24page for more information)**

1	2	3	4	5	6	7	8	9	10	11	12	13

Inch   Year Month   Serial No.

M Ass'y Factory code
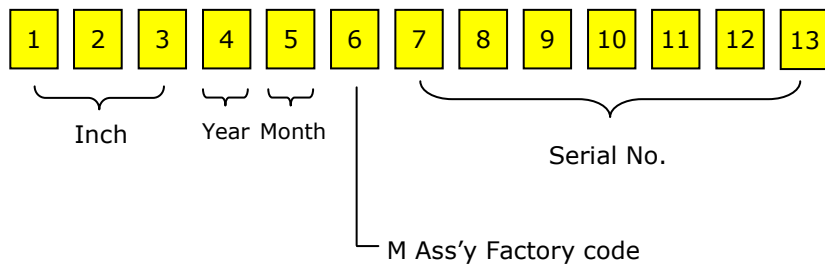
Appendix 2-4.

■ Box Label

LC370WXN		
SAA1		
4 PCS	LOT/MM-DD	
MADE IN KOREA		RoHS Verified
* * * * * * * * * * * * * * * *		

■ Pallet Label

LC370WXN		
SAA1		
12 PCS	LOT/MM-DD	
MADE IN KOREA		RoHS Verified
* * * * * * * * * * * * * * * *		

Appendix 3.

LVDS Data-Mapping info. (8bit)

■ **LVDS Select : "H" Data-Mapping (JEIDA format)**

RCLKP									
RCLKM									
RAP	R13'	R12'	G12	R17	R16	R15	R14	R13	R12	G12"
RBP	G14'	G13'	B13	B12	G17	G16	G15	G14	G13	B13"
RCP	B15'	B14'	DE	V_{SYNC}	H_{SYNC}	B17	B16	B15	B14	DE"
RDP	R11'	R10'	X	B11	B10	G11	G10	R11	R10	X"
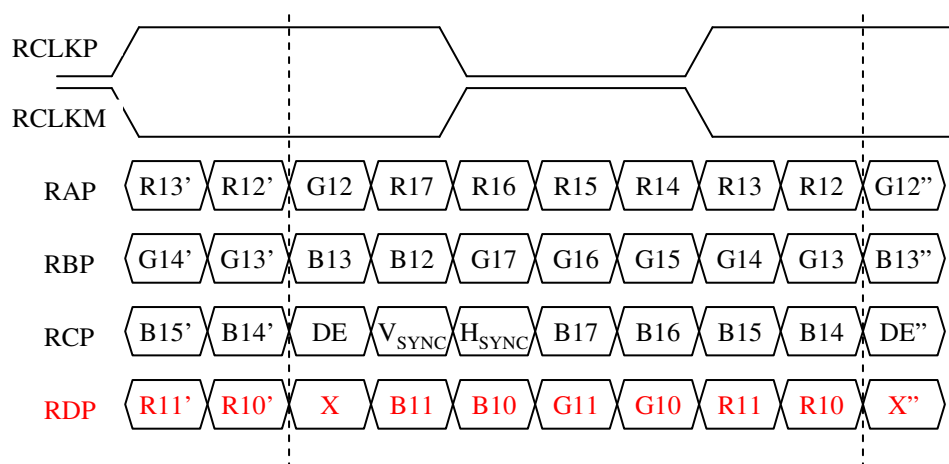
■ **LVDS Select : "L" Data-Mapping (VESA format)**

RCLKP									
RCLKM									
RAP	R11'	R10'	G10	R15	R14	R13	R12	R11	R10	G10"
RBP	G12'	G11'	B11	B10	G15	G14	G13	G12	G11	B15"
RCP	B13'	B12'	DE	V_{SYNC}	H_{SYNC}	B15	B14	B13	B12	DE"
RDP	R17'	R16'	X	B17	B16	G17	G16	R17	R16	X"