



SPECIFICATION FOR APPROVAL

() Preliminary Specification

() Final Specification

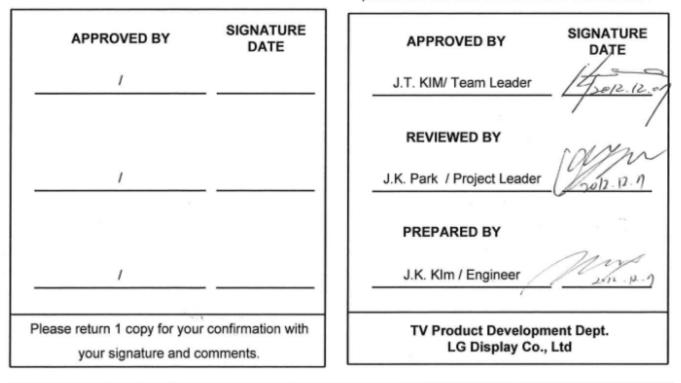
Title

42.0" WUXGA TFT LCD

BUYER	LGE
MODEL	

SUPPLIER	LG Display Co., Ltd.	
*MODEL	LC420DUE	
SUFFIX	SFR1 (RoHS Verified)	

*When you obtain standard approval, please use the above model name without suffix



Ver. 1.0

LC420DUE

Product Specification

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.1	Aug, 21, 2012	-	Preliminary Specification (First Draft)
0.2	Sep, 26, 2012		
0.3	Nov, 06, 2012	3	General Description
		5	Electrical characteristics
		6	Electrical characteristics (Notes 8)
		20	Mechanical characteristics
1.0	Dec, 07, 2012		Final Specification.

LC420DUE

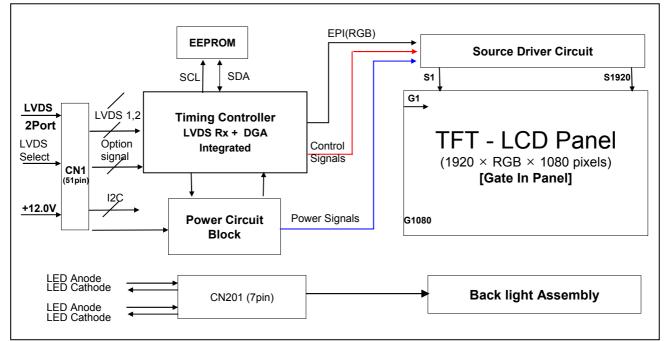
Product Specification

1. General Description

The LC420DUE is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 41.92 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7Million colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze < 1%)
Display Mode	Transmissive mode, Normally black
Weight	6.3 kg
Power Consumption	Total 54.85W [Logic= 6.4W, LED Driver=48.45W (IF_cathode=285mA))
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Luminance, White	300 cd/m ² (Center 1point ,Typ.)
Color Depth	8bit, 16.7Million colors
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Pixel Pitch	0.4833 mm x 0.4833 mm
Outline Dimension	955.8(H) X 554.4(V) X 35.0(B) mm (Typ.)
Active Screen Size	41.92 inches(1064.67mm) diagonal

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2. Absolute Maximum Ratings

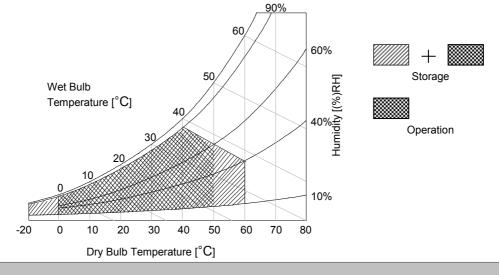
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Va	Value		Note
Faia	Falanielei		Min	Max	Unit	Note
Power Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	Vdc	
	Forward Voltage	VF1	-	+127	VDC	1
LED Input Voltage		VF2		+83		
T-Con Option Selection	T-Con Option Selection Voltage		-0.3	+4.0	VDC	
Operating Temperature	Operating Temperature		0	+50	°C	2,3
Storage Temperature		Tst	-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	2.2
Storage Humidity		Hs⊤	10	90	%RH	2,3

Note 1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below.
 - Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below $40^{\circ}C$ condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



3. Electrical Specifications

3-1. Electrical Characteristics

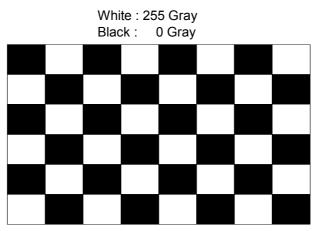
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight .

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
Falameter	Symbol	Min	Тур	Max	Unit	Note
Circuit :	Circuit :					
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC	
Power Input Current	ILCD	-	530	690	mA	1
		-	770	1000	mA	2
Power Consumption	PLCD		6.4	8.3	Watt	1
Rush current	IRUSH	-	-	3.0	A	3

Note 1. The specified current and power consumption are under the V_{LCD}=12.0V, Ta=25 \pm 2°C, f_V=60Hz condition, and mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).
- 4. Ripple voltage level is recommended under $\pm 5\%$ of typical voltage



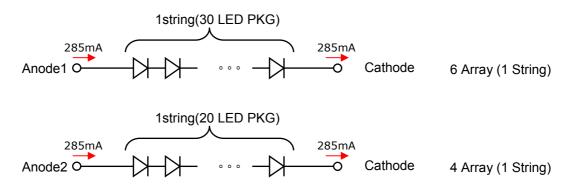
Mosaic Pattern(8 x 6)

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol	Symbol Values				Note
i aia	Faranteter		Min	Min Typ Max	Max	Unit	Note
Backlight Assem	bly :						
Forward Current	Anode	I _{F (anode)}		285		mAdc	±5%
(one array)	Cathode	I _{F (cathode)}	271	285	299	mAdc	2, 3
		V _{F1}	93	102	111	Vdc	4
Forward Voltage		V _{F2}	62	68	74	Vac	4
Power Consumption	n	P _{BL}	-	48.45	54.15	W	5
Burst Dimming Dut	ty	On duty	1		100	%	
Burst Dimming Frequency		1/T	95		182	Hz	7
LED Array : (APP	ENDIX-V)		-	-	-	~	-
Life Time			30,000			Hrs	6

Notes :The design of the LED driver must have specifications for the LED array in LCD Assembly. The electrical characteristics of LED driver are based on Constant Current driving type. The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed. When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD– Assembly should be operated in the same condition as installed in your instrument.

- 1. Electrical characteristics are based on LED Array specification.
- 2. Specified values are defined for a Backlight Assembly. (IBL :10 LED array/LCM)
- Each LED array has 1 anode terminal and 1 cathode terminal. The forward current (I_F) of the anode terminal is 285mA and it supplies 285mA into one string, respectively



- 4. The forward voltage (V_F) of LED array depends on ambient temperature (Appendix-VI)
- 5. Maximum level of power consumption is measured at initial turn on.
- Typical level of power consumption is measured after 1hrs aging at $25 \pm 2^{\circ}$ C.
- The life time (MTTF) is determined as the time at which brightness of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at 25 ± 2°C, based on duty 100%.
- The reference method of burst dimming duty ratio. It is recommended to use synchronous V-sync frequency to prevent waterfall (Vsync * 2 =Burst Frequency)

Though PWM frequency is over 182Hz (max252Hz), function of backlight is not affected.

3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and 2pin,2pin connector is used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF(manufactured by JAE) or GT05P-51S-H38(manufactured by LSM) or IS050-C51B-C39(manufactured by UJU)
- Mating Connector : FI-R51HL(JAE) or compatible

No	Symbol	Description	No	Symbol	Description
1	NC or GND	No Connection or Ground	27	NC	No Connection
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Note 4)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Note 4)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection (Note 4)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection (Note 4)	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	NC	No Connection
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	NC	No Connection
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	NC	No Connection	50	VLCD	Power Supply +12.0V
25	NC	No Connection	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

Note 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)
- 5. Specific pin No. #44 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-2-2. Backlight Module

[CN201]

1) LED Array ass'y Connector (Plug)

: SMH200-07

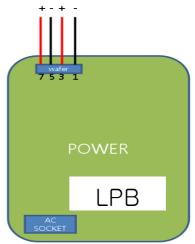
(black color, manufactured by Yeonho)

2) Mating Connector (Receptacle)

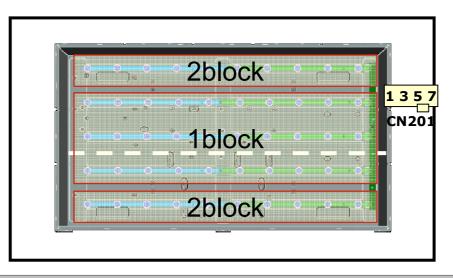
: SMAW200A-H07AA(Dip Type) 20037WR-H07AA(SMD Type) (black color, manufactured by Yeonho)

Table 5.	BACKLIGHT CONNECTOR PIN CONFIGURATION(CN201)

No	Symbol(CN201)	Description
1	Cathode2	LED output Current
2	N.C	Open
3	Anode2	LED input Current
4	N.C	Open
5	Cathode1	LED output Current
6	N.C	Open
7	Anode1	LED input Current



Rear view of LCM



3-3. Signal Timing Specifications

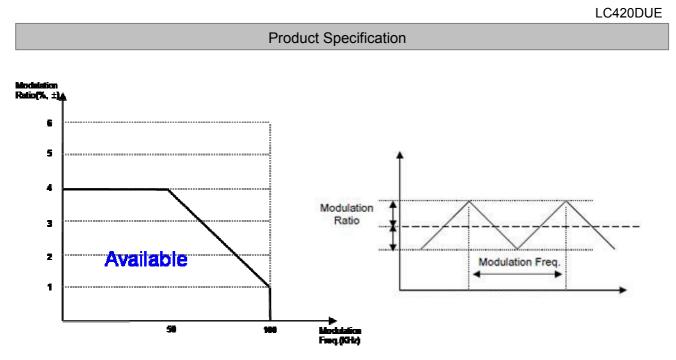
Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

ITE	м	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tн∨	960	960	960	tCLK	1920 / 2
Horizontal	Horizontal Blank		100	140	240	tCLK	1
	Total	tHP	1060	1100	1200	tCLK	
	Display Period	tvv	1080	1080	1080	Lines	
Vertical	Blank	tvв	20 (228)	45 (270)	69 (300)	Lines	1
	Total	tvp	1100 (1308)	1125 (1350)	1149 (1380)	Lines	

Table 6. TIMING TABLE (DE Only Mode)

ITEM DCLK Horizontal		Symbol	Min	Тур	Мах	Unit	Note
	DCLK	fclk	63.00	74.25	78.00	MHz	
	Horizontal	fн	57.3	67.5	70	KHz	2
Frequency	Vertical	f∨	57 (47)	60 (50)	63 (53)	Hz	2 NTSC : 57~63Hz (PAL : 47~53Hz)

- Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.
 - 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
 - Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by (7 – 0.06*Fmod), where Modulation Frequency (FMOD) unit is KHz. LVDS Receiver Spread spectrum Clock is defined as below figure
 - * Timing should be set based on clock frequency.

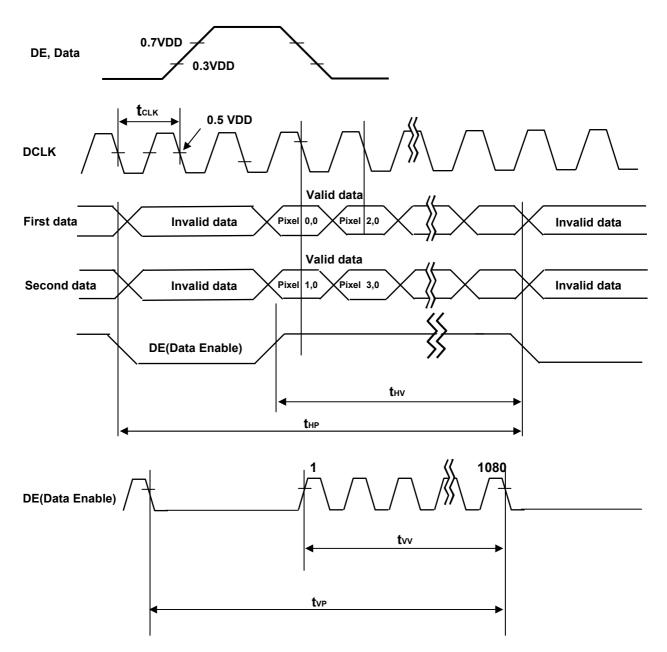


* Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD)

- 1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
- 2. Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.

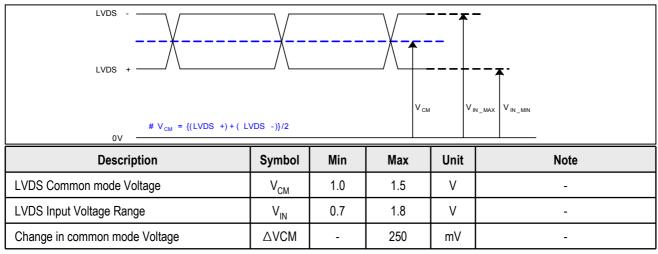
3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram

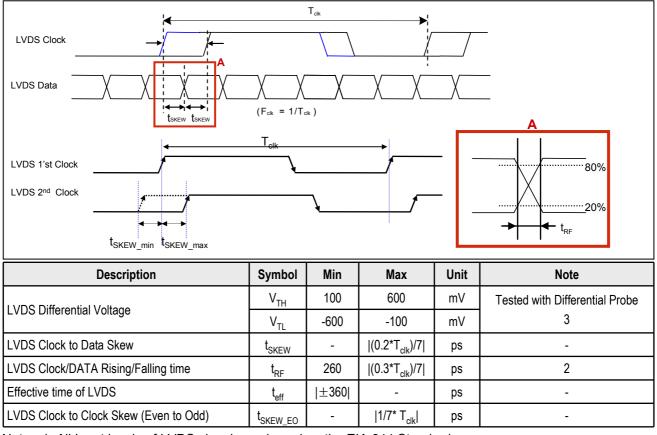


3-4-2. LVDS Input Signal Characteristics

1) DC Specification



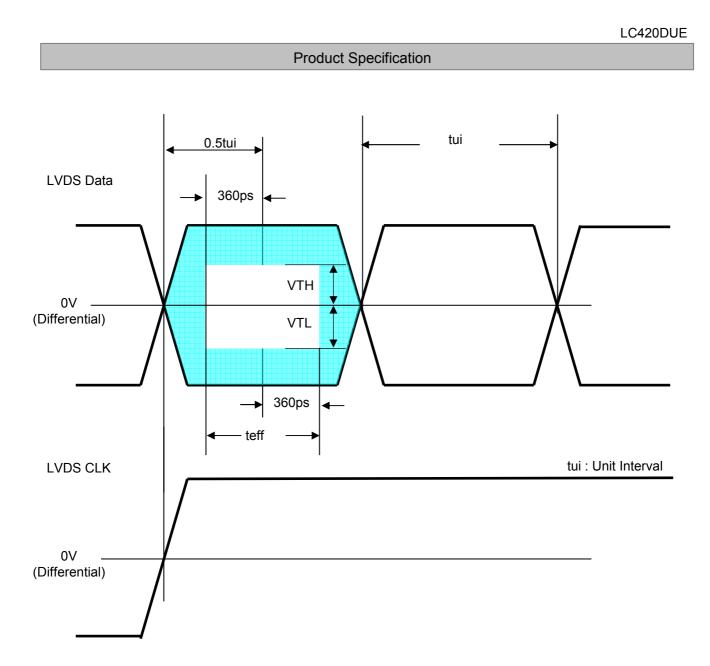
2) AC Specification



Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range. 3. LVDS Differential Voltage is defined within t_{eff}

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* This accumulated waveform is tested with differential probe

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

											I	npu	t Co	olor	Dat	а									
	Color		~ -		RE	D				_			GRE	EEN			_				BL	UE			
			SB					LS			ISB					LSI			ISB					LS	
	[7 R6													G1 (7 B6						_
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED						•																			
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN		ĺ												•				ĺ							
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		İ																İ							
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

3-6-1. LCD Driving circuit

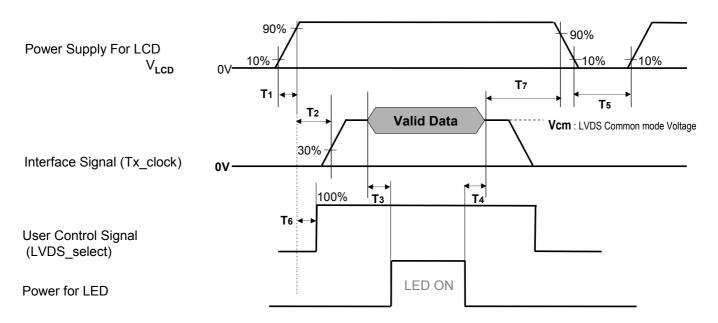


Table 8. POWER SEQUENCE

Deremeter		Value		l leit	Notos
Parameter	Min	Unit	Notes		
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
Т3	400	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	s	4
T6	0	-	T2	ms	5
T7	0	-	-	ms	6

Note :

- 1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
 - 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
 - 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 - 4. T5 should be measured after the Module has been fully discharged between power off and on period.
 - 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}),

it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.

- 6. It is recommendation specification that T7 has to be 0ms as a minimum value.
- * Please avoid floating state of interface signal at invalid period.
- * When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

4. Optical Specification

Table 10. OPTICAL CHARACTERISTICS

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values are specified at 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°. FIG. 1 shows additional information concerning the measurement equipment and method.

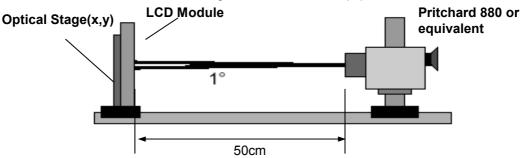


FIG. 1 Optical Characteristic Measurement Equipment and Method

Ta= 25 \pm 2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz,

IF _{cathode} = 285mA (Typ)

							п _са		(90)			
	D	arameter	Cumph			Value		Unit	Note			
	Pa	rameter	Symb	001	Min	Тур	Max	Unit	Note			
Contrast	Ratio		CR		CR		850	1200	-		1	
Surface	Surface Luminance, white		L _{WH}	2D	240	300	-	cd/m ²	2			
Luminan	Luminance Variation		δ _{WHITE}	9P	60	70	-	%	3			
Doopopo	Response Time Gray-to-Gray Uniformity		G to C	θ σ	-	6	9	ms	5			
Respons			G to G	BW	-	9	13		4			
			Rx			0.649						
		RED	Ry	Ry		0.333]					
		Gx	Gx		0.301	1						
	ordinates	GREEN	Gy Bx By		Тур	0.590	Тур					
[CIE193	1]				-0.03	0.149	+0.03					
		BLUE				0.061	1					
			Wx			0.281						
		WHITE	Wy	Wy		Wy	Wy		0.288			
Color Ter	mperature					10,000		К				
Color Ga	mut					68	1	%				
		x axis, right(_{\$=0°})	θr		89	-	-					
Viewing 2D		x axis, left (ϕ =180°)	θΙ		89	-	- 1	doaraa				
U U	(CR>10)	y axis, up (∳=90°)	θu		89	-	-	degree	6			
		y axis, down (∳=270°)	θθ		89	-	-					
Gray Sca	ale				-	-	-		7			

Note : 1. Contrast Ratio(CR) is defined mathematically as :

Surface Luminance with all white pixels Contrast Ratio = Surface Luminance with all black pixels

It is measured at center 1-point.

- 2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : $\delta \text{ WHITE(9P)} = \text{Minimum } (L_{\text{on1}}, L_{\text{on2}} \sim L_{\text{on8}}, L_{\text{on9}}) \text{ / Maximum } (L_{\text{on1}}, L_{\text{on2}} \sim L_{\text{on8}}, L_{\text{on9}})^*100$ Where L_{on1} to L_{\text{on9}} are the luminance with all pixels displaying white at 9 locations For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transit from any gray to white (Rise Time, Tr_{P}) and from any gray to black (Decay time, Tr_D). For additional information see the FIG. 3. * G to G_{BW} Spec stands for average value of all measured points.

5. G to G $_{\sigma}$ is Variation of Gray to Gray response time composing a picture

G to G (
$$\sigma$$
) = $\sqrt{\frac{\Sigma(Xi-u)^2}{N}}$ Xi = Individual Data
u = Data average
N : The number of Data

- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.

Gray Level	Luminance [%] (Typ)
LO	0.08
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

Table 11. GRAY SCALE SPECIFICATION

Measuring point for surface luminance & measuring point for luminance variation.

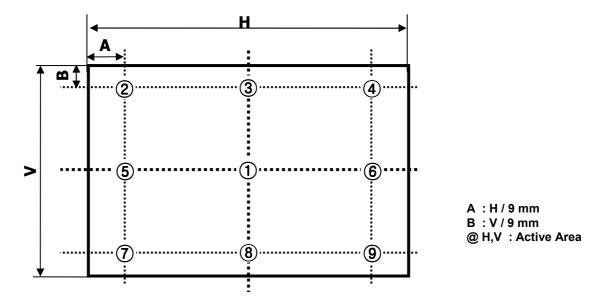


FIG. 2 9 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".

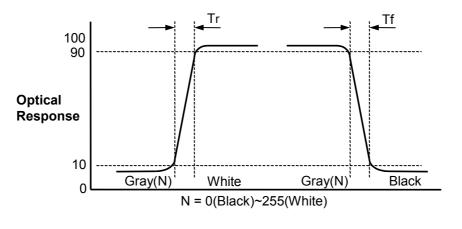
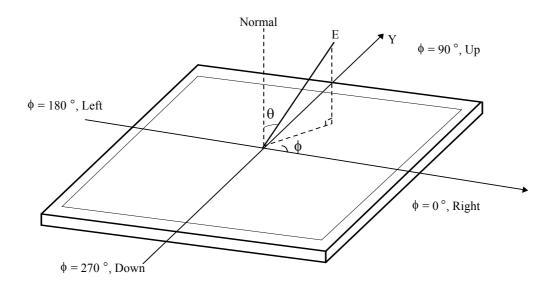
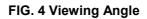


FIG. 3 Response Time

Dimension of viewing angle range





5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

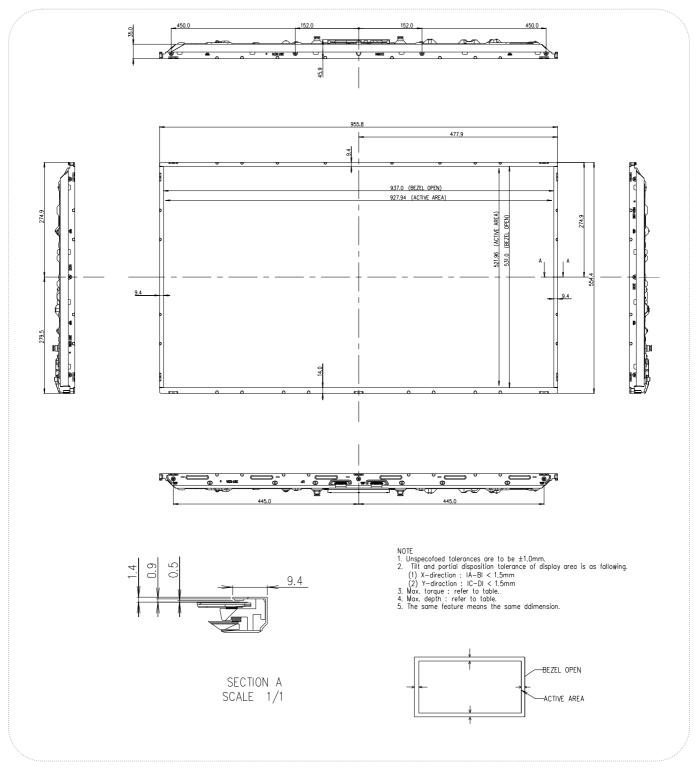
Table 12. MECHANICAL CHARACTERISTICS

Item	Value	;
	Horizontal	955.8 mm
Outline Dimension	Vertical	554.4 mm
	Depth	35.0 mm
	Horizontal	937.0 mm
Bezel Area	Vertical	531.0 mm
	Horizontal	927.94 mm
Active Display Area	Vertical	521.96 mm
Weight	6.3 Kg (Typ.), 7.0 kg (Max.)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

Product Specification

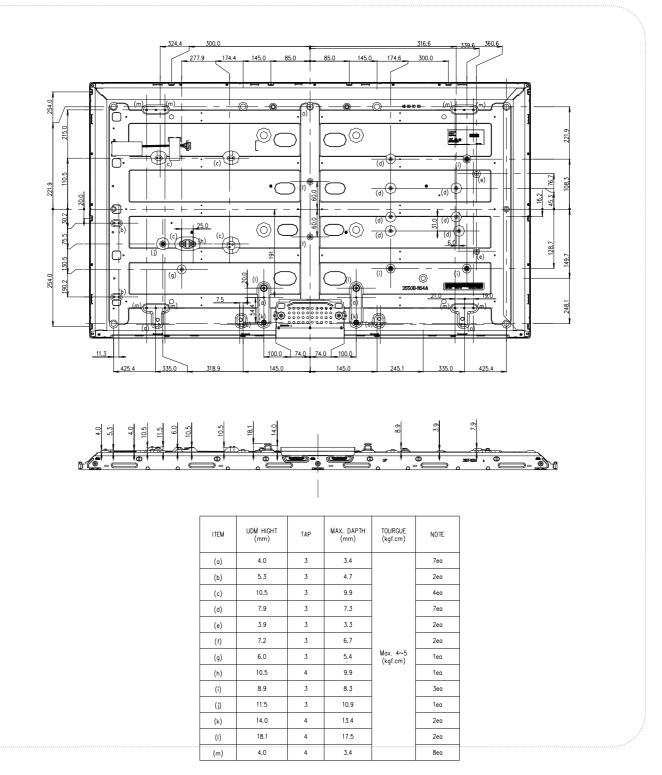




LC420DUE

Product Specification

[REAR VIEW]



6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition					
1	High temperature storage test	Ta= 60°C 240h					
2	Low temperature storage test	Ta= -20°C 240h					
3	High temperature operation test	Ta= 50°C 50%RH 240h					
4	Low temperature operation test	Ta= 0°C 240h					
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, Each direction per 10 min					
6	Shock test (non-operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction					
7	Humidity condition Operation	Ta= 40 °C ,90%RH					
8	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft					

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, Underwriters Laboratories Inc. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class1M)

2. Caution

: LED inside. Class 1M laser (LEDs) radiation when open. Do not open while operating.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

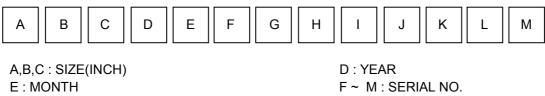
7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

8. Packing

8-1. Information of LCM Label

a) Lot Mark



Note

_	I. ILAN										
	Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
	Mark	А	В	С	D	E	F	G	Н	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one Pallet : 20 pcs
- b) Pallet Size : 1300 mm(W) X 1140mm(D) X 790 mm(H)

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
- And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
- (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

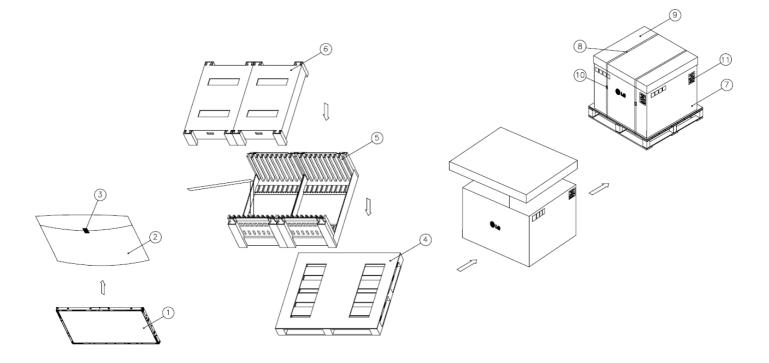
- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
- It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

9-6. Handling Precautions for Protection Film

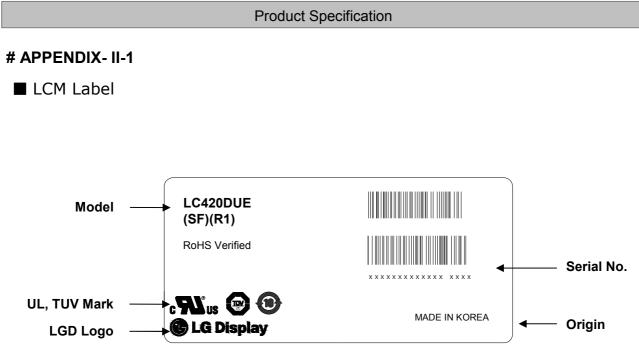
- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

APPENDIX-I

Pallet Ass'y



NO.	DESCRIPTION	MATERIAL
1	LCD Module	42" LCD
2	BAG	AL BAG
3	TAPE	MASKING 20MMX50M
4	PALLET	Plywood 1300X1140X125.5mm
5	PACKING,BOTTOM	PAPER
6	PACKING,TOP	PAPER
7	ANGLE, PACKING	PAPER
8	BAND	PP
9	ANGLE.COVER	PAPER
10	BAND,CLIP	STEEL or PP
11	LABEL	YUPO 80G 100X70



Production site

- LG Display (Paju) Co., LTD
- LG Display (Guangzhou) Co., LTD
- LG Display (Poland) Co., LTD

Note 1. The origin of LCM Label will be changed according to the production site.

APPENDIX- II-2

Pallet Label

<	100.0	>	1
	C420[
	SFF	K 1	
20 PCS	001/01-01		0.0
MADE	IN KOREA	RoHS Verified	
	xxxxxxxxx	XXXX XXX	

APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "L" or "NC")

Host System		тнс	63LVD103				
30 Bit		or C	ompatible				Timing
RED0		33					Controller
RED1		34		FI-RE51S-HF			
RED2		35					
RED3		36		31			
RED4		37	TA-	30	12	<u>100Ω</u> >	RO0N
RED5		38	TA+	30	13	10025	RO0P
RED6		59					
RED7		61	TB-	29	14		RO1N
RED8		4		28		100Ω ≶	
RED9		5	TB+		15		RO1P
GREEN0		40		25			
GREEN1		41	TC-		16		RO2N
GREEN2		42	TC+	24	17	<u>100</u> Ω 🗧	RO2P
GREEN3		44					
GREEN4		45	TCLK-	23	19		ROCLKN
GREEN5		46		22		<u>100</u> Ω <	
GREEN6		62	TCLK+		20		ROCLKP
GREEN7		63		21			
GREEN8		6	TD-		22	>	RO3N
GREEN9		8	TD+	20	23	<u>100</u> Ω ໂ	RO3P
BLUE0		48					
BLUE1		49					
BLUE2		50					
BLUE3		52					
BLUE4		53					
BLUE5		54			7		VESA/ JEIDA
BLUE6		64					
BLUE7		1				1	
BLUE8		9			1		
BLUE9		11]
Hsync		55		G		LCM Module	
Vsync		57		GND			
Data Enable		58					
CLOCK	<u>}</u>	12]			

Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '7' means MSB and '0' means LSB at R,G,B pixel data.

Ver. 1.0

APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "H")

Host System	тнс	63LVD103				
30 Bit	or C	ompatible				Timing
RED0	4					Controller
RED1	5		FI-RE51S-HF			
RED2	59					
RED3	 61		31			
RED4	33	TA-	30	12	<u>100Ω</u> }	RO0N
RED5	34	TA+	30	13	10025	ROOP
RED6	35					
RED7	36	TB-	29	14		RO1N
RED8	37		28		100Ω ≷	
RED9	38	TB+		15		RO1P
GREEN0	6		25			
GREEN1	8	TC-		16	>	RO2N
GREEN2	 62	TC+	24	17	100Ω ≶	RO2P
GREEN3	 63					
GREEN4	 40	TCLK-	23	19		ROCLKN
GREEN5	41		22		100Ω ≷	
GREEN6	42	TCLK+		20		ROCLKP
GREEN7	44		21			
GREEN8	45	TD-		22	<u> </u>	RO3N
GREEN9	46	TD+	20	23	100Ω 🗧	RO3P
BLUE0	9					
BLUE1	11					
BLUE2	64					
BLUE3	1					
BLUE4	 48					
BLUE5	 49			7		VESA / JEIDA
BLUE6	 50					
BLUE7	52				I	
BLUE8	 53					
BLUE9	54					
Hsync	 55		<u><</u>		LCM Module	
Vsync	57		VCC			
Data Enable	58					
CLOCK	12					

Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

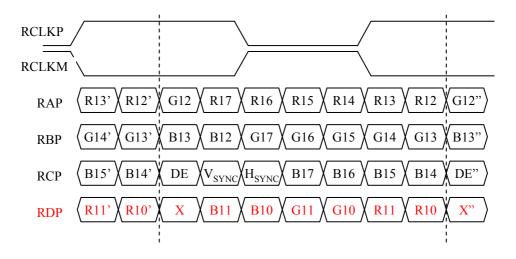
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '7' means MSB and '0' means LSB at R,G,B pixel data.

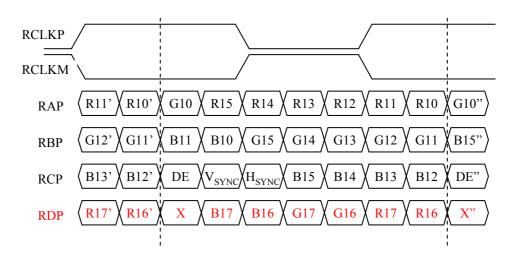
APPENDIX- IV

LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



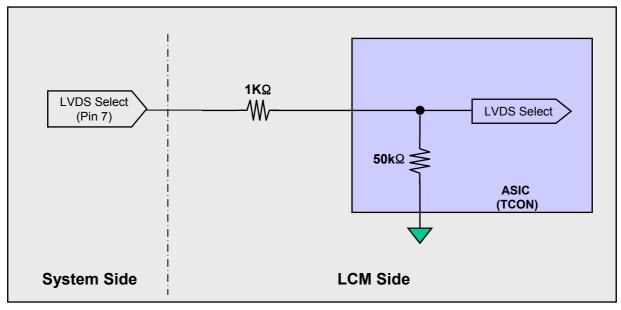
2) LVDS Select : "L" Data-Mapping (VESA format)



APPENDIX- V

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



APPENDIX- VII

■ LED Array Electrical Spec

	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Voltage	Vf	lf = 285mA	15.5		22.2	V

Forward Current vs. Forward Voltage

