

Global LCD Panel Exchange Center



LC420EUN

Product Specification

SPECIFICATION FOR APPROVAL

(, ,	Preliminary	Specification
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() Final Specification

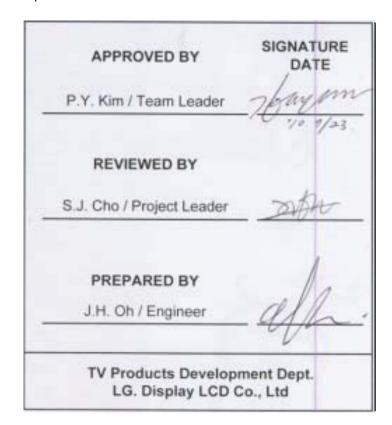
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BUYER	GENERAL
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC420EUN
SUFFIX	SCV1

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
Please return 1 copy for your o	confirmation with
your signature and cor	mments.



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RECORD OF REVISIONS

0.1 Aug. 11. 2010 - Preliminary Specification (First Draft) 0.2 Aug. 30, 2010 29 # APPENDIX-I Final Sept.08.2010 23 5. Mechanical Characteristics	Revision No.	Revision Date	Page	Description
7 ELECTRICAL CHARACTERISTICS	0.1	Aug. 11. 2010	-	Preliminary Specification (First Draft)
	0.2	Aug, 30, 2010	29	# APPENDIX-I
Final Sept.08.2010 23 5. Mechanical Characteristics			7	ELECTRICAL CHARACTERISTICS
	Final	Sept.08.2010	23	5. Mechanical Characteristics
	rinai	Sept.08.2010		5. Mechanical Characteristics

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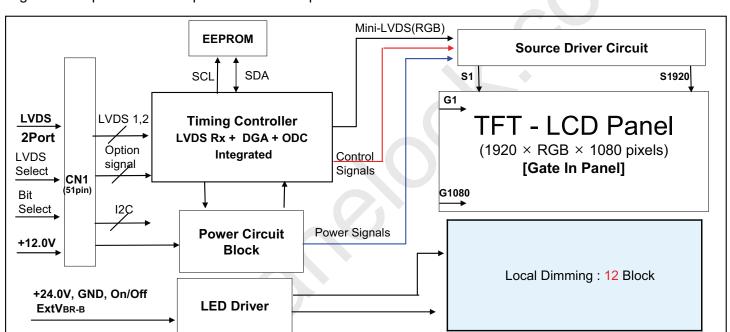
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1. General Description

The LC420EUN is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 42.02 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.06Billion colors.

It has been designed to apply the 10-bit 4-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	42.02 inches(1067.31mm) diagonal
Outline Dimension	983.0(H) x 576.0 (V) x 51.0 mm(D) (Typ.)
Pixel Pitch	0.4845 mm x 0.4845 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10bit(D) , 1.06Billon colors
Luminance, White	360 cd/m² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 95.8W (Typ.) [Logic= 7.8W, LED Driver= 88(TBD)W (ExtVbr_B=100%)]
Weight	11.3 Kg (Typ.) ,12.0Kg(Max)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)

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2. Absolute Maximum Ratings

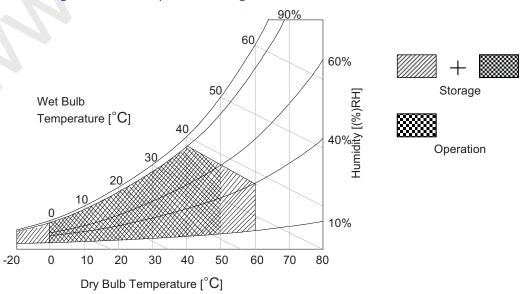
The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Dara	meter	Symbol	Valu		Unit	Note
raia	meter	Syllibol	Min	Max	Oilit	Note
Dower Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
Power Input Voltage	Driver	VBL	-0.3	+ 27.0	VDC	
Driver Central Valtage	ON/OFF	Voff / Von	-0.3	+5.5	VDC	1
Driver Control Voltage	Brightness	EXTVBR-B	0.0	+5.5	VDC	
T-Con Option Selection Voltage Operating Temperature Storage Temperature Panel Front Temperature		VLOGIC	-0.3	+4.0	VDC	
		Тор	0	+50	°C	2.2
		Tst	-20	+60	°C	2,3
		Tsur	-	+68	°C	4
Operating Ambient Hum	idity	Нор	10	90	%RH	0.0
Storage Humidity		Hst	10	90	%RH	2,3

Note1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may degraded in case of improper thermal management in final product design.



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3. Electrical Specifications

3-1. Electrical Characteristics

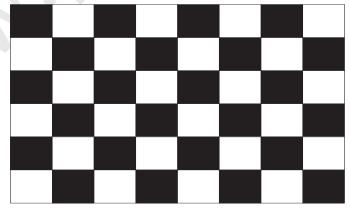
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Devemates	Cymphol		Value	11:4	No.4a	
Parameter	Symbol	Min	Тур	Max	Unit	Note
Circuit :						
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC	
Dower Input Current	lion	455	650	845	mA	1
Power Input Current	ILCD	668	955	1240	mA	2
Power Consumption	PLCD		7.8	9.2	Watt	1
Rush current	IRUSH	-	-	5.0	А	3

- Note 1. The specified current and power consumption are under the V_{LCD} =12.0V, Ta=25 ± 2°C, f_V =60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
 - 2. The current is specified at the maximum current pattern.
 - 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

White: 1023 Gray Black: 0 Gray



Mosaic Pattern(8 x 6)

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Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Complete al	Values			11:4	Nete	
		Symbol	Min	Тур	Max	Unit	Notes	
LED Driver :								
Power Supply Inpu	t Voltage		VBL	22.8	24.0	25.2	Vdc	1
Power Supply Input	Current		IBL_A	-	3.7	4.0	Α	Ext VBR-B = 100%
Power Supply Input Current (In-Rush) Power Consumption		-Rush)	Irush	-	-	6.5	A	VBL = 22.8V Ext VBR-B = 100% 4
		PBL	-	88	94.1	W	Ext VBR-B = 100%	
	On/Off	On	V on	2.5	-	5.0	Vdc	
		Off	V off	-0.3	0.0	0.7	Vdc	
Input Voltage for Control System Signals	Brightness Adjust		ExtVBR-B	10	-	100	%	On Duty
	PWM Frequency for NTSC & PAL		PAL		100		Hz	3
NTSC & PAL		NTSC		120		Hz	3	
LED:								
Life Time				30,000	50,000		Hrs	2

Notes:

- 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (ExtVBR-B: 100%), it is total power consumption.
- 2. The life time(MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at 25±2°C.
- 3. LGD recommend that the PWM freq. is synchronized with One time harmonic of Vsync signal of system. Though PWM frequency is over 120Hz (max 252Hz), function of LED Driver is not affected.
- 4. The duration of rush current is about 10ms.
- 5. Even though inrush current is over the specified value, there is no problem if I^2T spec of fuse is satisfied.

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3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector : FI-R51S-HF(manufactured by JAE) or KN25-51P-0.5SH(manufactured by Hirose)

(CN1) Refer to below table

- Mating Connector : FI-R51HL(JAE) or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	Bit Select	'H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Reserved for LGD)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Reserved for LGD)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Reserved for LGD)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection	34	GND	Ground
9	NC	No Connection	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC	No Connection
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC	No Connection
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC	No Connection		-	-

Note

- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the **EIA 644** Standard.
- 4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module. These pins should be no connection.
- 5. LVDS pin (pin No. **#24,25,40,41**) are used for 10Bit(D) of the LCD module. If used for 8Bit(R), these pins are no connection.
- 6. Specific pin No. **#44** is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

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3-2-2. Backlight Module

Master

- LED Driver Connector : 20022WR-14B1(Yeonho)

or Equivalent

- Mating Connector: 20022HS-14 or Equivalent

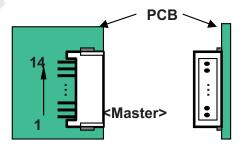
Table 5. LED DRIVER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Master	Note
1	VBL	Power Supply +24.0V	VBL	
2	VBL	Power Supply +24.0V	VBL	
3	VBL	Power Supply +24.0V	VBL	
4	VBL	Power Supply +24.0V	VBL	
5	VBL	Power Supply +24.0V	VBL	
6	GND	Backlight Ground	GND	
7	GND	Backlight Ground	GND	
8	GND	Backlight Ground	GND	1
9	GND	Backlight Ground	GND	
10	GND	Backlight Ground	GND	
11	NC	No connection	OPEN or GND	
12	VON/OFF	Backlight ON/OFF control	VON/OFF	
13	EXTVBR-B	External PWM	EXTVBR-B	2
14	NC	No connection	OPEN or GND	TBD

Notes: 1. GND should be connected to the LCD module's metal frame.

- 2. High: on duty / Low: off duty, Pin#13 can be opened. (if Pin #13 is open, EXTVBR-B is 100%)
- 3. Each impedance of pin #12 and 13 is over 50 [K Ω].

♦ Rear view of LCM



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3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6-1. TIMING TABLE for NTSC (DE Only Mode)

		•	•	,			
ı	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	tHV	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tvv	-	1080	-	tHP	
Vertical	Blank	t∨B	11	45	69	tHP	
	Total	tvp	1091	1125	1149	tHP	
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fн	65	67.5	70	KHz	
	Vertical	fv	57	60	63	Hz	

Table 6-2. TIMING TABLE for PAL (DE Only Mode)

ı	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	thv	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tvv	-	1080	-	tHP	
Vertical	Blank	tvB	228	270	300	tHP	
	Total	tvp	1308	1350	1380	tHP	
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fH	65	67.5	70	KHz	
	Vertical	fv	47	50	53	Hz	

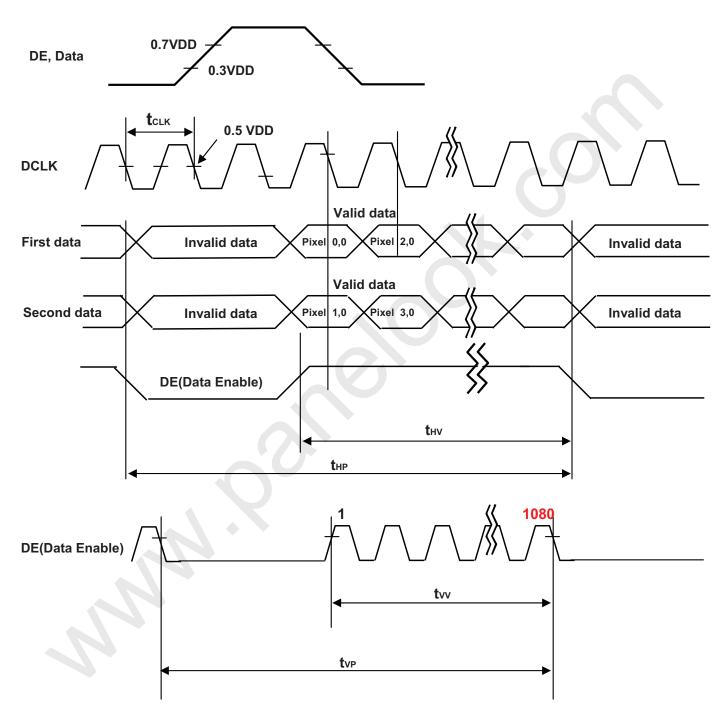
Note The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

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3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram



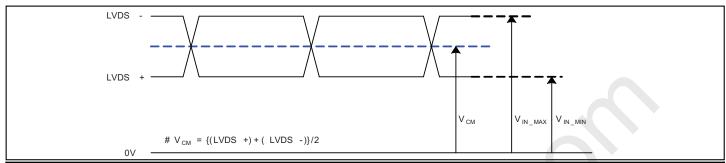
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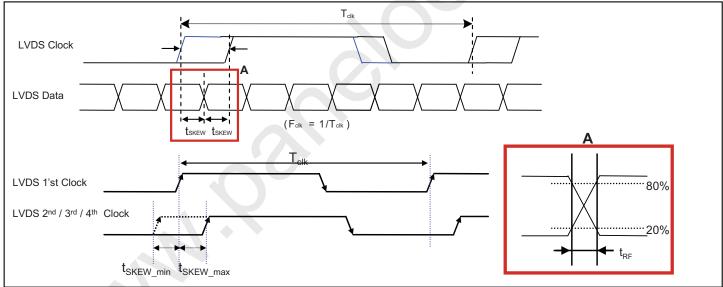
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	V_{CM}	1.0	1.5	٧	-
LVDS Input Voltage Range	V _{IN}	0.7	1.8	V	-
Change in common mode Voltage	△VCM		250	mV	-

2) AC Specification



Description	ı	Symbol	Min	Max	Unit	Note
LVDS Differential Voltage	High Threshold	V_{TH}	100	300	mV	2
LVD3 Dillerential Voltage	Low Threshold	V_{TL}	-300	-100	mV	J
LVDS Clock to Data Skew Mar	rgin	t _{SKEW}		(0.25*T _{clk})/7	ps	-
LVDS Clock/DATA Rising/Fall	ing time	t _{RF}	260	(0.3*T _{clk})/7	ps	2
Effective time of LVDS		t _{eff}	±360		ps	-
LVDS Clock to Clock Skew Ma	argin (Even to Odd)	t _{SKEW_EO}		1/7* T _{clk}	T _{clk}	-

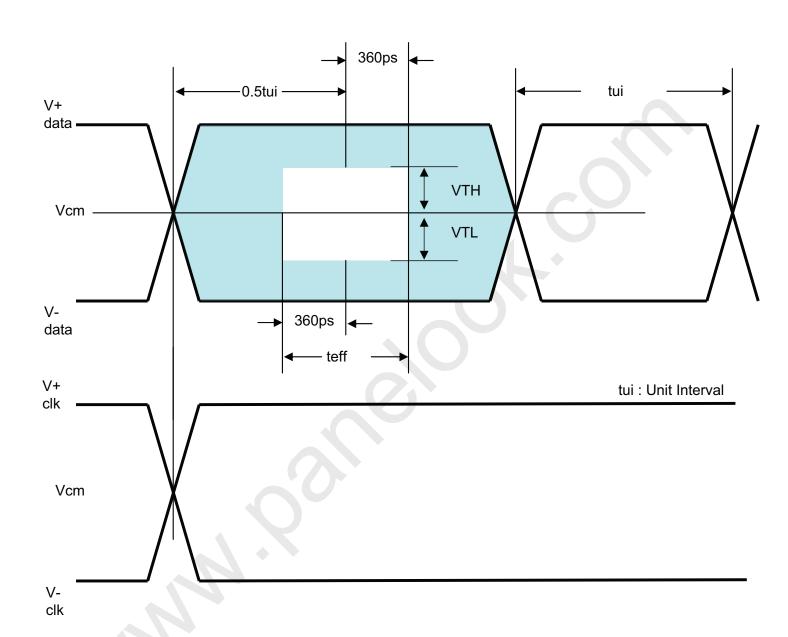
Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If $\rm t_{RF}$ isn't enough, $\rm t_{\rm eff}~$ should be meet the range.
- 3. LVDS Differential Voltage is defined within t_{eff}

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3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

										In	put	Со	lor	Da	ıta																
						RE	ΕD								_		EE									BL	UE				
	Color	MS	SB							L	.SB	MS	SB							L	SB	M	SB							L	SB
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	В9	B8	В7	В6	B5	В4	ВЗ	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																															
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
GREEN																															
,	GREEN (1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																															
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

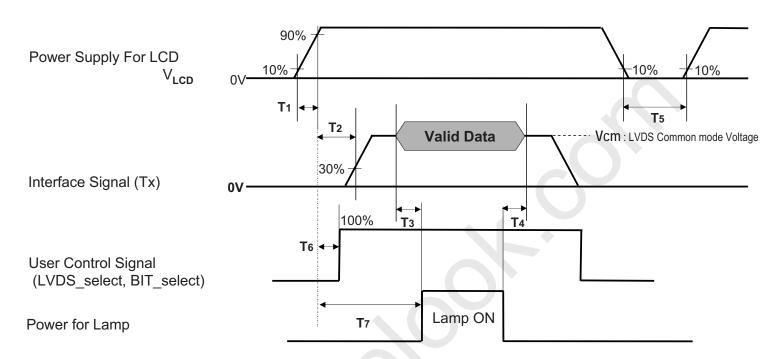


Table 8. POWER SEQUENCE

Dovometer		Value		l lmi4	Netes
Parameter	Min	Тур	Max	Unit	Notes
T1	0.5	-	20	ms	
T2	0	-	-	ms	4
Т3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	S	5
T6	-	-	T2	ms	4
T 7	0.5		-	S	

Note: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. If the on time of signals(Interface signal and user control signals) precedes the on time of Power(V_{LCD}), it will be happened abnormal display. When **T6** is NC status, **T6** doesn't need to be measured.
- 5. **T5** should be measured after the Module has been fully discharged between power off and on period.

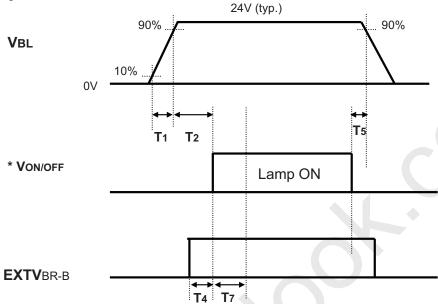
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3-6-2. Sequence for Inverter





3-6-3. Dip condition for Inverter

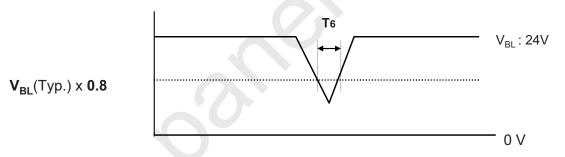


Table 9. Power Sequence for Inverter

Table 3: 1 ower ocqueries					
Parameter		Values		l leite	Note
Parameter	Min	Тур	Max	Units	Note
T1	20	-	-	ms	1
T 2	500	-	-	ms	
T4	0		-	ms	2
T 5	10	-	-	ms	
Т6	-	-	10	ms	V _{BL} (Typ) x 0.8
T 7	1000	- -	-	ms	3

Notes: 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

- 2. T4(max) is less than T2.
- 3. It is the recommendation to input Max Duty to Inverter** for EXTVBR-B during T7 period.
- * The recommendation of Von/off rising time is under 10ms.

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Product Specification

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

It is presented additional information concerning the measurement equipment and method in FIG. 1.

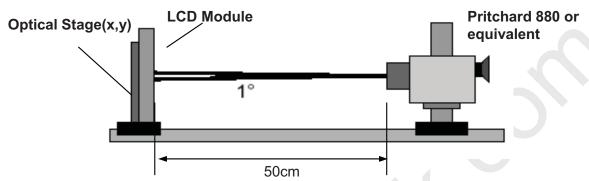


FIG. 1 Optical Characteristic Measurement Equipment and Method

Ta= $25\pm2^{\circ}$ C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz,

Table 10. OPTIC	AL CHARACTI	ERISTICS				EXT	V BR-B =100
Davana	.4	Cross book		Value		11:4	Note
Parame	eter	Symbol	Min	Тур	Max	Unit	Note
Contrast Ratio		CR	1000	1400	-		1
Surface Luminance	, white	L_WH	290	360	-	cd/m ²	2
Luminance Variatio	n	δ _{WHITE} 5P	-	-	1.3		3
Response Time	Gray-to-Gray	G to G	-	5	8	ms	4
rvesponse rime	Uniformity	δ дто д	-	-	1	ms	5
	DED	Rx		0.647			
	RED	Ry		0.332			
	CDEEN	Gx		0.309			
Color Coordinates	GREEN	Gy	Тур	0.601	Тур		
[CIE1931]	BLUE	Bx	-0.03	0.149	+0.03		
	BLUE	Ву		0.059			
	WHITE	Wx		0.279			
	VVIIII	Wy		0.292			
Color Temperature				10,000		K	
Color Gamut				72		%	
Viewing Angle (CR	>10)						
x axis	, right(φ=0°)	θr	89	-	-		
x axis	, left (φ=180°)	θΙ	89	-	-	4	
y axis	, up (φ=90°)	θи	89	-	-	degree	6
y axis	, down (φ=270°)	θd	89	-	-		
Gray Scale			-	-	-		7

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Product Specification

Note: 1. Contrast Ratio(CR) is defined mathematically as:

Surface Luminance with all white pixels Contrast Ratio = Surface Luminance with all black pixels

It is measured at center 1-point.

- 2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance, δ WHITE is defined as :

 $\delta \text{ WHITE(5P)} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, \ L_{\text{on3}}, \ L_{\text{on4}}, \ L_{\text{on5}}) \ / \ \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, \ L_{\text{on3}}, \ L_{\text{on4}}, \ L_{\text{on5}})$ Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations. For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transit from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M) G to G Spec stands for average value of all measured points. Photo Detector: RD-80S / Field: 2°
- Gray to Gray Response time uniformity is Reference data. Please see Appendix V.
- Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.

Gray Level	Luminance [%] (Typ)	
LO	0.067	
L15	0.27	
L31	1.04	
L47	2.49	
L63	4.68	
L79	7.66	
L95	11.5	
L111	16.1	
L127	21.6	
L143	28.1	
L159	35.4	
L175	43.7	
L191	53.0	
L207	63.2	
L223	74.5	
L239	86.7	
L255	100	

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Product Specification

Measuring point for surface luminance & luminance variation

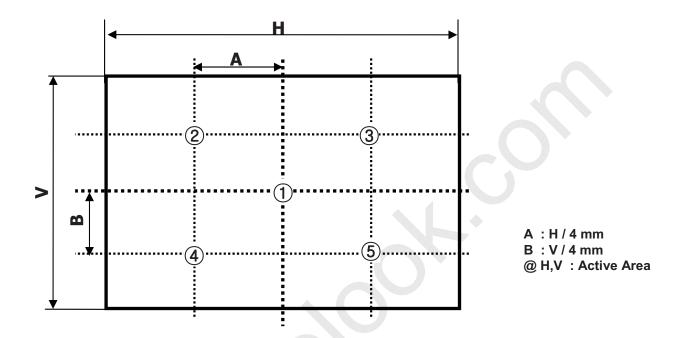


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

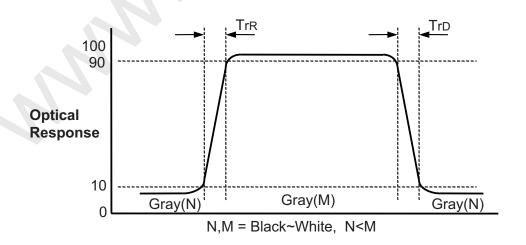


FIG. 3 Response Time

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Product Specification

Dimension of viewing angle range

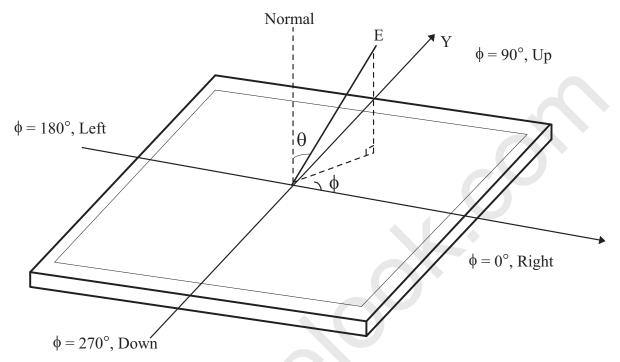


FIG. 4 Viewing Angle

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Product Specification

5. Mechanical Characteristics

Table 10 provides general mechanical characteristics.

Table 10. MECHANICAL CHARACTERISTICS

Item		Value
	Horizontal	973.2 mm
Outline Dimension	Vertical	566.2 mm
	Depth	10.8 mm
Donal Avea	Horizontal	937.2 mm
Bezel Area	Vertical	530.2 mm
Active Display Area	Horizontal	930.24 mm
Active Display Area	Vertical	523.26 mm
Weight	11.3 Kg(Typ), 12.0Kg(M	ax)

Note: Please refer to a mechanical drawing in terms of tolerance at the next page.

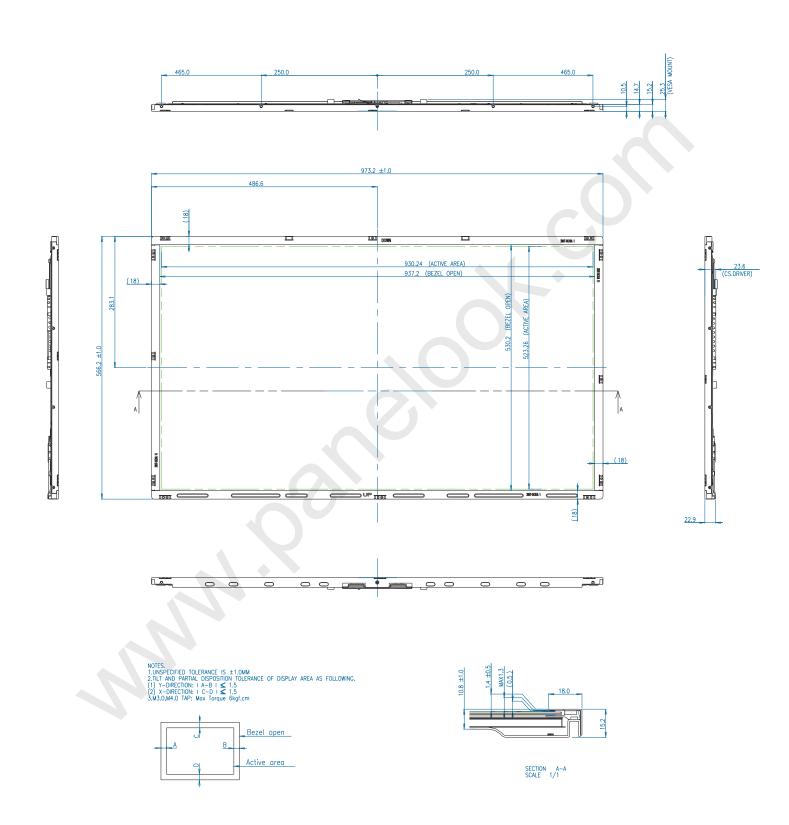
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<Front View>

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Product Specification



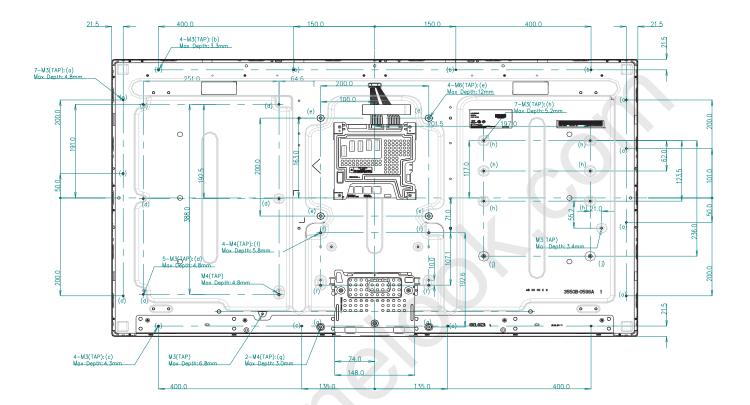
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<Rear View>

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Product Specification



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Product Specification

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min Each direction per 10 min
6	Shock test (non-operating)	Shock level : 50 Grms, 35 G(Z axis) Waveform : half sine wave, 11ms Direction : \pm X, \pm Y, \pm Z One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note: Before and after Reliability test, LCM should be operated with normal function.

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Product Specification

7. International Standards

7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc.
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC).
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
 (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1 LED Product IEC60825-1: 2001 Embedded LED Power (Class 1)

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

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Product Specification

8. Packing

8-1. Information of LCM Label

a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH)

E: MONTH

D : YEAR

F~ M: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	Α	В	С

3. FACTORY CODE

Factory Code	Gumi	Nanjing	Paju
Mark	K	С	Р

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one pallet: 15 ea

b) Pallet Size: 1140 mm X 990 mm X 798mm

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Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm200$ mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from transformers to prevent abnormal display, sound noise and temperature rising.
- (11) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic.

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Product Specification

(12) Partial darkness may happen under the long-term operation of any dimming without power on/off. This phenomenon which disappears naturally after 5 minutes is not a problem about reliability but LCD characteristics.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

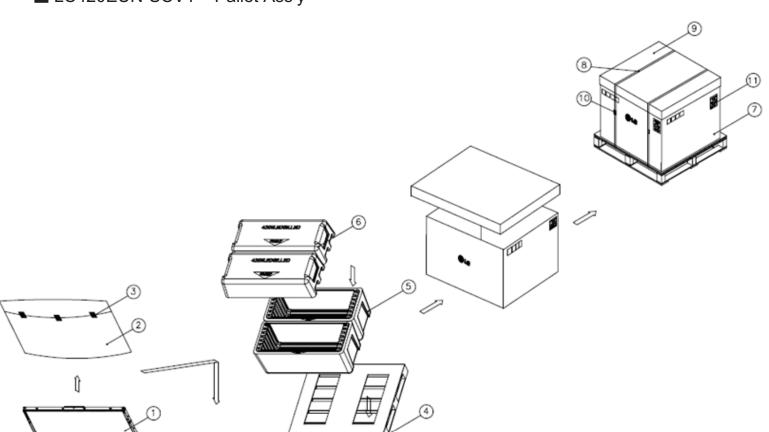
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Product Specification

APPENDIX-I

■ LC420EUN-SCV1 – Pallet Ass'y



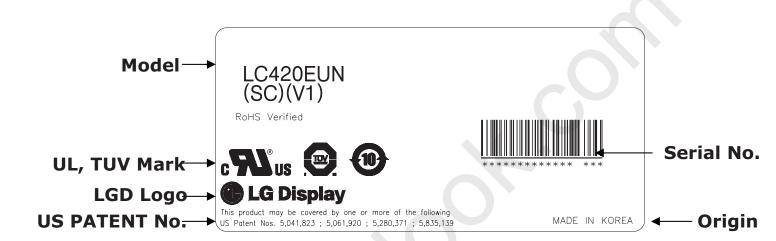
NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	42INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	Plywood 1140X990X125.5mm
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE,PACKING	PAPER
8	BAND	PP
9	ANGLE.COVER	PAPER
10	BAND,CLIP	STEEL or PP
11	LABEL	YUPO 80G 100X70

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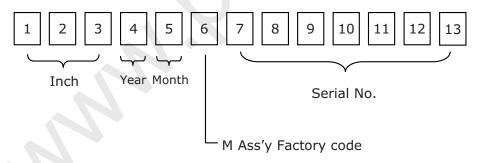


APPENDIX- II-1

■ LC420EUN-SCV1-LCM Label



■ Serial No. (See CAS 26page for more information)

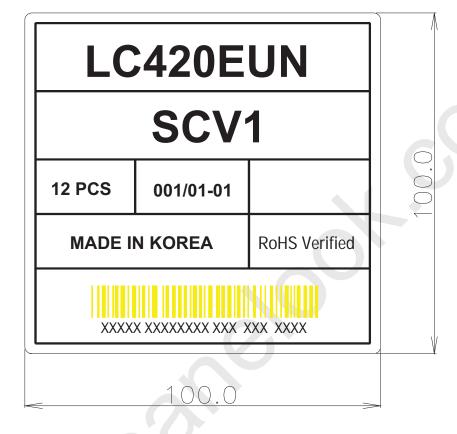


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APPENDIX- II-2

■ LC420EUN-SCV1-Pallet Label



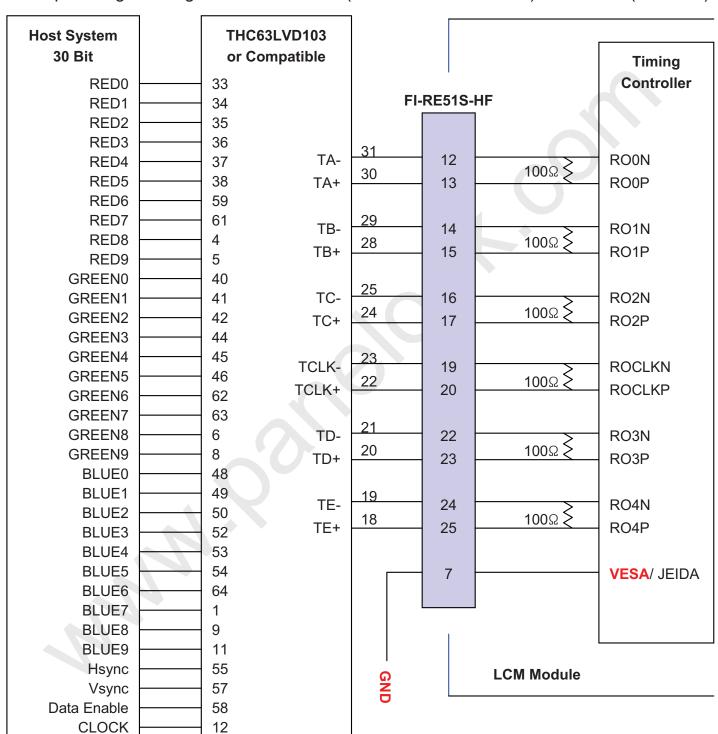
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Product Specification

APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine: THC63LVD103) Transmitter(Pin7="L")



Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

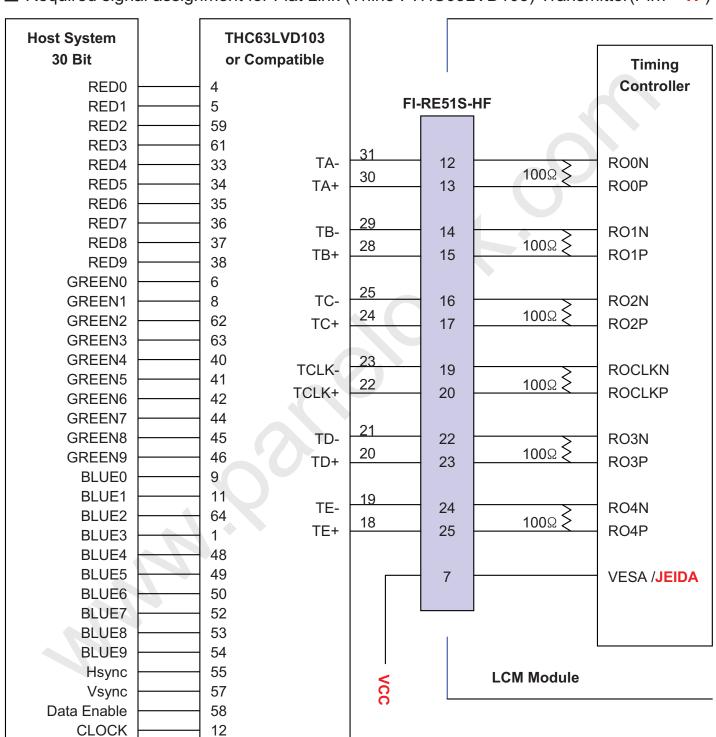
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Product Specification

APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine: THC63LVD103) Transmitter(Pin7="H")



Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

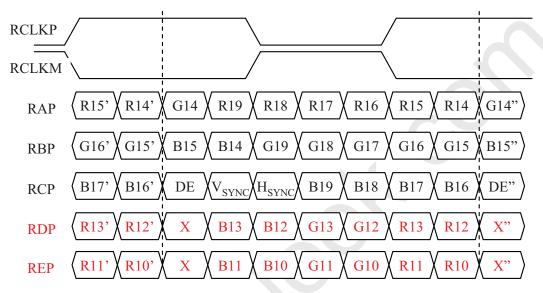
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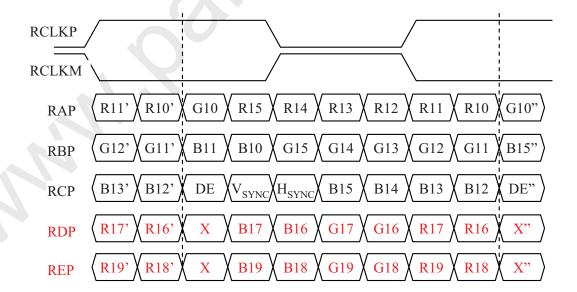
Product Specification

APPENDIX- III-2

- LVDS Data-Mapping Information (**10 Bit**)
 - 1) LVDS Select : "H" Data-Mapping (JEIDA format)



2) LVDS Select: "L" Data-Mapping (VESA format)



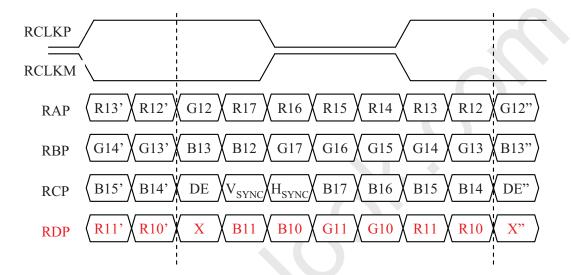
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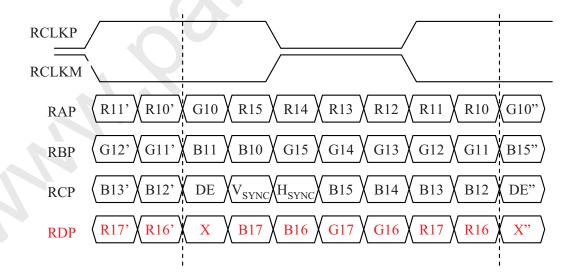
Product Specification

APPENDIX- III-2

- LVDS Data-Mapping Information (8 Bit)
 - 1) LVDS Select : "H" Data-Mapping (JEIDA format)



2) LVDS Select : "L" Data-Mapping (VESA format)



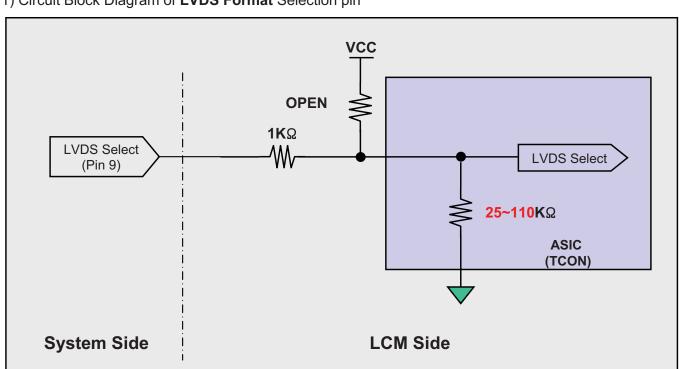
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Product Specification

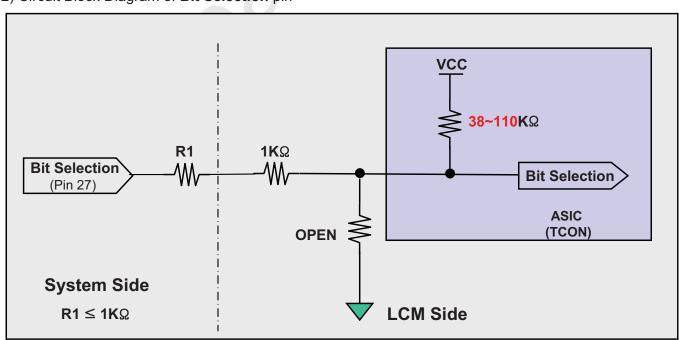
APPENDIX- III-3

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of Bit Selection pin



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