LCM ENGINEERING SPECIFICATION

*MODEL	LC550EUA
SUFFIX	AEM1
Update	Dec. 16. 2011

( ) **Preliminary Specification**

(●) **Final Specification**

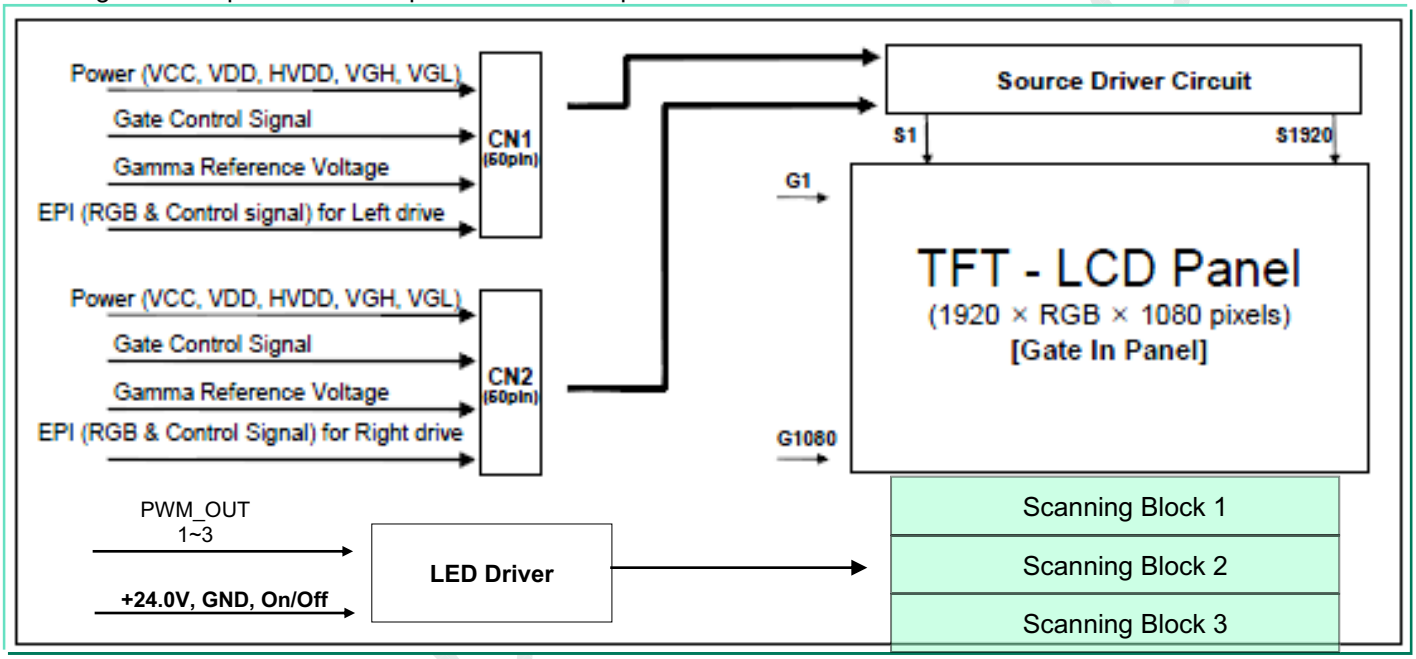
CONTENTS

RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.1	Oct 07, 2011	-	Preliminary Specification(First Draft)
1.0	Dec.15,2011	3	Power consumption is corrected Weight is corrected Outline dimension is corrected
		5	DC Electrical characteristics are corrected
		12	Timing Requirements is corrected
		17	Color gamut is corrected
		20	Weight is corrected
		27	Cancel US patent
		-	**Final Specification**

1. General Description

The LC550EUA is a Color Active Matrix Liquid Crystal Display with an integral the Source PCB and the four of Gate Drive IC (GD-IC) at each side. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 54.64 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.
It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	54.64 inches(1387.8mm) diagonal
Outline Dimension	1244.6(H) x 720.9 (V) x17.2 (D) (Typ.)
Pixel Pitch	0.630 mm x 0.630 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors (※ 1.06B colors @ 10 bit (D) System Output)
Drive IC Data Interface	Source D-IC : 8-bit mini-LVDS, and control signals TTL Gate D-IC : Line on Glass (LOG) Through Source D-IC
Luminance, White	400 cd/m2 (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 107.2W [Logic= 6.1W, LED Driver=101.1W (ExtVbr_B=100%)]
Weight	15.51Kg (Typ.)
Display Operating Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)

2. Absolute Maximum Ratings

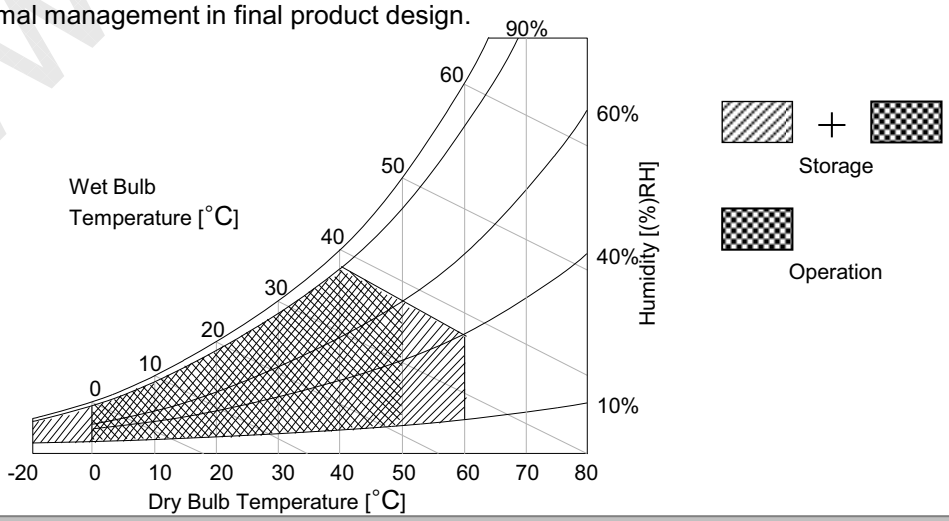
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value		Unit	Note
		Min	Max		
Logic Power Voltage	VCC	-0.5	+2.2	VDC	1
Gate High Voltage	VGH	+18.0	+30.0	VDC	
Gate Low Voltage	VGL	-8.0	-4.0	VDC	
Source D-IC Analog Voltage	VDD	-0.3	+18.0	VDC	
Gamma Ref. Voltage (Upper)	VGMH	½VDD-0.5	VDD+0.5	VDC	
Gamma Ref. Voltage (Low)	VGML	-0.3	½ VDD+0.5	VDC	
Panel Front Temperature	TSUR	-	+68	°C	4
Operating Temperature	TOP	0	+50	°C	2,3
Storage Temperature	TST	-20	+60	°C	
Operating Ambient Humidity	HOP	10	90	%RH	
Storage Humidity	HST	10	90	%RH	

Note1. Ambient temperature condition (Ta = 25 ± 2 °C)

2. Temperature and relative humidity range are shown in the figure below.
   Wet bulb temperature should be Max 39°C, and no condensation of water.
3. Gravity mura can be guaranteed below 40°C condition.
4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68℃. The range of operating temperature may be degraded in case of improper thermal management in final product design.



3. Electrical Specifications

It requires two kind of power inputs.
One is employed to power for the LCD circuit. The other Is used for the LED backlight circuit.

3-1. Electrical Characteristics

Table 2. DC ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	MIN	TYP	MAX	Unit	Note
Logic & EPI Power Voltage	VCC	-	1.62	1.8	1.98	VDC	
Logic High Level Input Voltage	VIH	-	1.4(TBD)	-	VCC	VDC	
Logic Low Level Input Voltage	VIL	-	0	-	0.4(TBD)	VDC	
Source D-IC Analog Voltage	VDD	-	16.7	16.9	17.1	VDC	
Half Source D-IC Analog Voltage	H_VDD	-	8.2	8.4	8.6	VDC	6
Gamma Reference Voltage	V_{GMH}	(GMA1 ~ GMA9)	H_VDD+0.2 (TBD)	-	VDD-0.2 (TBD)	VDC	
	V_{GML}	(GMA10 ~ GMA18)	0.2	-	H_VDD-0.2V	VDC	
Common Voltage	Vcom	Reverse	7.26	7.56	7.86	V	
EPI input common voltage	VCM	LVDS Type	0.8(TBD)	VCC/2	VCC-0.6-Vdiff/2(TBD)	V	5
EPI Input eye diagram	Veye	-	75(TBD)	-	-	mV	
EPI input differential voltage	Vdiff	-	150(TBD)	-	500(TBD)	mV	
Gate High Voltage	VGH	@ 25℃	27.7	28	28.3	VDC	
		@ 0℃	29.7	30	30.3	VDC	
Gate Low Voltage	VGL	–	-5.2	-5.0	-4.8	VDC	
GIP Bi-Scan Voltage	VGI_P VGI_N	-	VGL	-	VGH	VDC	
GIP Refresh Voltage	VGH even/odd	-	VGL	-	VGH	V	
GIP Start Pulse Voltage	VST	-	VGL	-	VGH	V	
GIP Operating Clock	GCLK	-	VGL	-	VGH	V	
Total Power Current	ILCD	-	-	915	1190	mA	1
Total Power Consumption	PLCD	-	-	7.4	9.67	Watt	1

Note:

1. The specified current and power consumption are under the V_{LCD}=12V., 25 ± 2°C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
2. The above spec is based on the basic model.
3. All of the typical gate voltage should be controlled within 1% voltage level
4. Ripple voltage level is recommended under ±5% of typical voltage
5. In case of EPI signal spec, refer to Fig 2 for the more detail.
6. HVDD Voltage level is half of VDD and it should be between Gamma9 and Gamma10.

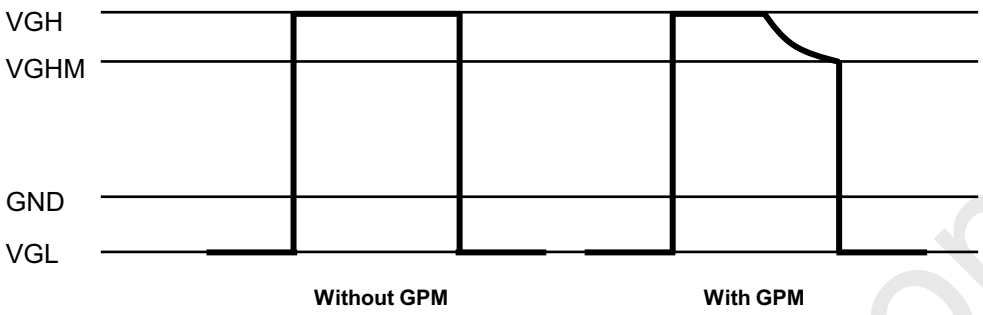


FIG. 1 Gate Output Wave form without GPM and with GPM

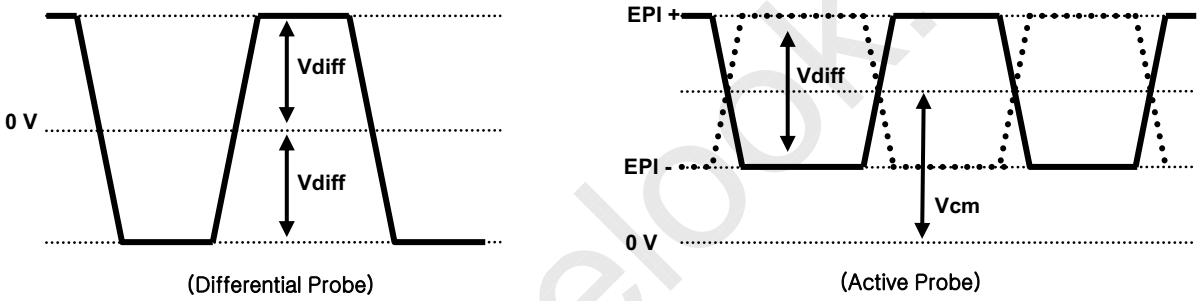


FIG. 2-1 EPI Differential signal characteristics

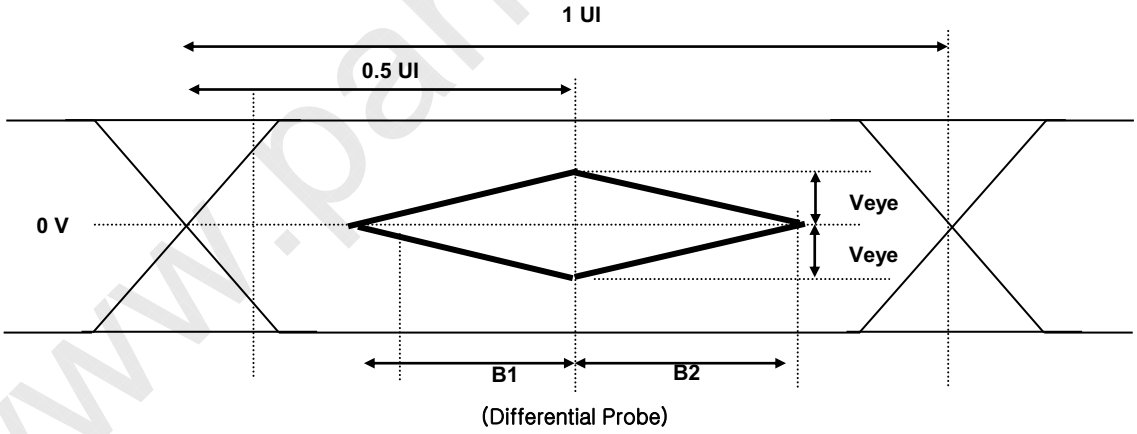


FIG. 2-2 Eye Pattern of EPI Input

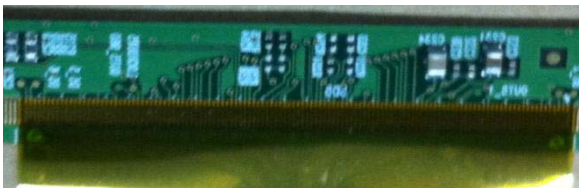


FIG. 3 Measure point

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter			Symbol	Values			Unit	Notes
				Min	Typ	Max		
LED Driver :								
Power Supply Input Voltage			VBL	22.8	24.0	25.2	Vdc	1
Power Supply Input Current			IBL	-	4.17	4.55	A	1
Power Supply Input Current (In-Rush)			In-rush	-	-	5.7(TBD)	A	VBL = 22.8V ExtV_{BR-B} = 100%
Power Consumption			PBL	-	100.1	109.3	W	1
Input Voltage for Control System Signals	On/Off	On	V on	2.5	-	5.0	Vdc	
		Off	V off	-0.3	0.0	0.7	Vdc	
LED :								
Life Time				30,000			Hrs	2

Notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (ExtVBR-B : 100%), it is total power consumption.
2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at 25±2°C.

3-2. Interface Connections

This LCD module employs two kinds of interface connection, two 50-pin FFC connector are used for the module electronics.

3-2-1. LCD Module

-LCD Connector (CN1): TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

Table 3-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description		No	Symbol	Description
1	LTD_OUT	LTD OUTPUT		26	GND	Ground
2	NC	No Connection		27	EPI2-	EPI Receiver Signal(2-)
3	GCLK1	GIP GATE Clock 1		28	EPI2+	EPI Receiver Signal(2+)
4	GCLK2	GIP GATE Clock 2		29	GND	Ground
5	GCLK3	GIP GATE Clock 3		30	GND	Ground
6	GCLK4	GIP GATE Clock 4		31	EPI1-	EPI Receiver Signal(1-)
7	GCLK5	GIP GATE Clock 5		32	EPI1+	EPI Receiver Signal(1+)
8	GCLK6	GIP GATE Clock 6		33	GND	Ground
9	VGI_N	GIP Bi-Scan (Normal =VGL Rotate = VGH)		34	VCC	Logic & EPI Power Voltage
10	VGI_P	GIP Bi-Scan (Normal =VGH Rotate = VGL)		35	NC	No Connection
11	VGH_ODD	GIP Panel VDD for Odd GATE TFT		36	LOCKOUT3	LOCKOUT3
12	VGH_EVEN	GIP Panel VDD for Even GATE TFT		37	NC	No Connection
13	VGL	GATE Low Voltage		38	GND	Ground
14	VST	VERTICAL START PULSE		39	GMA 18	GAMMA VOLTAGE 18 (Output From LCD)
15	GIP_Reset	GIP Reset		40	GMA 16	GAMMA VOLTAGE 16
16	VCOM_L_FB	VCOM Left Feed-Back Output		41	GMA 15	GAMMA VOLTAGE 15
17	VCOM_L	VCOM Left Input		42	GMA 14	GAMMA VOLTAGE 14
18	GND	Ground		43	GMA 12	GAMMA VOLTAGE 12
19	VDD	Driver Power Supply Voltage		44	GMA 10	GAMMA VOLTAGE 10 (Output From LCD)
20	VDD	Driver Power Supply Voltage		45	GMA 9	GAMMA VOLTAGE 9 (Output From LCD)
21	H_VDD	Half Driver Power Supply Voltage		46	GMA 7	GAMMA VOLTAGE 7
22	GND	Ground		47	GMA 5	GAMMA VOLTAGE 5
23	EPI3-	EPI Receiver Signal(3-)		48	GMA 4	GAMMA VOLTAGE 4
24	EPI3+	EPI Receiver Signal(3+)		49	GMA 3	GAMMA VOLTAGE 3
25	GND	Ground		50	GMA 1	GAMMA VOLTAGE 1(Output From LCD)

Note :

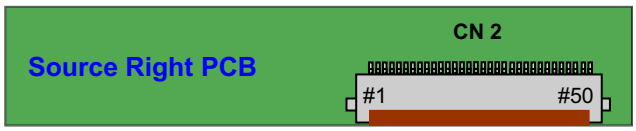
1. Please refer to application note for details.
   (**GIP** & **Half VDD & Gamma Voltage setting**)

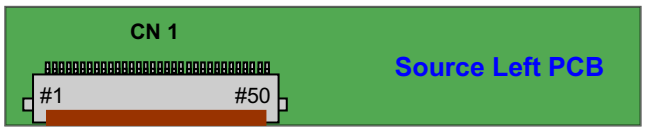
-LCD Connector (CN1): TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

Table 3-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION

No	Symbol	Description		No	Symbol	Description
1	GMA 1	GAMMA VOLTAGE 1 (Output From LCD)		26	GND	Ground
2	GMA 3	GAMMA VOLTAGE 3		27	EPI1-	EPI Receiver Signal(4-)
3	GMA 4	GAMMA VOLTAGE 4		28	EPI1+	EPI Receiver Signal(4+)
4	GMA 5	GAMMA VOLTAGE 5		29	GND	Ground
5	GMA 7	GAMMA VOLTAGE 7		30	H_VDD	Half Driver Power Supply Voltage
6	GMA 9	GAMMA VOLTAGE 9 (Output From LCD)		31	VDD	Driver Power Supply Voltage
7	GMA 10	GAMMA VOLTAGE 10 (Output From LCD)		32	VDD	Driver Power Supply Voltage
8	GMA 12	GAMMA VOLTAGE 12		33	GND	Ground
9	GMA 14	GAMMA VOLTAGE 14		34	VCOM_R	VCOM Right Input
10	GMA 15	GAMMA VOLTAGE 15		35	VCOM_R_FB	VCOM Right Feed-Back Output
11	GMA 16	GAMMA VOLTAGE 16		36	GIP_Reset	GIP Reset
12	GMA 18	GAMMA VOLTAGE 18 (Output From LCD)		37	VST	VERTICAL START PULSE
13	GND	Ground		38	VGL	GATE Low Voltage
14	LOCKOUT6	LOCKOUT6		39	VGH_EVEN	GIP Panel VDD for Even GATE TFT
15	LOCKIN3	LOCKIN3		40	VGH_ODD	GIP Panel VDD for Odd GATE TFT
16	NC	No Connection		41	VGI_P	GIP Bi-Scan (Normal =VGH Rotate = VGL)
17	VCC	Logic & EPI Power Voltage		42	VGI_N	GIP Bi-Scan (Normal =VGL Rotate = VGH)
18	GND	Ground		43	GCLK6	GIP GATE Clock 6
19	EPI6-	EPI Receiver Signal(6-)		44	GCLK5	GIP GATE Clock 5
20	EPI6+	EPI Receiver Signal(6+)		45	GCLK4	GIP GATE Clock 4
21	GND	Ground		46	GCLK3	GIP GATE Clock 3
22	GND	Ground		47	GCLK2	GIP GATE Clock 2
23	EPI5-	EPI Receiver Signal(5-)		48	GCLK1	GIP GATE Clock 1
24	EPI5+	EPI Receiver Signal(5+)		49	NC	No Connection
25	GND	Ground		50	LTD_OUT	LTD OUTPUT

Note : 1. Please refer to application note for details.
(**GIP** & **Half VDD & Gamma Voltage setting**)





3-2-2. Backlight Module

Master

-LED Driver Connector

: 20022WR - H14B2(Yeonho) or compatible

- Mating Connector

: 20022HS - 14B2 or compatible

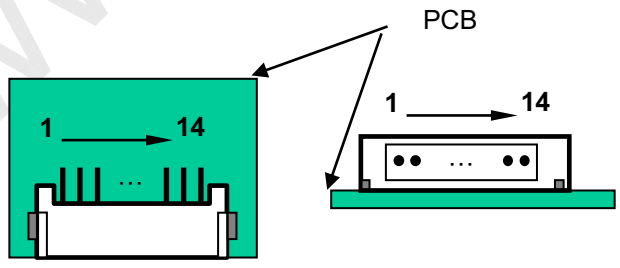
Table 5-1. LED DRIVER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Note
1	VBL	Power Supply +24.0V	
2	VBL	Power Supply +24.0V	
3	VBL	Power Supply +24.0V	
4	VBL	Power Supply +24.0V	
5	VBL	Power Supply +24.0V	
6	GND	Backlight Ground	
7	GND	Backlight Ground	
8	GND	Backlight Ground	1
9	GND	Backlight Ground	
10	GND	Backlight Ground	
11	Status	Back Light Status	2
12	VON/OFF	Backlight ON/OFF control	
13	NC	Don't care	
14	NC	Don't care	

Notes :1. GND should be connected to the LCD module's metal frame.

2. Normal : Low (under 0.7V) / Abnormal : Open

◆ Rear view of LCM



<Master>

3-2-2. Backlight Module

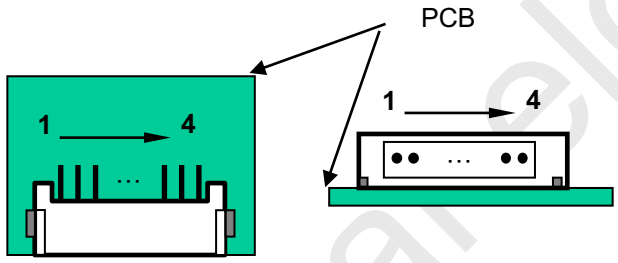
LED Driver Connector
: 10031HR-H04L_Black (YEONHO)

Table 5-2. LED DRIVER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Note
1	GND	Ground	1
2	SCAN3	PWM 3	
3	SCAN2	PWM 2	
4	SCAN1	PWM 1	

Notes :1. GND should be connected to the LCD module's metal frame.

◆ Rear view of LCM



3-3. Signal Timing Specifications

Table 4. Timing Requirements

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
Unit Interval	UI	-	0.59	-	-	ns	
DLL Lock time	Tlock	-	1.6	-	200	Us	Fig 4
Effective Veye width time	B1&B2	-	0.25	-	-	UI	Fig. 2
SSC	Vspread	@100KHz	-	-	2	%	
Receiver off to SOE rising time	tSOE_ Rising		5	-	-	Packet	Fig.5
SOE pulse width	tSOE_ Width	-	4	-	-	Packet	Fig.5
SOE rising to 1st data time	tSOE_ DATA	-	5	-	-	Packet	Fig.5
EPI Bandwidth		-	0.607	-	0.728	GBPS	

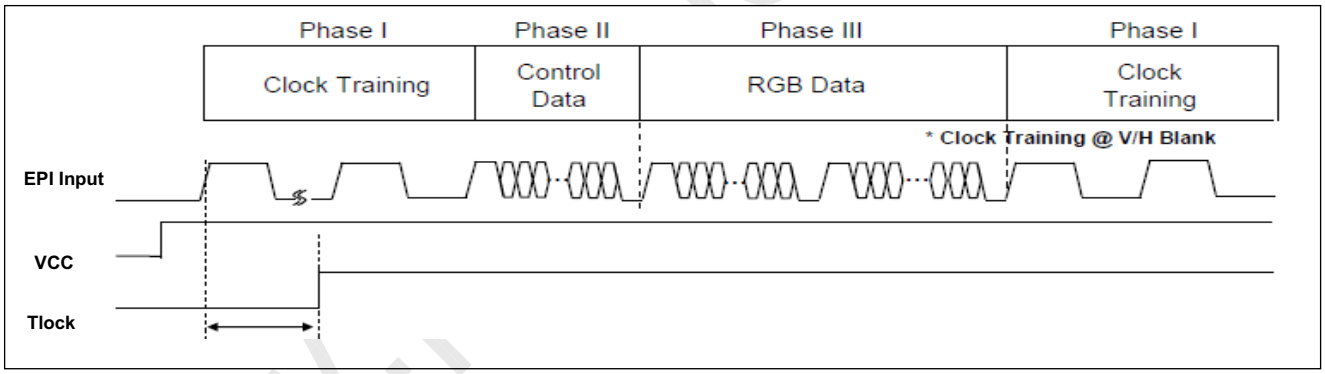


FIG 4. Power On to DLL Lock time

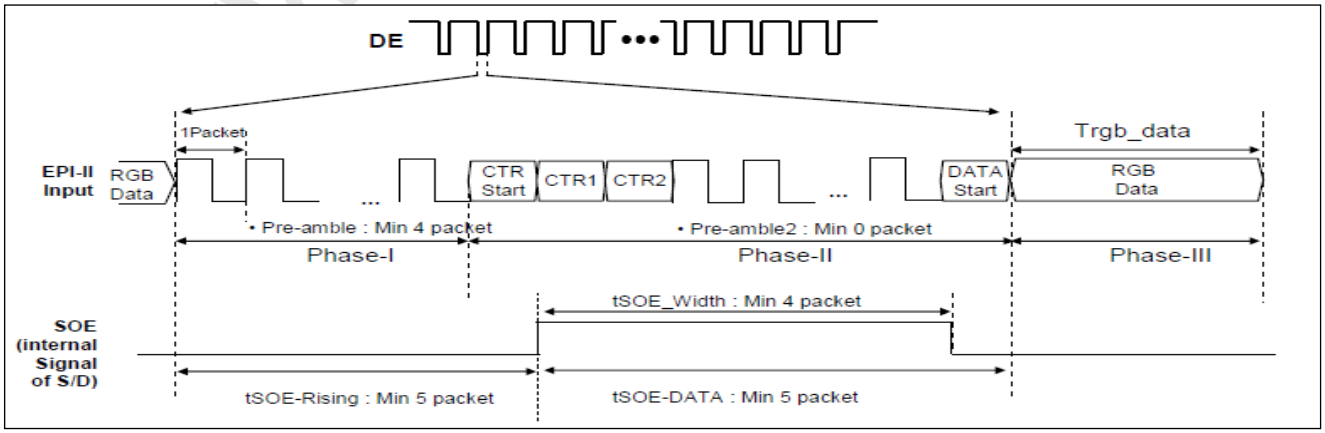


FIG 5. SOE Width & Timing

3-4. Panel Pixel Structure

FIG. 8 Panel Pixel Structure

3-7. Power Sequence

3-7-1. LCD Driving circuit

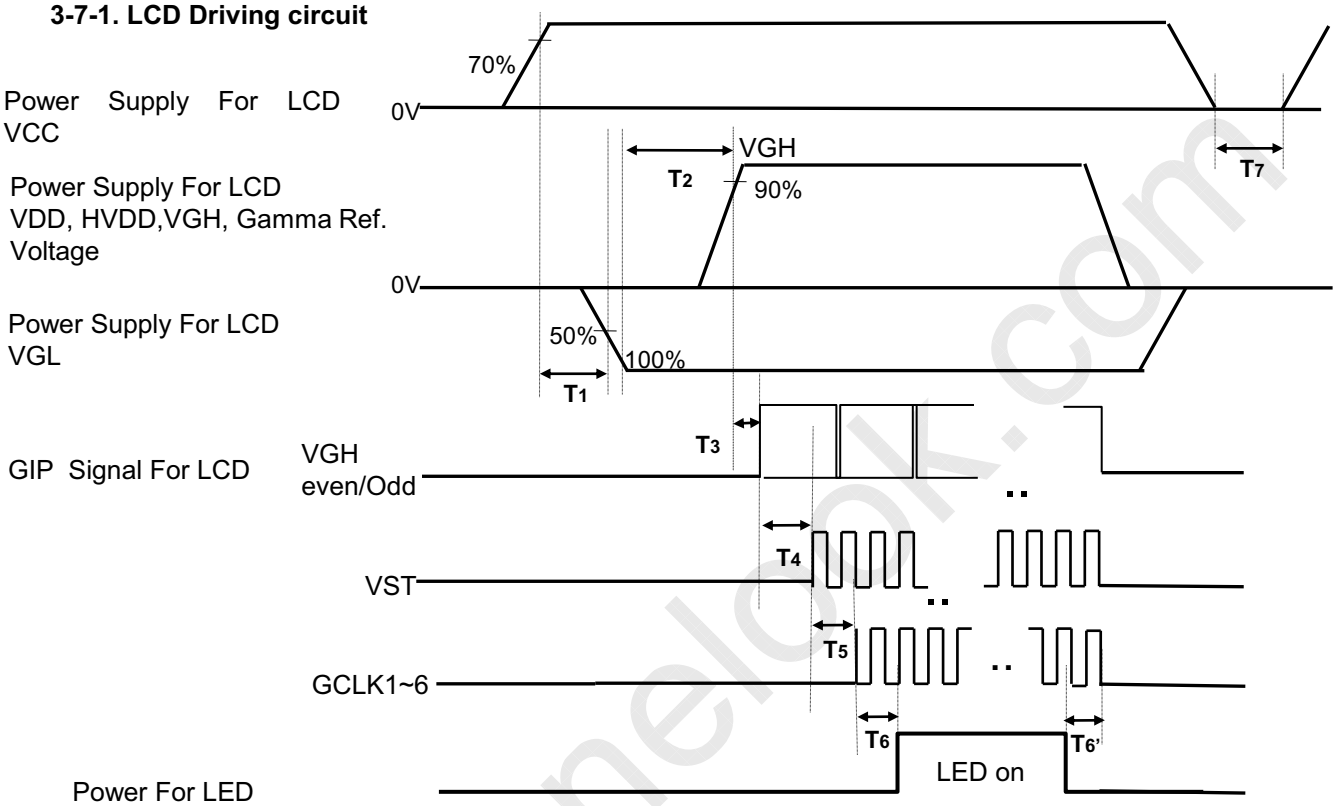


Table 9. POWER SEQUENCE Ta= 25±2°C, fv=60Hz

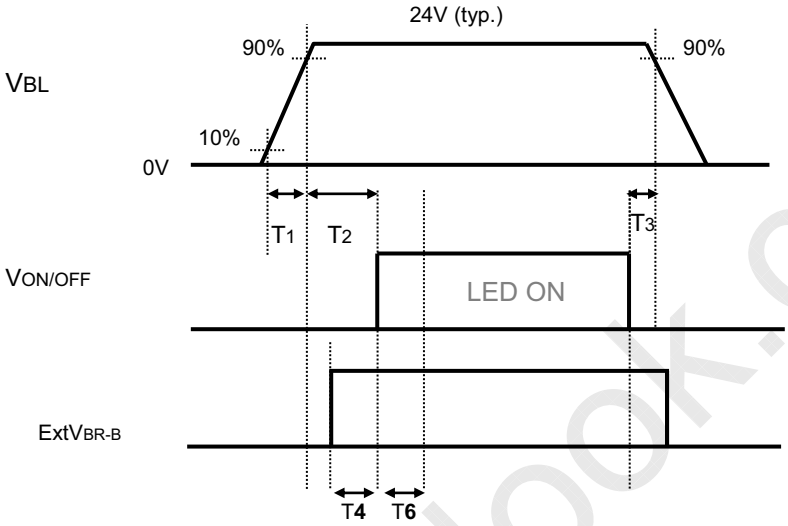
Parameter	Value			Unit	Notes
	Min	Typ	Max		
T1	0.5		-	ms	
T2	0.5		-	ms	
T3	0		-	ms	
T4	10		-	ms	2
T5	0		-	ms	
T6 / T6'	20		-	ms	6
T7	2		-	sec	

Note : 1. Power sequence for Source D-IC must follow the Case1 & 2.
※ Please refer to Appendix Ⅲ for more details.
2. VGH Odd signal should be started "High" status and VGH even & odd can not be "High at the same time.
3. Power Off Sequence order is reverse of Power On Condition including Source D-IC.
4. GCLK On/Off Sequence
Normal : GCLK4 → GCLK5 → GCLK6 → GCLK1 → GCLK2 → GCLK3.
Reverse :GCLK3 → GCLK2 →GCLK1 → GCLK6 → GCLK5 → GCLK4.
5. VDD_odd/even transition time should be within V_blank
6. In case of T6', If there is no abnormal display, no problem

3-7. Power Sequence

3-7-2. Sequence for LED Driver

Power Supply For LED Driver



3-6-3. Dip condition for LED Driver

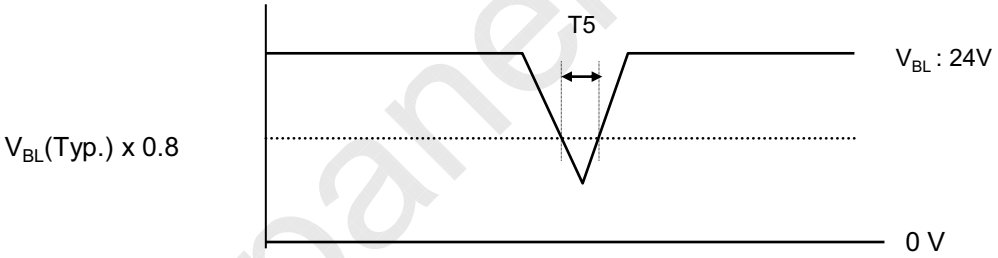


Table 9. Power Sequence for LED Driver

Parameter	Values			Units	Remarks
	Min	Typ	Max		
T1	20	-	-	ms	1
T2	500	-	-	ms	
T3	10	-	-	ms	
T4	0	-	-	ms	
T5	-	-	10	ms	V_{BL}(Typ) x **0.8**
T6	500	-	-	ms	2

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.
Even though T1 is over the specified value, there is no problem if I^2T spec of fuse is satisfied.
2. In T6 section, ExtVBR-B should be sustained from 5% to 100% .

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at 25±2°C. The values are specified at distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °. FIG. 1 shows additional information concerning the measurement equipment and method.

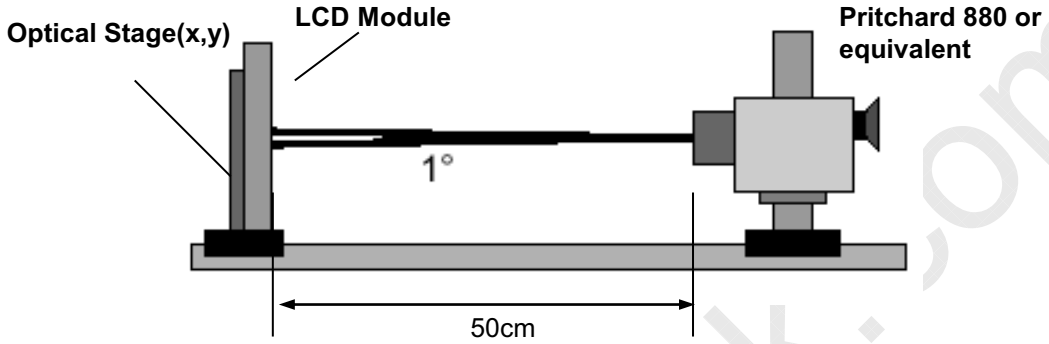


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= 25±2°C, VDD,H_VDD,VGH,VGL=typ, fv=120Hz, Clk=297MHz , **EXTV**BR-B =100% Back Light : LGD B/L

Parameter		Symbol	Value			Unit	Note
			Min	Typ	Max		
Contrast Ratio		CR	1000	1400	-		1
Surface Luminance, white		L_{WH}	290	360		cd/m^2	2
Luminance Variation		δ_{WHITE} 5P	-	-	1.3		3
Response Time	Variation	Rising		8	12	ms	5
	Gray to Gray(BW)	Falling		10	14	ms	4
Color Coordinates [CIE1931]	RED	Rx	Typ -0.03	0.649	Typ +0.03		
		Ry		0.333			
	GREEN	Gx		0.308			
		Gy		0.607			
	BLUE	Bx		0.145			
		By		0.064			
	WHITE	Wx		0.279			
		Wy		0.292			
Color Temperature				10,000		K	
Color Gamut				68		%	
Viewing Angle (CR>10)							
	x axis, right(ϕ=0°)	θr	89	-	-	degree	6
	x axis, left (ϕ=180°)	θl	89	-	-		
	y axis, up (ϕ=90°)	θu	89	-	-		
	y axis, down (ϕ=270°)	θd	89	-	-		
Gray Scale				-			7

Notes : 1. Contrast Ratio (CR) is defined mathematically as :

$$CR = \frac{\text{Surface Luminance at all white pixels}}{\text{Surface Luminance at all black pixels}}$$

It is measured at center 1-point.

2. Surface luminance is determined after the unit has been 'ON' and 1Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.

3. The variation in surface luminance , δ WHITE is defined as :

δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5})

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .
For more information, see the FIG. 2.

4. Response time is the time required for the display to transit from any gray to white (Rise Time, Tr_R) and from any gray to black (Decay time, Tr_D). For additional information see the FIG. 3.
※ G to G_{BW} Spec stands for average value of all measured points.
Photo Detector : RD-80S / Field : 2 °

5. G to G $_\sigma$ is Variation of Gray to Gray response time composing a picture

$$G \text{ to } G\ (\sigma) = \sqrt{\frac{\Sigma(Xi - u)^2}{N}}$$

Xi = Individual Data
u = Data average
N : The number of Data

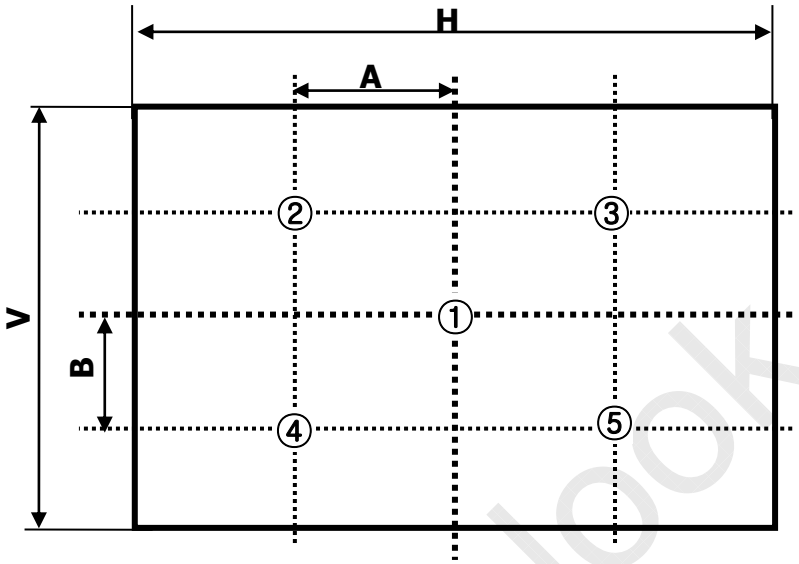
6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.

7. Gray scale specification
Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
L0	TBD
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

Measuring point for surface luminance & measuring point for luminance variation.



A : H / 4 mm
B : V / 4 mm
@ H,V : Active Area

FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

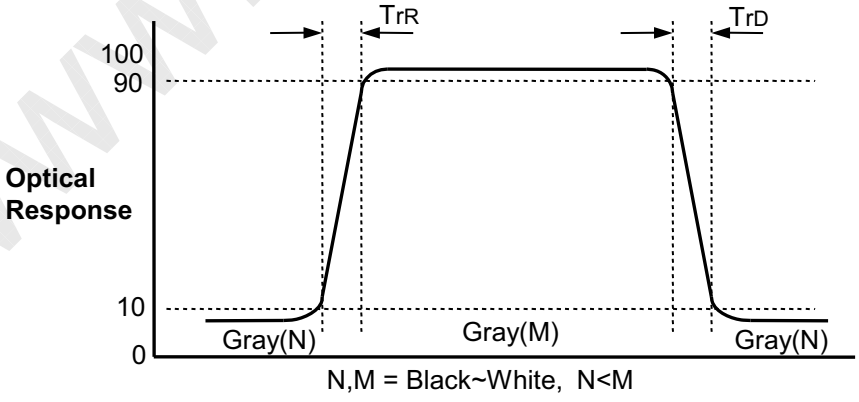


FIG. 3 Response Time

Dimension of viewing angle range

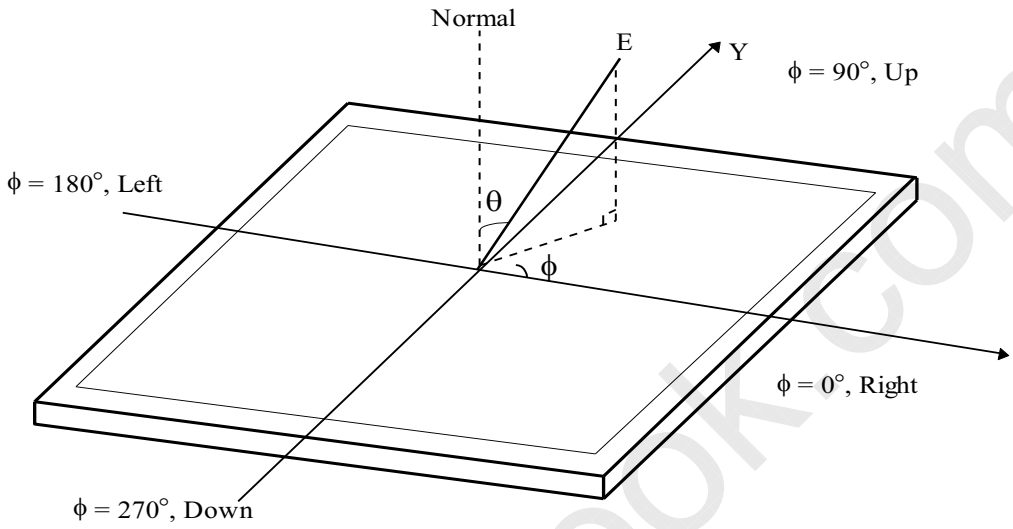


FIG. 4 Viewing Angle

FIG. 4 Viewing Angle

5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

Item	Value	
Outline Dimension (Only Glass)	Horizontal	1244.6 mm
	Vertical	720.9 mm
	Thickness	17.2 mm
Active Display Area	Horizontal	1209.6 mm
	Vertical	680.4 mm
Weight	15.51Kg (Typ)16.29Kg(Max)	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer(10%)	

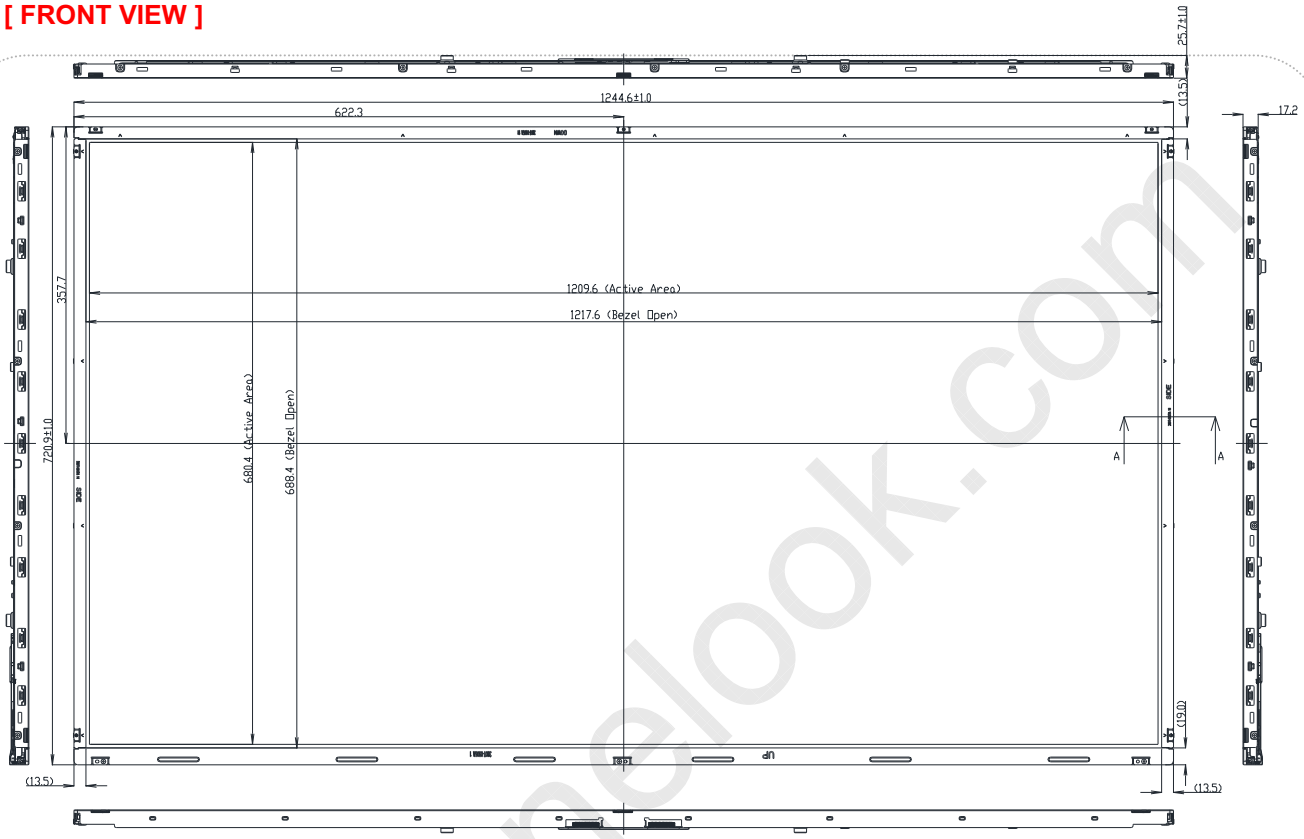
Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

机构图面：各Model Update

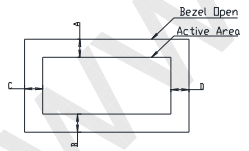
LC550EUA

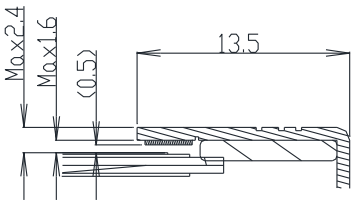
Product Specification

[FRONT VIEW]



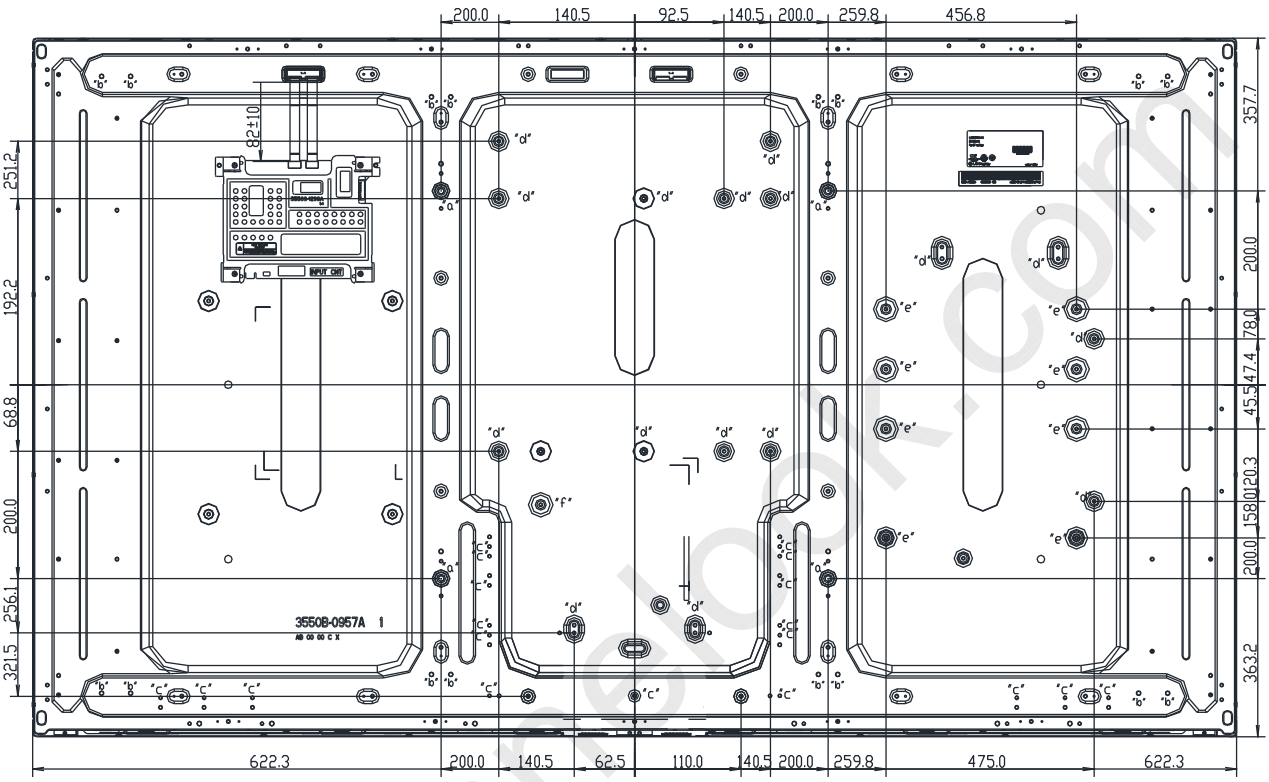
NOTES
1.UNSPECIFIED TOLERANCE IS ±1.0MM
2.TILT AND PARTIAL DISPOSITION TOLERANCE OF DISPLAY AREA AS FOLLOWING.
(1) Y-DIRECTION: | A-B | ≤1.5
(2) X-DIRECTION: | C-D | ≤1.5
3.M3.0 TAP: Max Torque 8kgf.cm
M4.0 TAP: Max Torque 10kgf.cm





SECTION A-A
SCALE 2/1

[REAR VIEW]



ITEM	TAP	Max Depth (mm)	Torque (Kgf.cm)	Notes
"a"	M6	12.0	MAX 12.0	
"b"	M4	8.0	MAX 10.0	
"c"	M3	8.0	MAX 10.0	
"d"	M3	4.0	MAX 8.0	
"e"	M3	6.4	MAX 8.0	
"f"	M3	6.0	MAX 8..0	
"g"				
"h"				
"i"				
"j"				
"k"				
"l"				
"m"				
"n"				
"o"				

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Humidity condition Operation	Ta= 40 °C ,90%RH

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. LED Array - Safty

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class 1M)

2. Caution
: LED inside.
Class 1M laser (LEDs) radiation when open.
Do not open while operating.

7-2. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

8. Precautions

Please pay attention to the followings when you use this TFT LCD module.

8-1. Mounting Precautions

(1) You must mount a module using specified mounting holes (Details refer to the drawings).
(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

8-2. Operating Precautions

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V=±200mV(Over and under shoot voltage)
(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
(4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
(7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
(8) A screw which is fastened up the steels should be a machine screw.
(if not, it can causes conductive particles and deal LCM a fatal blow)
(9) Please do not set LCD on its edge.
(10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

8-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

8-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

8-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.
(3) Storage condition is guaranteed under packing conditions.
(4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition

8-6. Handling Precautions for Protection Film

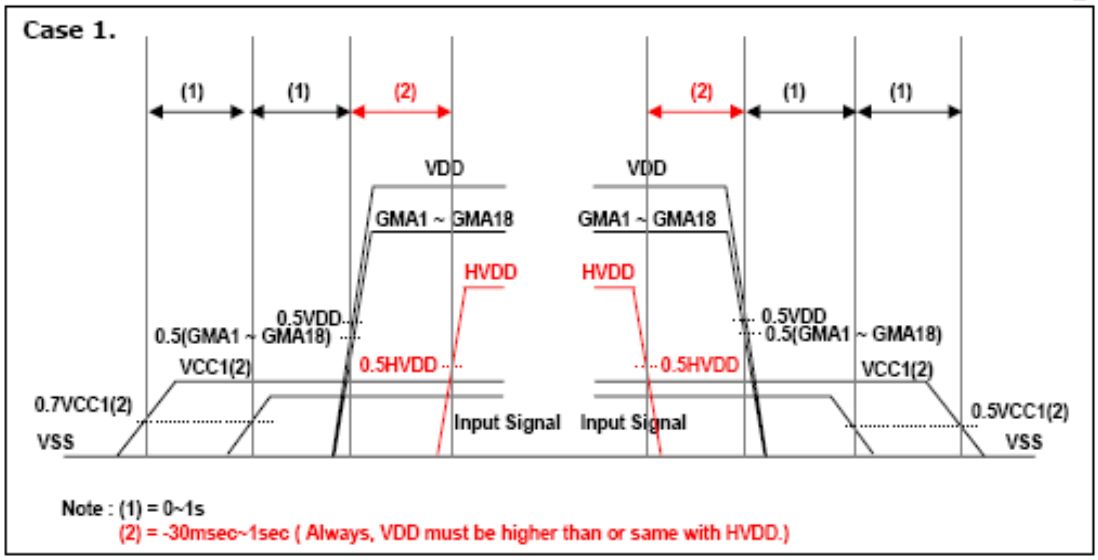
(1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
(3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

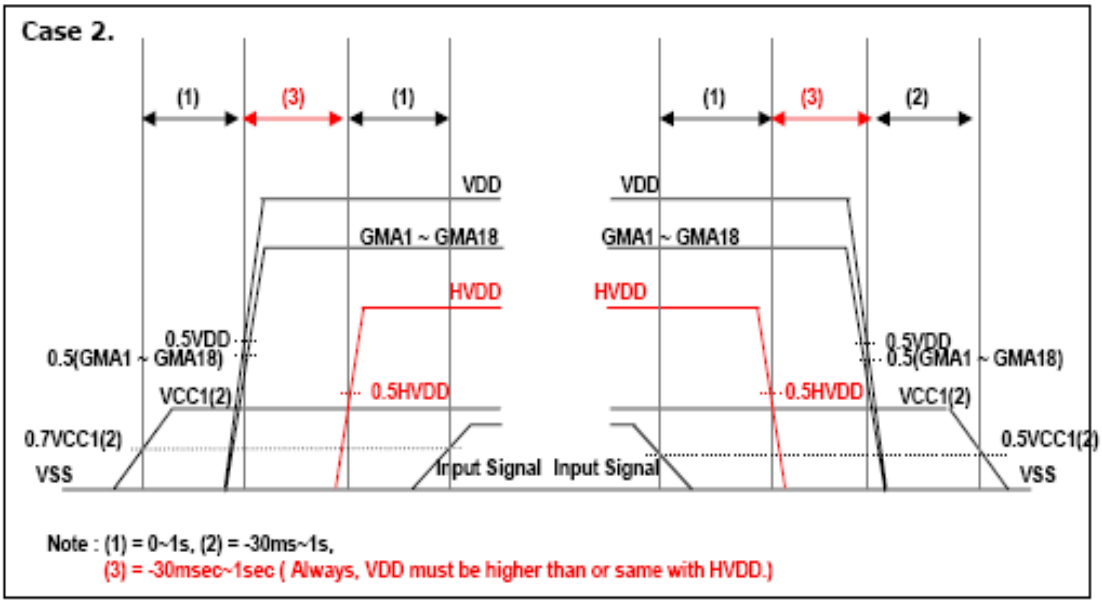
 # APPENDIX- II-1

■ LCM Label

APPENDIX- IV

■ Source D-IC Power Sequence





- Input Signal : SOE,POL,GSP,H_CONV,OPT_N, mini-LVDS