

LCM ENGINEERING SPECIFICATION

*MODEL	LC550EUE
SUFFIX	PEF1
Update	Aug. 08, 2012

- () Preliminary Specification
- (•) Final Specification



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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
1.0	Aug. 08. 2012	-	Preliminary Specification (First Draft)

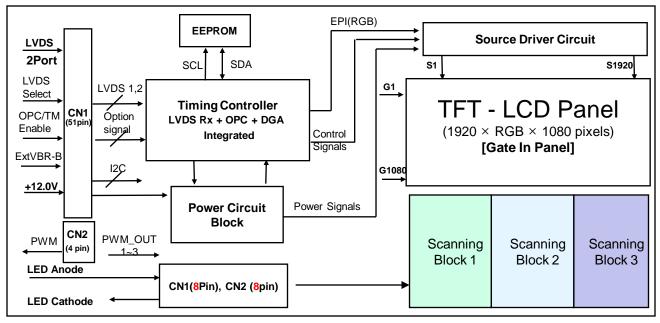


1. General Description

The LC550EUE is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 54.64 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.06Bilion colors.

It has been designed to apply the 10-bit 4-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	54.64 inches(1387.80mm)diagonal
Outline Dimension	1229.4(H) X 711.8(V) X 9.6(B) X 20.7
Pixel Pitch	0.630mm x 0.630 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10bit(D), 1.06Billon colors
Luminance, White	360 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 107.7W (Typ.) (Logic=9.4W with T-CON, LED Backlight =98.3W)(TBD)
Weight	15.4 Kg (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(2H), Anti-glare treatment of the front polarizer (Haze < 1%)



2. Absolute Maximum Ratings

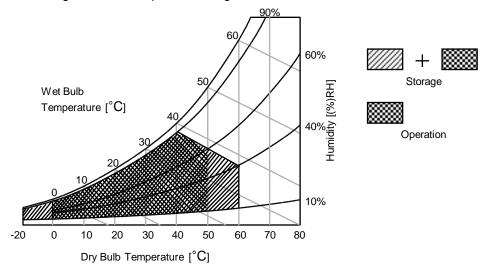
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Para	Parameter		Value		Unit	Note
r arai	Symbol	Min	Max	Onic	Note	
Power Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
LED Input Voltage	Forward Voltage	VF	-	+73.9	Vdc	1
T-Con Option Selection	Voltage	VLOGIC	-0.3	+4.0	Vdc	
Operating Temperature		Тор	0	+50	°C	2.2
Storage Temperature	Storage Temperature		-20	+65	°C	2,3
Panel Front Temperature		TSUR	-	+68	°C	4
Operating Ambient Hum	Нор	10	90	%RH	0.0	
Storage Humidity		Hs⊤	10	90	%RH	2,3

Note 1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.





3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

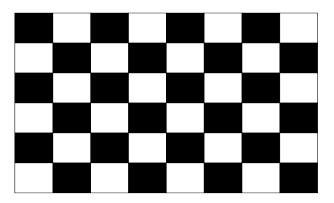
Parameter	Symbol		Value	Unit	Note	
Falameter	Symbol	Min	Тур	Max		Note
Circuit :						
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC	
Power Input Current	ILCD	-	784	1019	mA	1
Power Input Current	ILCD	-	TBD	TBD	mA	2
Power Consumption	PLCD		9.4	12.23	Watt	1
Rush current	IRUSH	-	-	TBD	А	3
	ExtV _{BR-B}	5	-	100	%	On Duty
Brightness Adjust for Back Light	LALV BR-B	1	-	100	%	4
	ExtV _{BR-B} Frequency	40	50/60	80	Hz	
Pulse Duty Level	High Level	2.5	-	3.6	Vdc	HIGH : on duty
(PWM)	Low Level	0	-	0.8	Vdc	LOW : off duty

Note 1. The specified current and power consumption are under the V_{LCD}=12.0V, Ta=25 \pm 2°C, f_V=60Hz condition, and mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

4. ExtV_{BR-B} signal have to input available duty range and sequence. After Driver ON signal is applied, $ExtV_{BR-B}$ should be sustained from 5% to 100% more than 500ms. After that, $ExtV_{BR-B}$ 1% and 100% is possible For more information, please see 3-6-2. Sequence for LED Driver.

5. Ripple voltage level is recommended under \pm 5% of typical voltage



White: 1023 Gray Black: 0 Gray

Mosaic Pattern(8 x 6)



Table 3. ELECTRICAL CHARACTERISTICS (Continue)

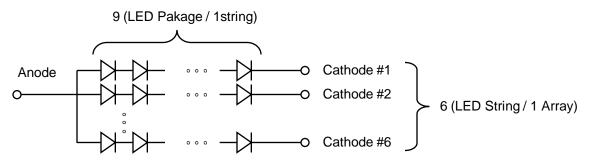
Parameter		Symbol		Values	Unit	Note	
		Gynnbol	Min	Тур	Max	Onic	Note
Backlight Assemb	ly:						
Forward Current	Anode	I _{F (anode)}		630		mAdc	±5%
(one array)	Cathode	I _{F (cathode)}	99.75	105	110.25	mAdc	2, 3
Forward Voltage		V _F	70.8	78.0	84.9	Vdc	4
Forward Voltage Va	ariation	$ riangle V_F$			1.7	Vdc	5
Power Consumptio	n	P _{BL}		98.3	107.4	W	6
Burst Dimming Duty	/	On duty	1		100	%	
Burst Dimming Frequency		1/T	95		182	Hz	8
LED Array : (APPE	LED Array : (APPENDIX-V)						
Life Time			30,000	50,000		Hrs	7

NotesThe design of the LED driver must have specifications for the LED array in LCD Assembly.

The electrical characteristics of LED driver are based on Constant Current driving type.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed. When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD– Assembly should be operated in the same condition as installed in your instrument.

- 1. Electrical characteristics are based on LED Array specification.
- 2. Specified values are defined for a Backlight Assembly. (IBL :2 LED array/LCM)
- 3. Each LED array has one anode terminal and six cathode terminals. The forward current(I_F) of the anode terminal is 630mA and it supplies 105mA into six strings, respectively



- 4. The forward voltage(V_F) of LED array depends on ambient temperature (Appendix-V)
- 5. ΔV_F means Max V_F -Min V_F in one Backlight. So V_F variation in a Backlight isn't over Max. 1.7V
- 6. Maximum level of power consumption is measured at initial turn on. Typical level of power consumption is measured after 1hrs aging at $25 \pm 2^{\circ}$ C.
- 7. The life time(MTTF) is determined as the time at which brightness of the LED is 50% compared to that of
- initial value at the typical LED current on condition of continuous operating at 25 ± 2°C, based on duty 100%.
 8. The reference method of burst dimming duty ratio.

It is recommended to use synchronous V-sync frequency to prevent waterfall (Vsync * 2 =Burst Frequency)

Though PWMfrequency is over 182Hz (max252Hz), function of backlight is not affected.



3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-R51S-HF(manufactured by JAE) or compatible
- Mating Connector : FI-R51HL(JAE) or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	PCID_EN	'H' : PCID Enable, 'L' or NC: PCID Disable (3D Mode Only)	27	Bit Select	'H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	SDA	SDA (For Local Dimming)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	SCL	SCL (For Local Dimming)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection (Note 4)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection (Note 4)	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground (Note 6)
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

- 1. All GND (ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. #2~#3,#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)
- 5. LVDS pin (pin No. **#24,25,40,41**) are used for 10Bit(D) of the LCD module. If used for 8Bit(R), these pins are no connection.
- 6. Specific pin No. **#44** is used for "No signal detection" of system signal interface. It should be GND for NSB (No Signal Black) while the system interface signal is not. If this pin is "H", LCD Module displays AGP (Auto Generation Pattern).
- 7. Specific pin (pin No. #4, #5) is used for Controlling Local Dimming register in the LCM Module

Note



3-2-2. Backlight Module

[CN201]

- 1) LED Array assy Connector (Plug)
 - : HS100-L08N-N62 (black color, manufactured by UJU)
- 2) Mating Connector (Receptacle)
 - : IS100-L08T-C46 (black color, manufactured by UJU)

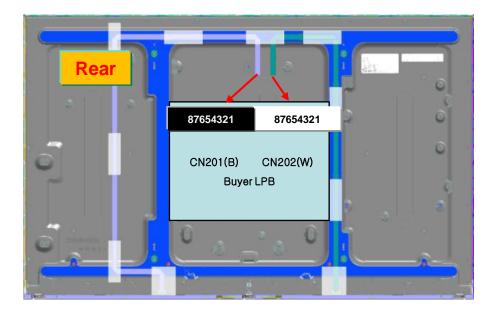
[CN202]

- 1) LED Array assy Connector (Plug)
 - : HS100-L08N-N62-A (natural color, manufactured by UJU)
- 2) Mating Connector (Receptacle)
- : IS100-L08T-C46-A (natural color, manufactured by UJU)

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN201,CN202)

No	Symbol(CN201)	Description	Note
1	L1 Cathode	LED Output Current	
2	L2 Cathode	LED Output Current	
3	L3 Cathode	LED Output Current	
4	N.C	Open	
5	N.C	Open	
6	N.C	Open	
7	N.C	Open	
8	Anode_L	LED Input Current for L1~L6	

No	Symbol(CN202)	Description	Note
1	Anode_R	LED Input Current for R1~R6	
2	N.C	Open	
3	N.C	Open	
4	N.C	Open	
5	N.C	Open	
6	R3 Cathode	LED Output Current	
7	R2 Cathode	LED Output Current	
8	R1 Cathode	LED Output Current	





3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

ITE	м	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tн∨	960	960	960	tCLK	1920 / 2
Horizontal	Blank	tнв	100	140	240	tCLK	1
	Total	tHP	1060	1100	1200	tCLK	
	Display Period	t∨∨	1080	1080	1080	Lines	
Vertical	Blank	t∨B	20 (228)	45 (270)	86 (300)	Lines	1
	Total	tvp	1100 (1308)	1125 (1350)	1149 (1380)	Lines	

Table 6. TIMING TABLE (DE Only Mode)

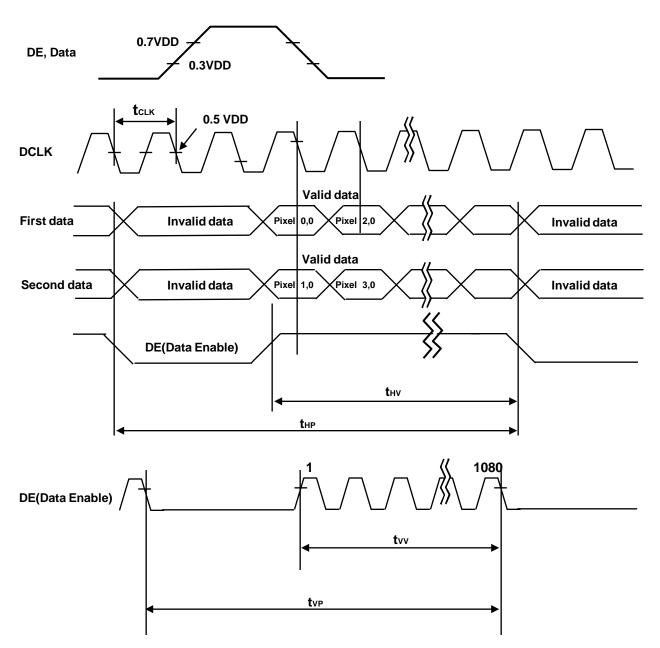
ITE	м	Symbol	Min	Тур	Max	Unit	Note
	DCLK	fclk	63	74.25	78.00	MHz	
	Horizontal	fн	57.3	67.5	70	KHz	2
Frequency	Vertical	f∨	57 (47)	60 (50)	63 (53)	Hz	2 NTSC (PAL)

- Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.
 - 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
 - % Timing should be set based on clock frequency.



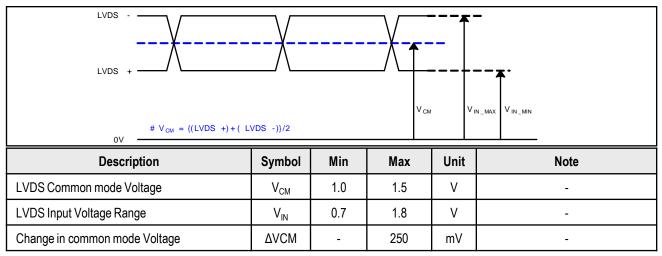
3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram

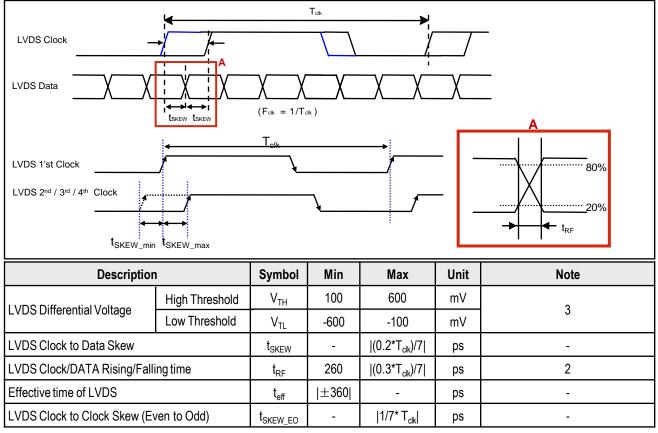


3-4-2. LVDS Input Signal Characteristics

1) DC Specification

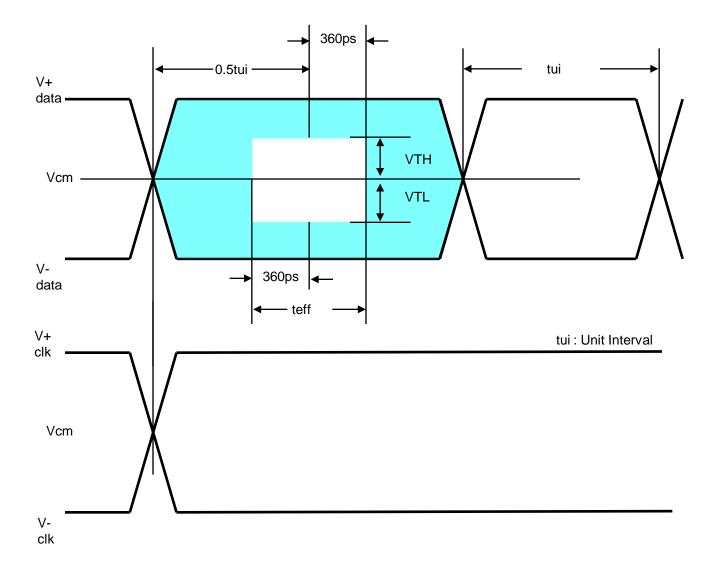


2) AC Specification



Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range.
- 3. LVDS Differential Voltage is defined within $\mathbf{t}_{\rm eff}$





3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE	Table 7.	COLOR DATA REFERENCE
-------------------------------	----------	----------------------

											I	npu	t Co	lor [Data										
	Color				R	ED		_		_			GRI	EEN		_					BL	UE		_	_
		MS							SB									MS							SB
	i	R	7 R6	6 R5	6 R4	R3	R2 F	R1 R	0	G	7 G6	6 G 5	G4	G3	G2 (G1 (60	B	7 B6	6 B5	6 B4	B3	B2 E	31 E	30
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic	Red (1023)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (1022)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (1022)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3-6. Power Sequence

3-6-1. LCD Driving circuit

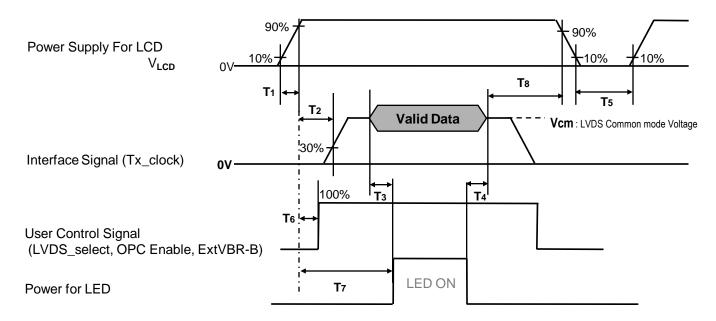


Table 8. POWER SEQUENCE

Devementer		11:4	Netes		
Parameter	Min	Тур	Unit	Notes	
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
Т3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	S	4
T6	-	-	T2	ms	5
T7	0.5	-	-	S	6
Т8	100	-	-	ms	7

Note :

1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.

- 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.

4. T5 should be measured after the Module has been fully discharged between power off and on period.

5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.

- 6. If there is no abnormal display, no problem.
- 7. It is recommendation specification that T8 has to be 100ms as a minimum value.
- ※ Please avoid floating state of interface signal at invalid period.

* When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values are specified at 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°. FIG. 1 shows additional information concerning the measurement equipment and method.

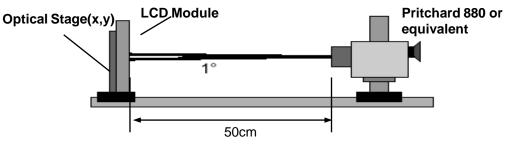


FIG. 1 Optical Characteristic Measurement Equipment and Method

Ta= $25\pm2^{\circ}$ C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz,

Table 10. OPTICAL CHARACTERISTICS

EXTV BR	-в =100	%

	Daman		0	uh al		Value		Unit	Nista
	Paran	neter	Syr	nbol	Min	Тур	Max		Note
Contrast	Ratio		(R	1000	1400	-		1
0	Surface Luminance, white			2D	290	360			
Surrace	Luminance, wh	lte	L _{WH}	3D	120	150		cd/m ²	2
Luminan	ce Variation		δ _{WHITE}	5P			1.3		3
Response Time		G to	ο G _σ	-	5	8		5	
Respons	se Time	Gray to Gray (BW)	G to G _{BW}		-	8	12	ms	4
		F	₹x		0.647				
RED		KED	F	₹y]	0.334			
Color Coordinates	GREEN	(Эx		0.308				
	OREEN		Gy	Тур	0.607	Тур			
[CIE1937	1]	BLUE	E	Зх	-0.03	0.150	+0.03		
			E	Зу		0.066			
		WHITE	Wx			0.279			
			V	Vy		0.292			
Color Ter	mperature					10,000		ĸ	
Color Ga	mut					68		%	
		right(φ=0°)	θr (x	axis)	89	-	-		
	2D	left (φ=180°)	өl (x	axis)	89	-	-	degree	6
Viewing	(CR>10)	up (ф=90°)	0 и (у	/ axis)	89	-	-	degree	6
Angle		down (ϕ =270°)	θd (у	/ axis)	89	-	-		
	3D (CT≤10%) up + down		θu (y axis) +θd (y axis)		14	20	-	degree	8
3D Cros	stalk		3D	C/T		1	3	%	
Gray Sc	ale				-	-	-		7



Note : 1. Contrast Ratio(CR) is defined mathematically as :

Surface Luminance with all white pixels

Contrast Ratio = Surface Luminance with all black pixels

It is measured at center 1-point.

- Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum(L_{on1},L_{on2},L_{on3},L_{on4},L_{on5}) / Minimum(L_{on1},L_{on2},L_{on3},L_{on4},L_{on5}) Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transit from any gray to white (Rise Time, Tr_R) and from any gray to black (Decay time, Tr_D). For additional information see the FIG. 3.

% G to G_{BW} Spec stands for average value of all measured points. Photo Detector : RD-80S / Field : 2 $^\circ$

5. G to G $_{\sigma}\,$ is Variation of Gray to Gray response time composing a picture

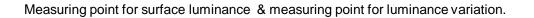
G to G (
$$\sigma$$
) = $\sqrt{\frac{\Sigma(Xi-u)^2}{N}}$ Xi = Individual Data
u = Data average
N : The number of Data

- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification
- Gamma Value is approximately 2.2. For more information, see the Table 11
- 8. 3D performance specification is expressed by 3D luminance and 3D viewing angle.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
LO	0.07
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100





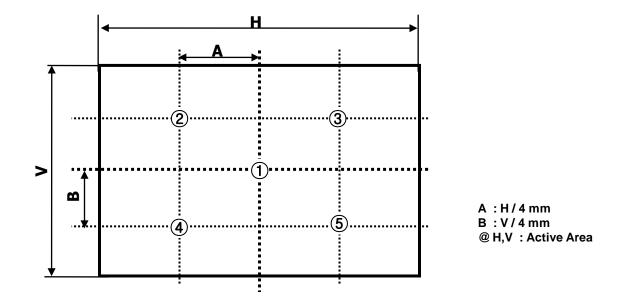


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".

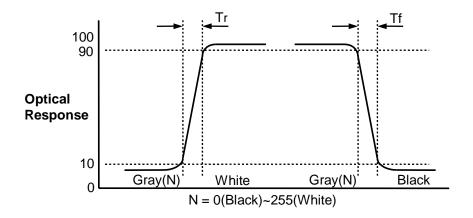
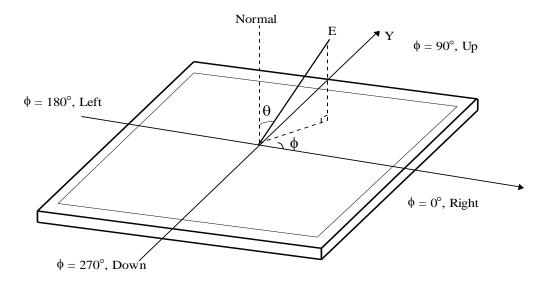


FIG. 3 Response Time

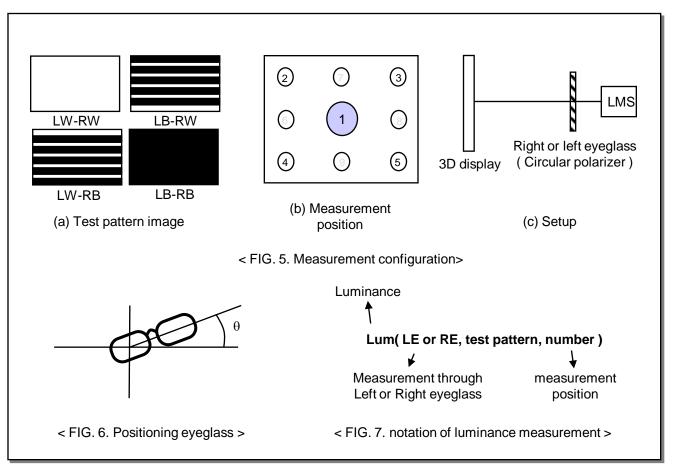


Dimension of viewing angle range









In order to measure 3D luminance, 3D crosstalk and 3D viewing angle, it need to be prepared as below;

1) Measurement configuration

4-Test pattern images. Refer to FIG 5.

- -. LW-RW: White for left and right eye
- -. LW-RB: White for left eye and Black for right eye
- -. LB-RW : Black for left eye and white for right eye
- -. LB-RB : Black for left eye and right eye

Image files where black and white lines are displayed on even or odd lines.

Luminance measurement system (LMS) with narrow FOV (field of view) is used. Refer to FIG 1.

2) Positioning Eyeglass (refer to appendix-VII for standard specification of eyeglass) Find angle of minimum transmittance.

This value would be provided beforehand or measured by the following steps;

- (i) Test image (LB-RW) is displayed.
- (ii) Left eyeglass are placed in front of LMS and luminance is measured,
- rotating right eyeglass such as FIG 6. The notation for luminance measurement is "Lum(LE, LB-RW,1)". (iii) Find the angle where luminance is minimum.
- * Following measurements should be performed at the angle of minimum transmittance of eyeglass.

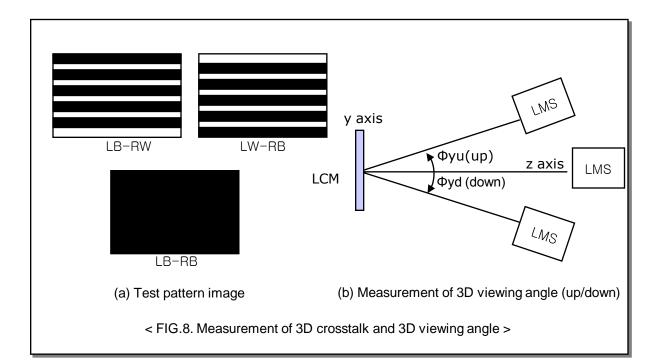


- 3) Measurement of 3D luminance
 - (i) Test image (LW-RW) is displayed.
 - (ii) Left or right eyeglass are placed in front of LMS successively and luminance is measured at center 1 point where the notation for luminance measurement is "Lum(LE, LW-RW,1)" or "Lum(RE, LW-RW,1).
- 4) Measurement of 3D crosstalk
 - (i) Test image (LB-RW, LW-RB and LB-RB) is displayed.
 - (ii) Right or left eyeglass are placed in front of LMS successively and luminance is measured for position 1.
 with rotating LMS or sample vertically.

or $\frac{Lum(LE, LB-RW, 1) - Lum(LE, LB-RB, 1)}{Lum(LE, LW-RB, 1) - Lum(LE, LB-RB, 1)}$ $\frac{Lum(RE, LW-RB, 1) - Lum(RE, LB-RB, 1)}{Lum(RE, LB-RW, 1) - Lum(RE, LB-RB, 1)}$

5) Measurement of 3D Viewing Angle

3D viewing angle is the angle at which the 3D crosstalk is under 10%. The angles are determined for the vertical or y axis with respect to the z axis which is normal to the LCD module surface and measured for position 1. For more information, see the Fig 8





5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

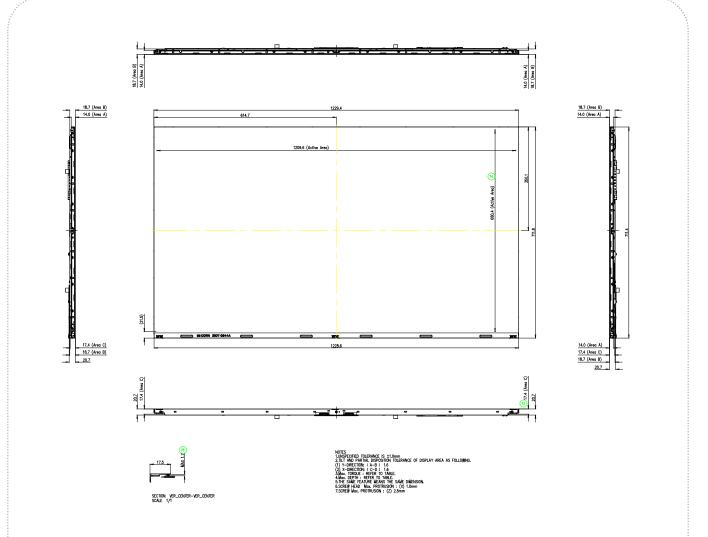
Table 12. MECHANICAL CHARACTERISTICS

Item	Value				
	Horizontal	1229.4 mm			
Outline Dimension	Vertical	711.8 mm			
	Depth	20.7 mm			
Deed Area	Horizontal	1229.4mm			
Bezel Area	Vertical	694.3 mm			
Active Display Area	Horizontal	1209.6 mm			
Active Display Area	Vertical	680.4 mm			
Weight	15.4 Kg (Typ.), 16.2kg (Max.)				

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

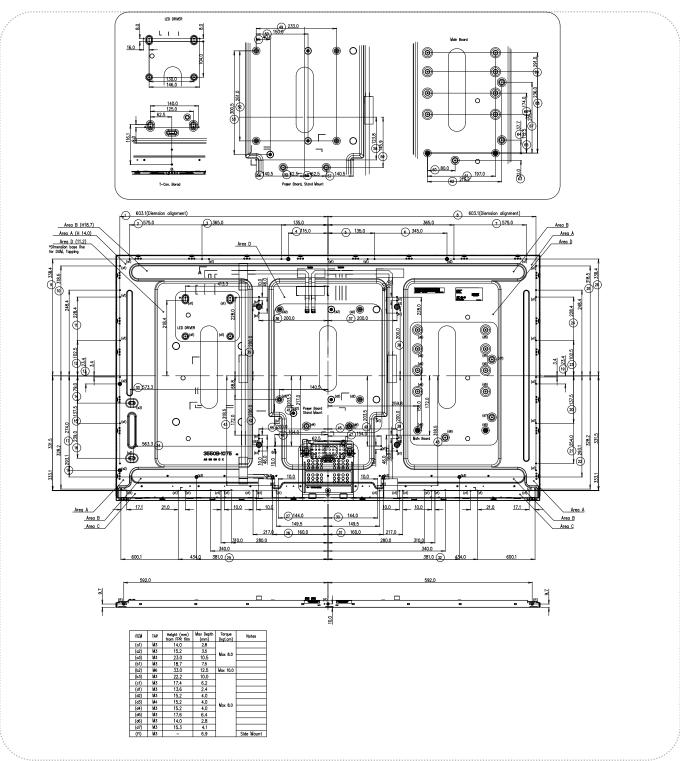


[FRONT VIEW]





[REAR VIEW]





6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition					
1	High temperature storage test	Ta= 60°C 240h					
2	Low temperature storage test	Ta=-20°C 240h					
3	High temperature operation test	Ta= 50°C 50%RH 240h					
4	Low temperature operation test	Ta=0°C 240h					
5	Humidity condition Operation	Ta= 40 °C ,90%RH					
6	Vibration test (non-operating)	No Guarantee					
7	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft					

Note 1 : Before and after Reliability test, LCM should be operated with normal function.



7. International Standards

7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1:2001 Embedded LED Power (Class 1M)

2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open. Do not open while operating.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003



8. Packing

8-1. Information of LCM Label

a) Lot Mark



A,B,C : SIZE(INCH) E : MONTH D : YEAR F ~ M : SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	А	В	С	D	E	F	G	Н	J	К

2. MONTH

Mo	onth	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
M	lark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one Pallet : 16 pcs
- b) Pallet Size : 1300 mm(W) X 1140 mm(D) X 848.5 mm(H)



9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the term of term of terms of the term of terms of term

module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer.
- Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for here hard or groupy sloth (Some competing and detrimental).

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (3) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (7) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (8) Please do not set LCD on its edge.



9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- (2) Storage condition is guaranteed under peaking conditions
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

9-6. Operating condition guide

- (1) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : 5 ~ 40 $^\circ C$
 - Display pattern : continually changing pattern (Not stationary)
- (2) If the product will be used in extreme conditions such as high temperature, display patterns or operation time etc..,

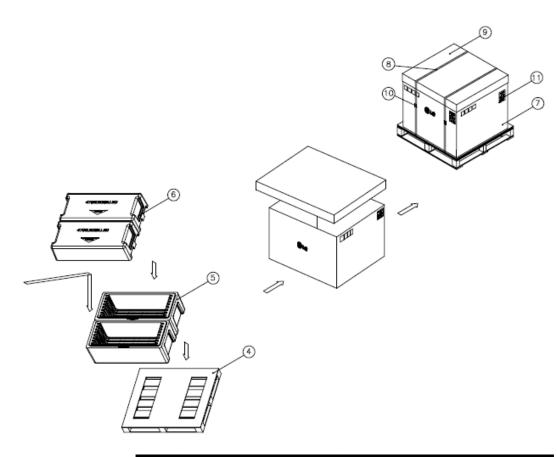
It is strongly recommended to contact LGD for Qualification engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems. The LCD product should be applied by global standard environment. (refer ETSI EN 300, IEC 60721)

APPENDIX-I

Pallet Ass'y

(3)

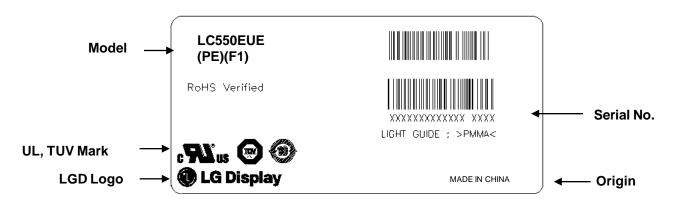
Î



NO.	DESCRIPTION	MATERIAL
1	LCD Module	55" LCD
2	BAG	55INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	Plywood 1440X1140X 950 mm
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE, PACKING	PAPER
8	BAND	PP
9	ANGLE,COVER	PAPER
10	BAND	STEEL OR PP
11	LABEL	YUPO 80G 100X70

APPENDIX- II-1

■ LCM Label



APPENDIX- II-2

Pallet Label

<	100.0	>	4				
	<u>C550</u>	EUE					
PEF1							
16 PCS	001/01-01		70.0				
MADE	IN CHINA	RoHS Verified					

APPENDIX- III-1

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "L" or "NC")

Host System	THC63LVD103] [
24 Bit	or Compatible		Timing	
RED0	33		Controller	
RED1	34	FI-RE51S		
RED2	35			
RED3	36	31 40		
RED4	37 TA-	12	100Ω ≷	RO0N
RED5		13		RO0P
RED6	59			
RED7		29 14	<u> </u>	RO1N
		28 15	100Ω ≷	RO1P
	5	10		
GREEN0	40	25		
GREEN1	41 TC-	16	100Ω ξ	RO2N
GREEN2	42 TC+	17	10022	RO2P
GREEN3	44			
GREEN4	45 TCLK-	23 19	<u> </u>	ROCLKN
GREEN5		22 20	<u>100Ω</u>	ROCLKP
GREEN6 GREEN7				
GREEN/		21 00		DOON
		20 22	100Ω ξ	RO3N
BLUE0		20 23	100322	RO3P
BLUE1	48			
BLUE2				
BLUE3	52			
BLUE4	53			
BLUE5	54	7		VESA/ JEIDA
BLUE6	64			
BLUE7	1]	
	9			
	11			
Hsync	55		LCM Module	
Vsync	57	GND		
Data Enable	58			
СГОСК —	12			

Note: 1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")

Host System	THC6	3LVD103				
24 Bit	or Co	ompatible				Timing
RED0	4					Controller
RED1	 5		FI-RE51S-HF			
RED2	59					
RED3	61		31			
RED4	33	TA-		12	1000 2	RO0N
RED5	34	TA+	30	13	100Ω ξ	ROOP
RED6	 35					
RED7	36	TB-	29	14		RO1N
	37		28		100Ω ≶	
	38	TB+		15	`	RO1P
GREEN0	6		25			
GREEN1	8	TC-		16		RO2N
GREEN2	 62	TC+	24	17	100Ω ξ	RO2P
GREEN3	63					
GREEN4	40	TCLK-	23	19		ROCLKN
GREEN5	41	TCLK+	22	20	<u>100Ω ξ</u>	ROCLKP
GREEN6	42	TOLKŦ		20		
GREEN7	44		21			
	45	TD-	20	22	4000	RO3N
	46	TD+	20	23	100Ω ξ	RO3P
BLUE0	9					
BLUE1	11					
BLUE2	64					
BLUE3	1					
BLUE4	48					
BLUE5	49 50			7		VESA/ JEIDA
BLUE6 BLUE7	50 52					
BLUE/	52				-	
	53			1		
Hsync	55					
Vsync	57		VCC		LCM Module	
Data Enable	58		č	L		
CLOCK	12					
			l			

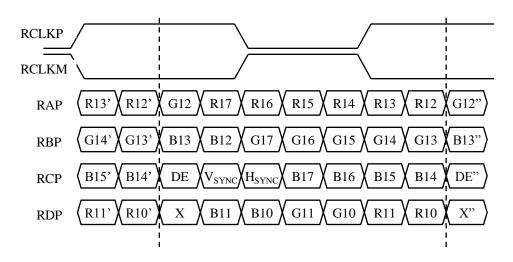
Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

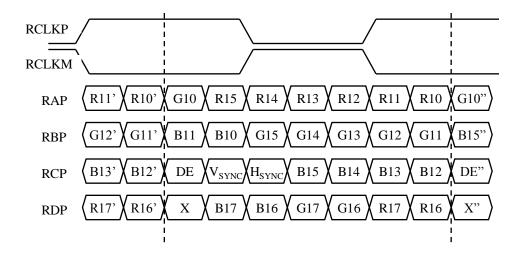
APPENDIX- IV

LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



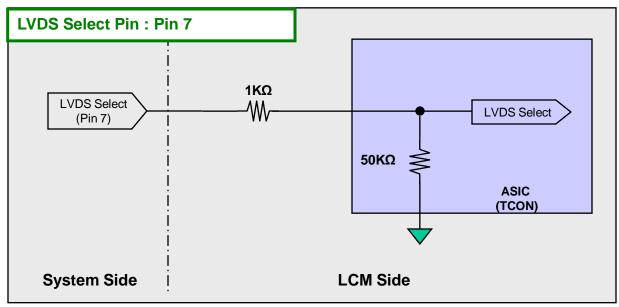
2) LVDS Select : "L" Data-Mapping (VESA format)



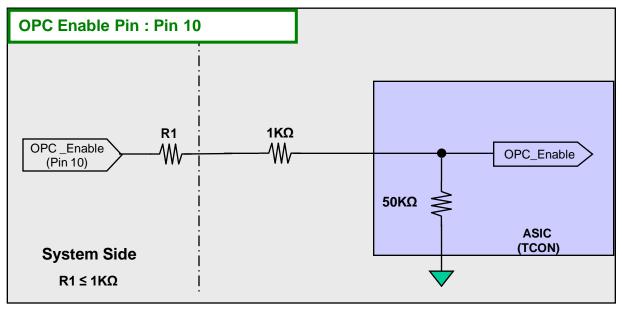
APPENDIX- V

Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of OPC Enable Selection pin



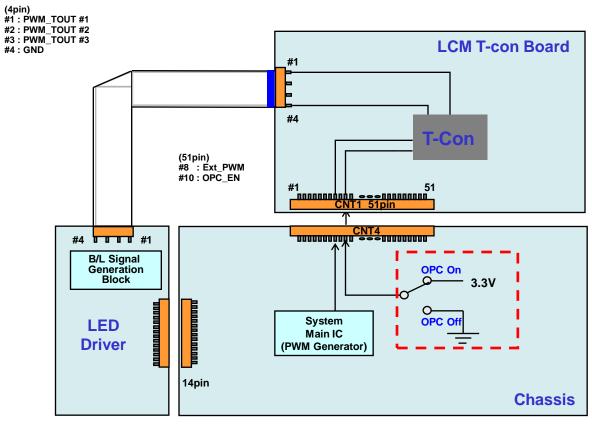


APPENDIX- VI

Scanning and OPC Design Guide

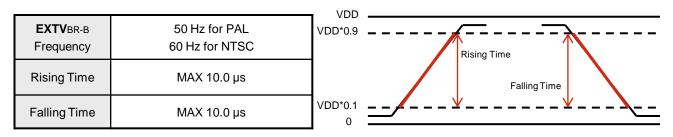
♦ When OPC Enable is "L", OPC Output = System Dimming.

OPC Output(PWM Signal) is synchronized with V-Sync Freq. of System in T-Con Board.





- \diamond PWM Specification (VDD = 3.3V) @ OPC
 - 1. PWM High Voltage Range : 2.5V~3.6V
 - 2. PWM Low Voltage Range : 0.0V~0.8V



APPENDIX- VII

Standard specification of Eyeglasses

This is recommended data of Eyeglasses for LC550EUN-SEF1 model. (details refer to table)

For each item, depending on the eyeglass manufacturer tolerances may occur, this tolerance can affect 3D performance. (3D Crosstalk, 3D luminance, 3D viewing angle)

De	Left	Right	Remark		
Optical axis	a) Slow axis of retarder	-45°	45°	Refer to drawing	
	b) Transmission axis of polarizer	0°	0°		
Retardation value	Retarder 125nm		@550nm		

<Table. Standard specification of Eyeglasses>

※Recommended polarizer

Polarization efficiency: more than 99.90%

